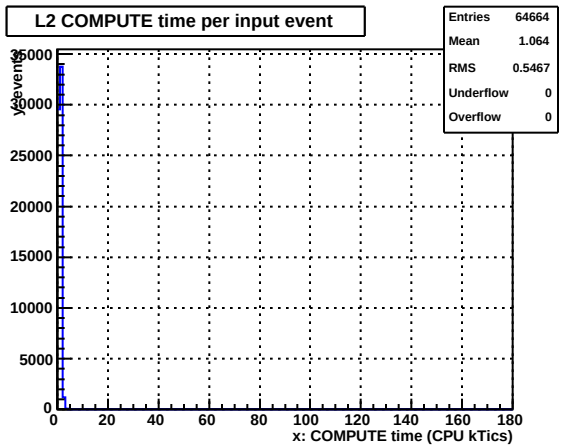
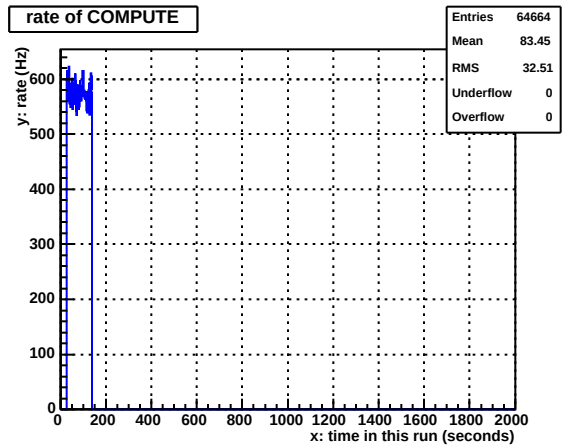
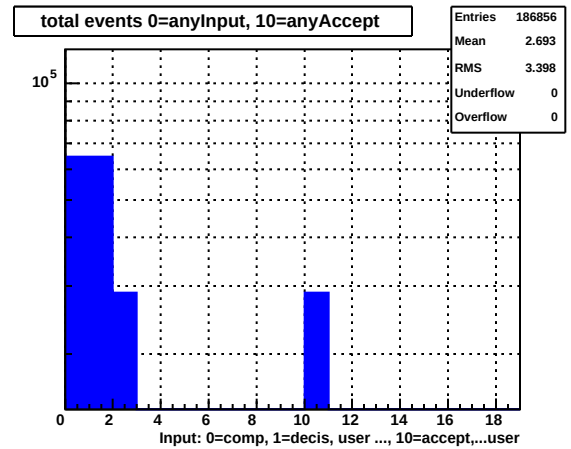
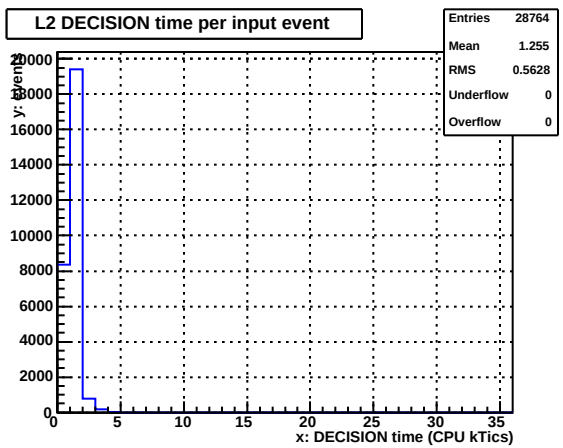
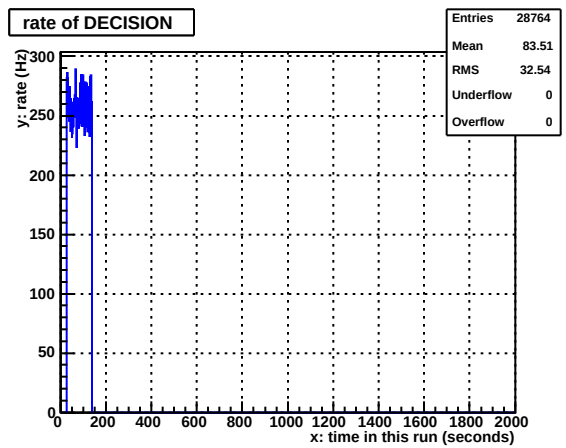
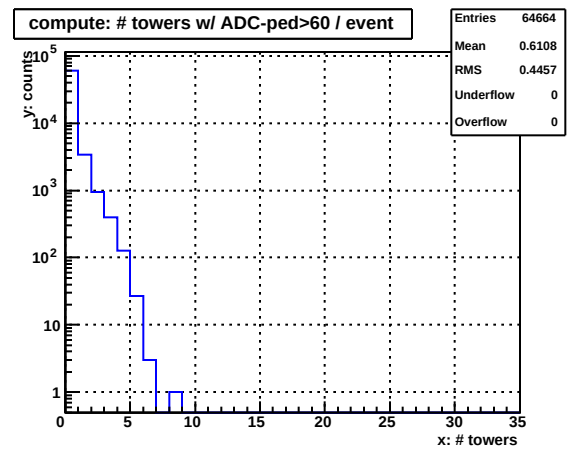

L2 COMPUTE time per input event
Entries 64664
Mean 1.064
RMS 0.5467
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

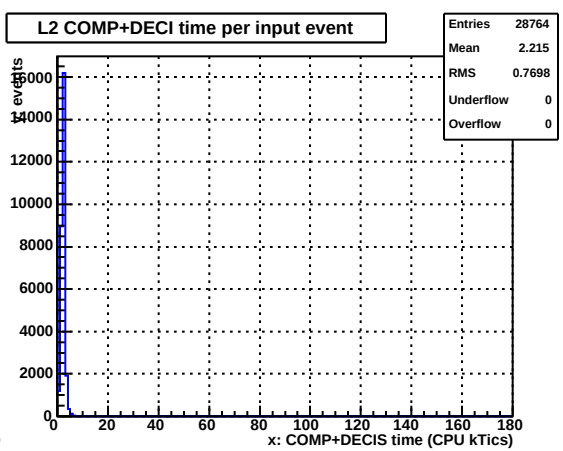

rate of COMPUTE
Entries 64664
Mean 83.45
RMS 32.51
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

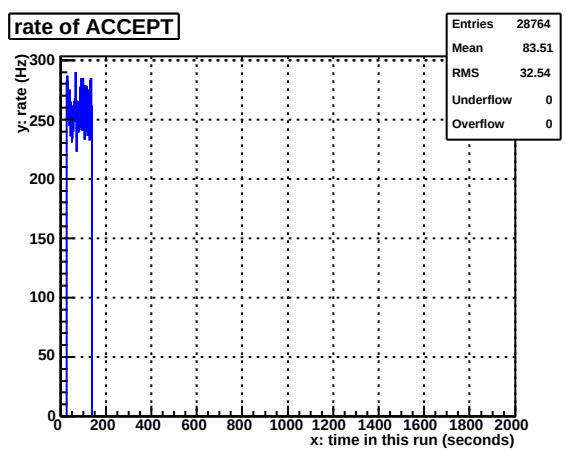

total events 0=anyInput, 10=anyAccept
Entries 186856
Mean 2.693
RMS 3.398
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

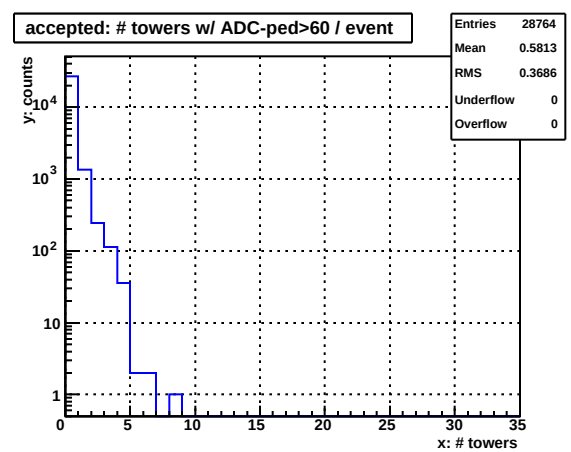

L2 DECISION time per input event
Entries 28764
Mean 1.255
RMS 0.5628
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

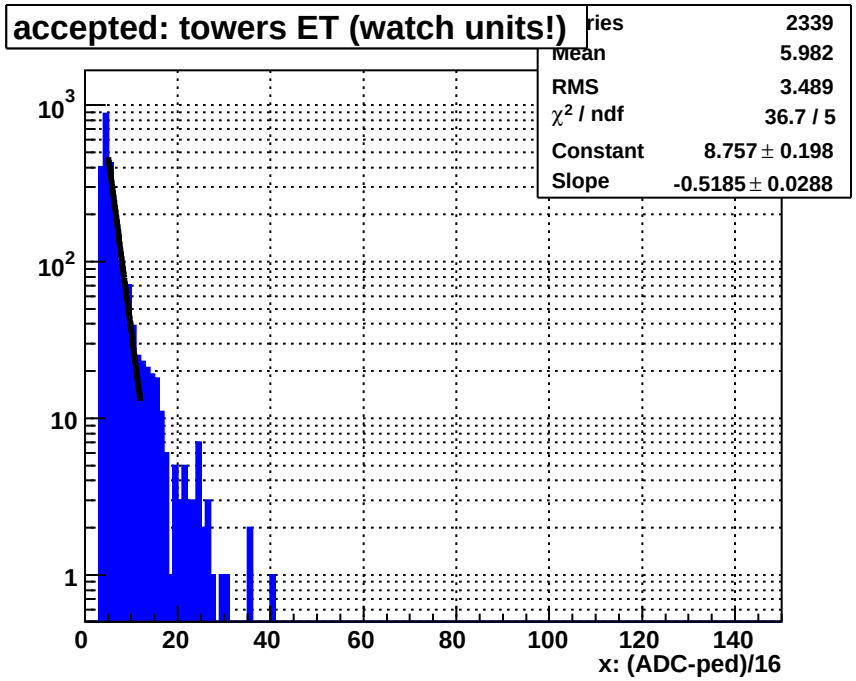

rate of DECISION
Entries 28764
Mean 83.51
RMS 32.54
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

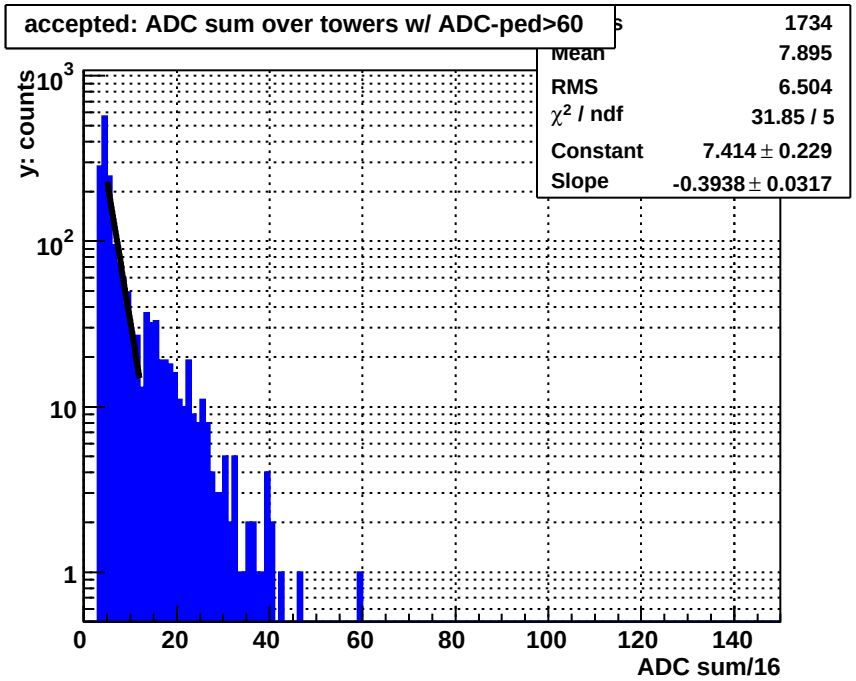

compute: # towers w/ ADC-ped>60 / event
Entries 64664
Mean 0.6108
RMS 0.4457
Underflow 0
Overflow 0
y: counts
x: # towers

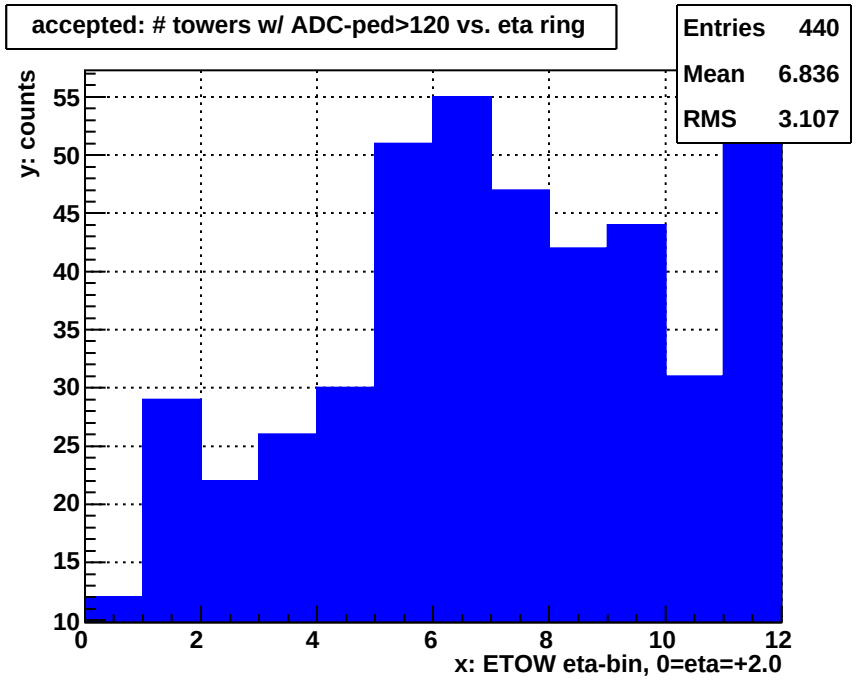

L2 COMP+DECI time per input event
Entries 28764
Mean 2.215
RMS 0.7698
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

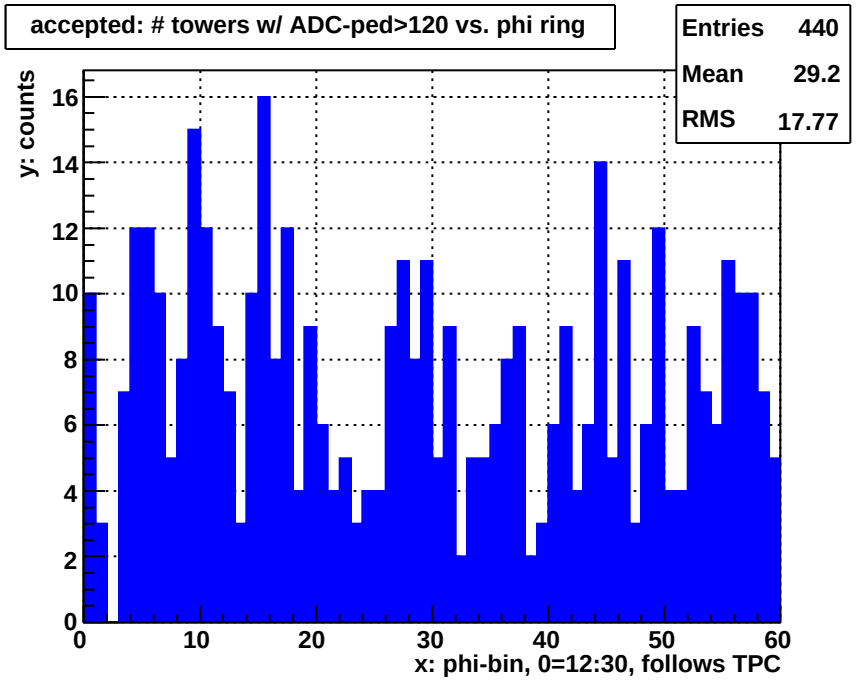

rate of ACCEPT
Entries 28764
Mean 83.51
RMS 32.54
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 28764
Mean 0.5813
RMS 0.3686
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 2339
Mean 5.982
RMS 3.489
χ² / ndf 36.7 / 5
Constant 8.757 ± 0.198
Slope -0.5185 ± 0.0288
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 1734
Mean 7.895
RMS 6.504
χ² / ndf 31.85 / 5
Constant 7.414 ± 0.229
Slope -0.3938 ± 0.0317
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 440
Mean 6.836
RMS 3.107
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 440
Mean 29.2
RMS 17.77
y: counts
x: phi-bin, 0=12:30, follows TPC