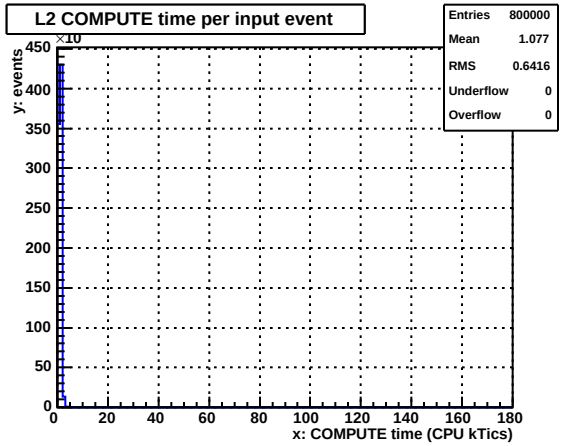
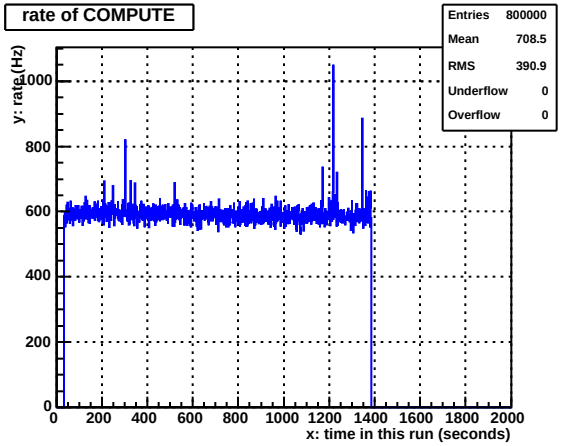
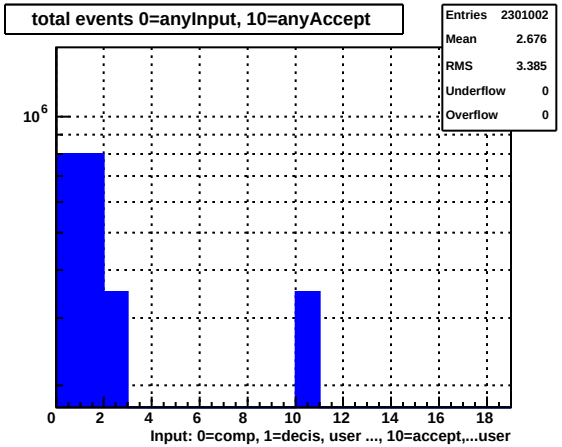
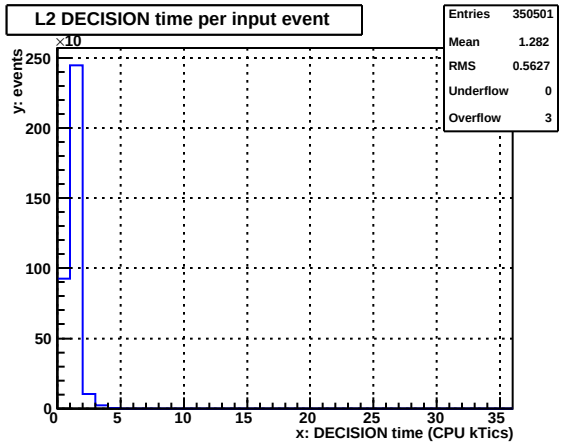
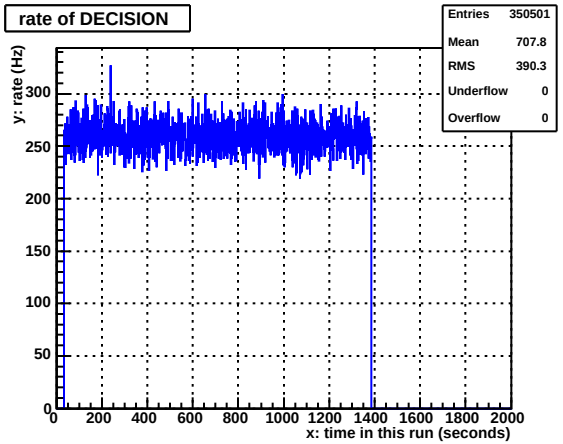
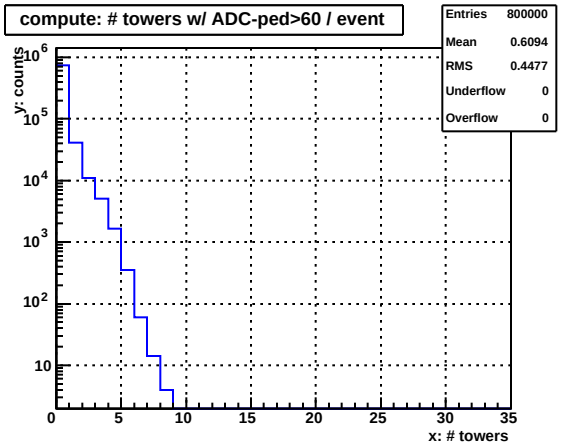

L2 COMPUTE time per input event
Entries 800000
Mean 1.077
RMS 0.6416
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

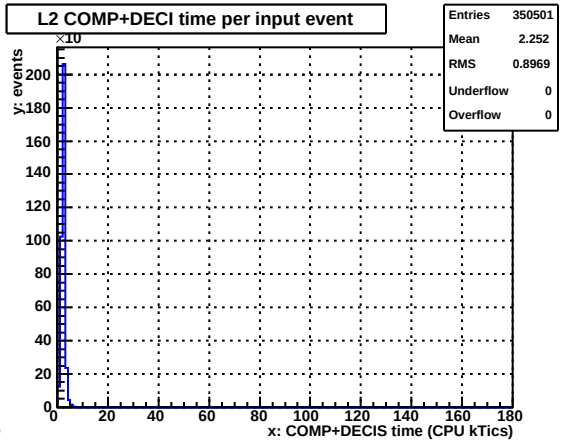

rate of COMPUTE
Entries 800000
Mean 708.5
RMS 390.9
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

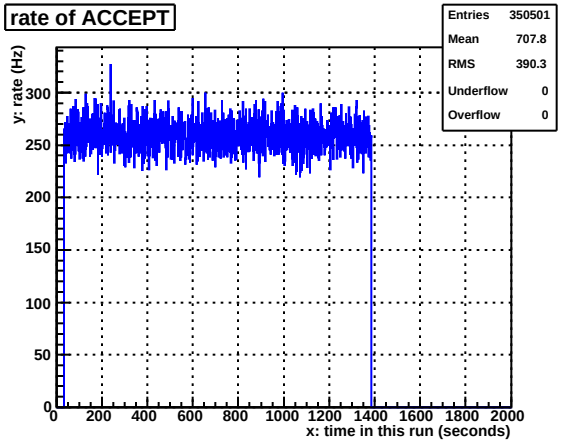

total events 0=anyInput, 10=anyAccept
Entries 2301002
Mean 2.676
RMS 3.385
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

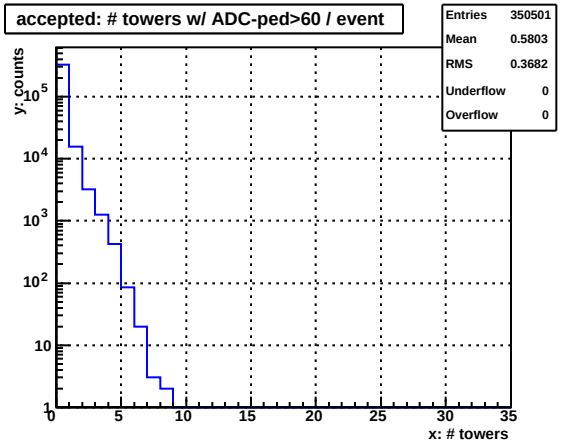

L2 DECISION time per input event
Entries 350501
Mean 1.282
RMS 0.5627
Underflow 0
Overflow 3
y: events
x: DECISION time (CPU kTics)

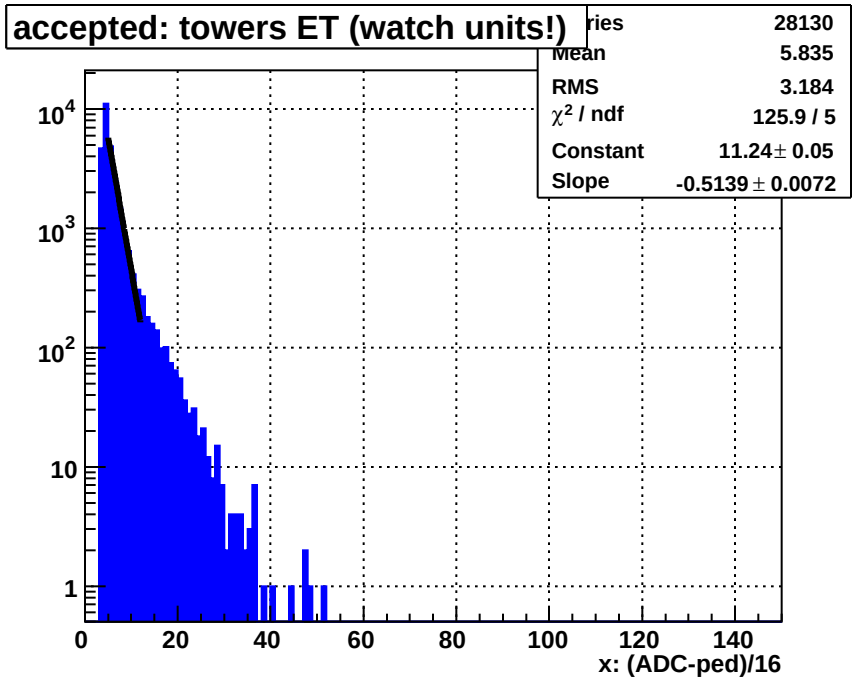

rate of DECISION
Entries 350501
Mean 707.8
RMS 390.3
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

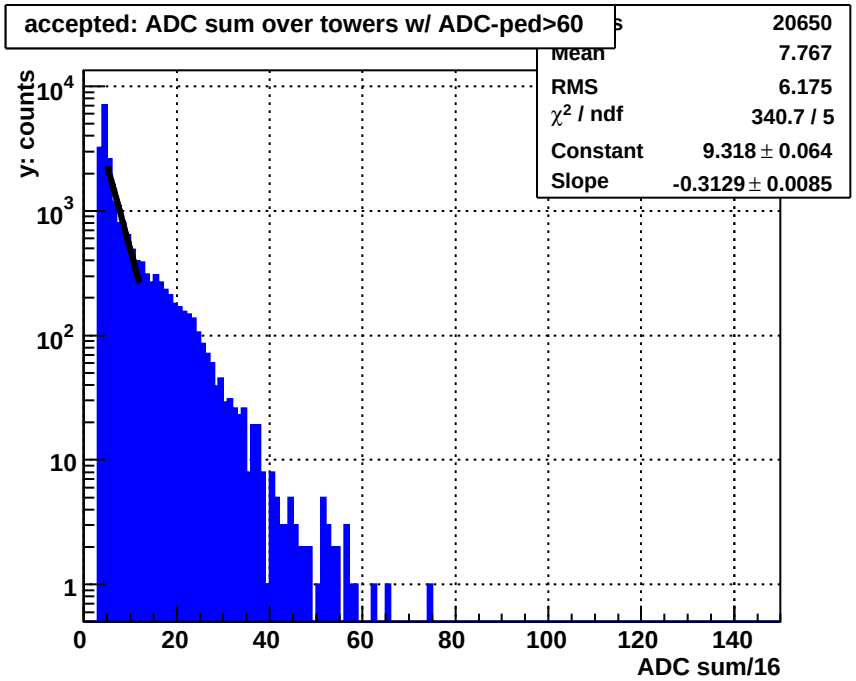

compute: # towers w/ ADC-ped>60 / event
Entries 800000
Mean 0.6094
RMS 0.4477
Underflow 0
Overflow 0
y: counts
x: # towers

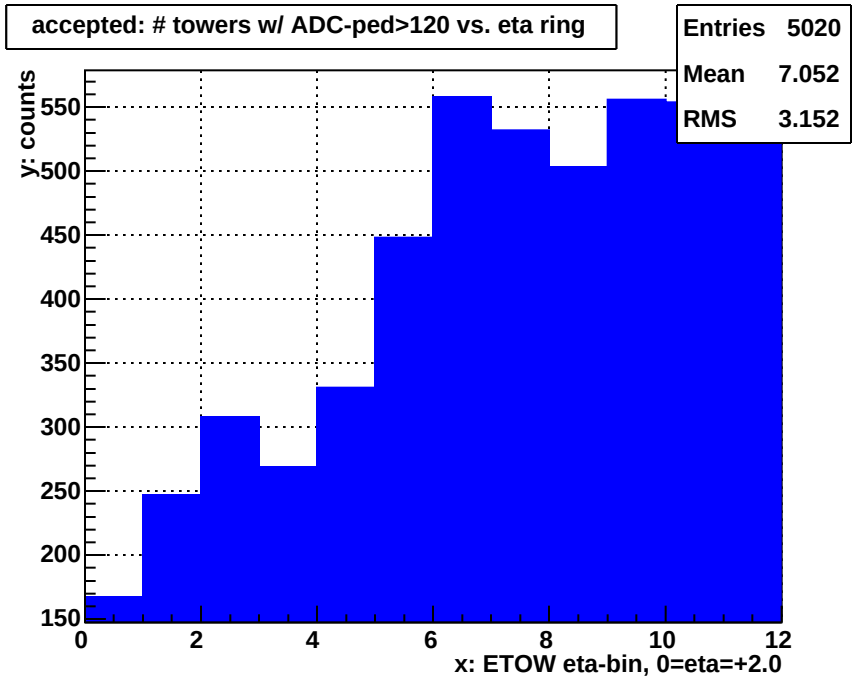

L2 COMP+DECI time per input event
Entries 350501
Mean 2.252
RMS 0.8969
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

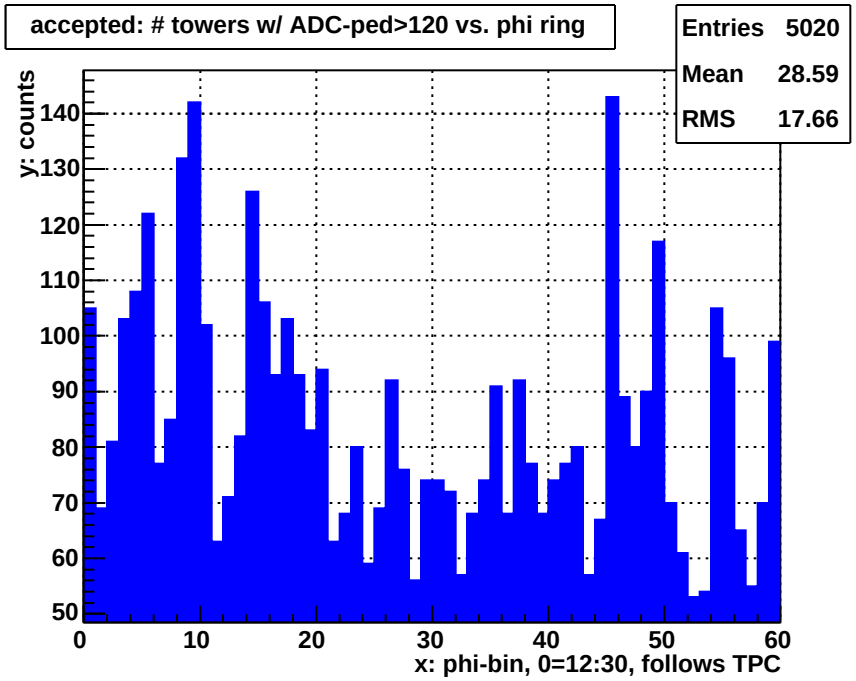

rate of ACCEPT
Entries 350501
Mean 707.8
RMS 390.3
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 350501
Mean 0.5803
RMS 0.3682
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 28130
Mean 5.835
RMS 3.184
χ² / ndf 125.9 / 5
Constant 11.24 ± 0.05
Slope -0.5139 ± 0.0072
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 20650
Mean 7.767
RMS 6.175
χ² / ndf 340.7 / 5
Constant 9.318 ± 0.064
Slope -0.3129 ± 0.0085
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 5020
Mean 7.052
RMS 3.152
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 5020
Mean 28.59
RMS 17.66
y: counts
x: phi-bin, 0=12:30, follows TPC