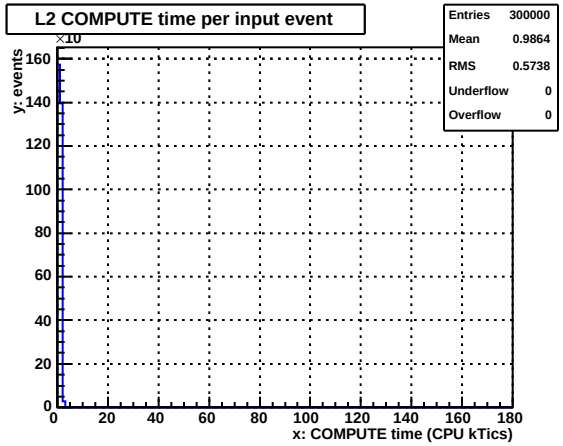
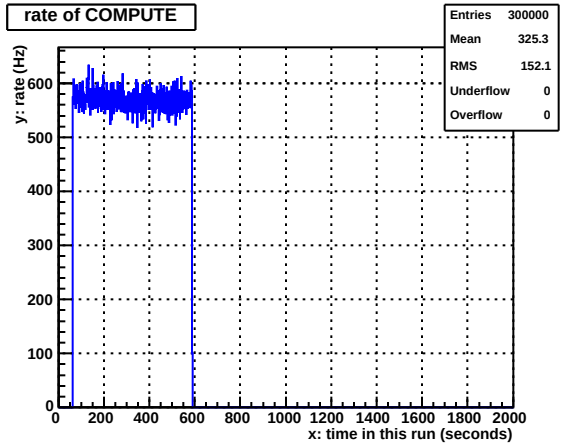
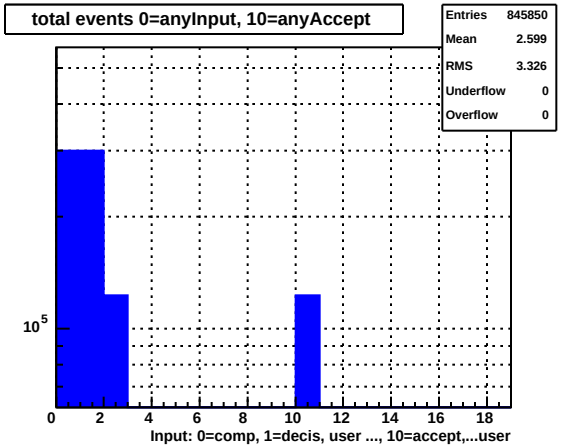
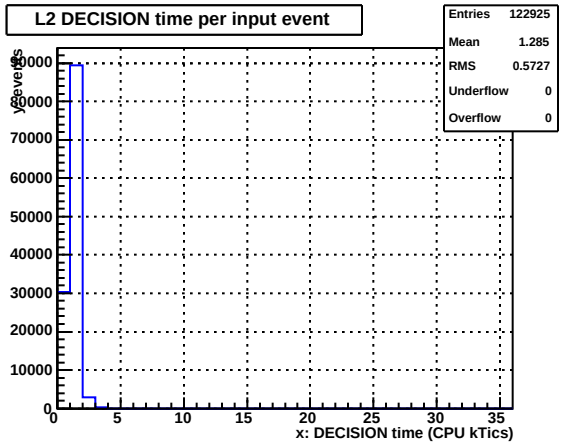
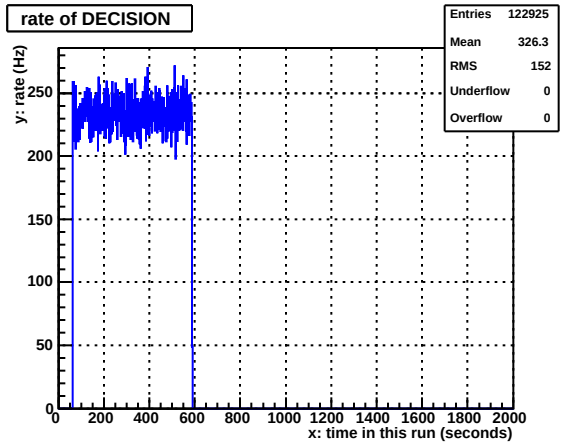
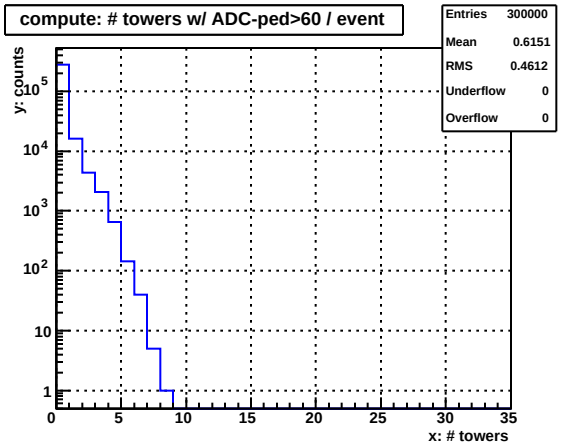

L2 COMPUTE time per input event
Entries 300000
Mean 0.9864
RMS 0.5738
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

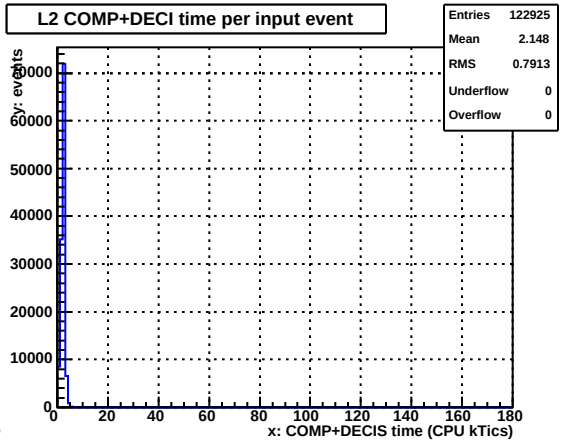

rate of COMPUTE
Entries 300000
Mean 325.3
RMS 152.1
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

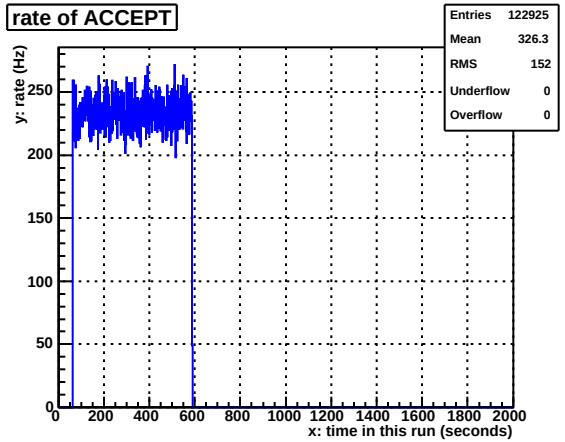

total events 0=anyInput, 10=anyAccept
Entries 845850
Mean 2.599
RMS 3.326
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

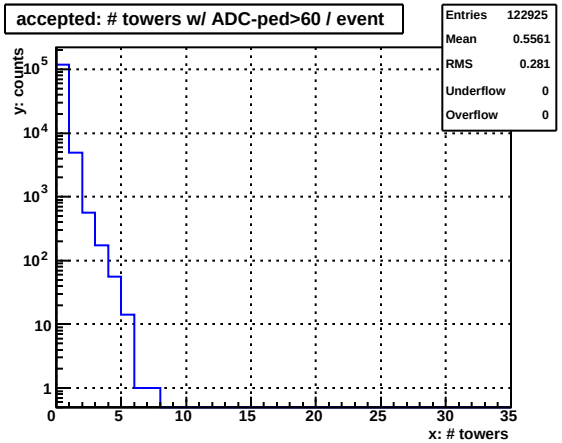

L2 DECISION time per input event
Entries 122925
Mean 1.285
RMS 0.5727
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

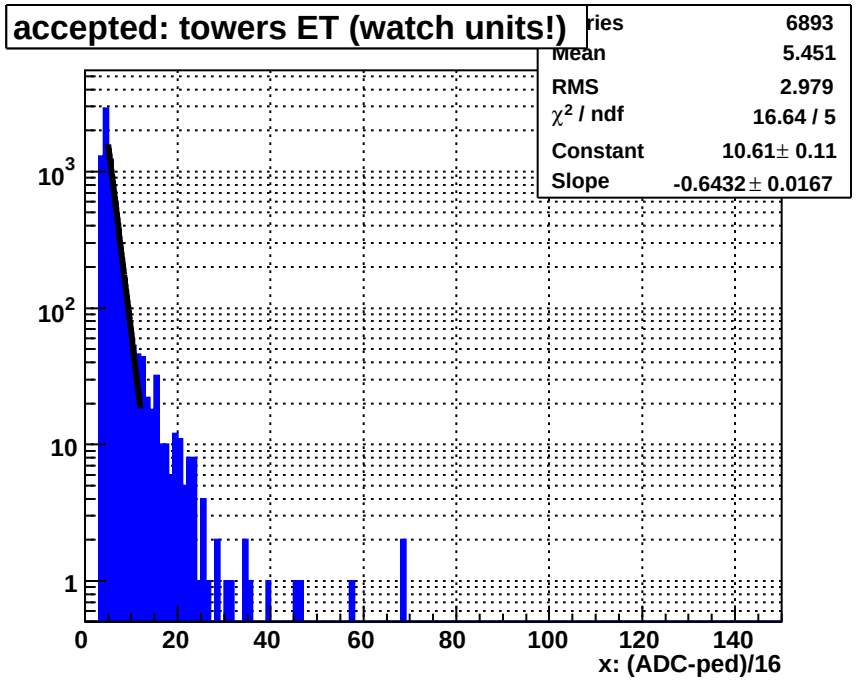

rate of DECISION
Entries 122925
Mean 326.3
RMS 152
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

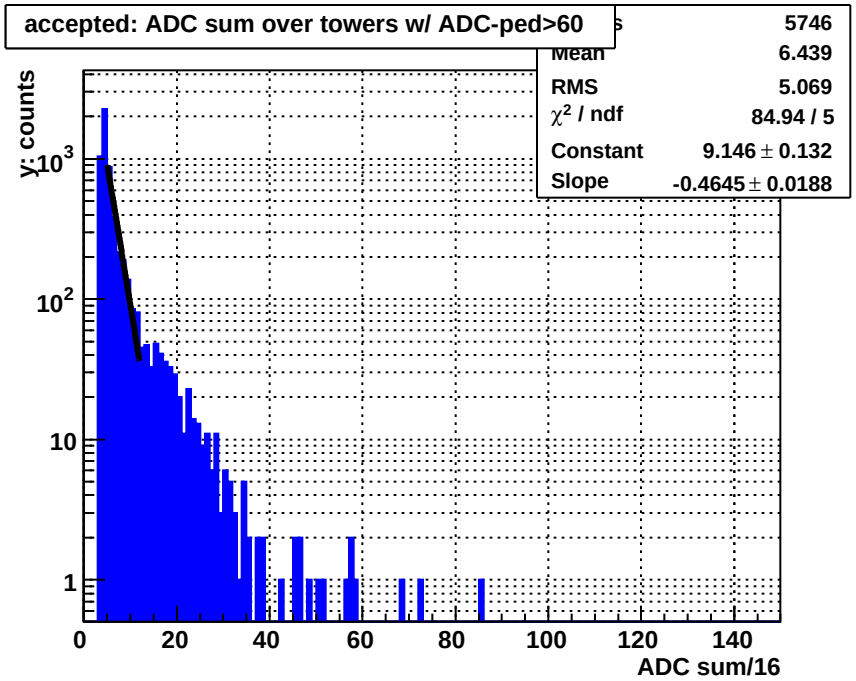

compute: # towers w/ ADC-ped>60 / event
Entries 300000
Mean 0.6151
RMS 0.4612
Underflow 0
Overflow 0
y: counts
x: # towers

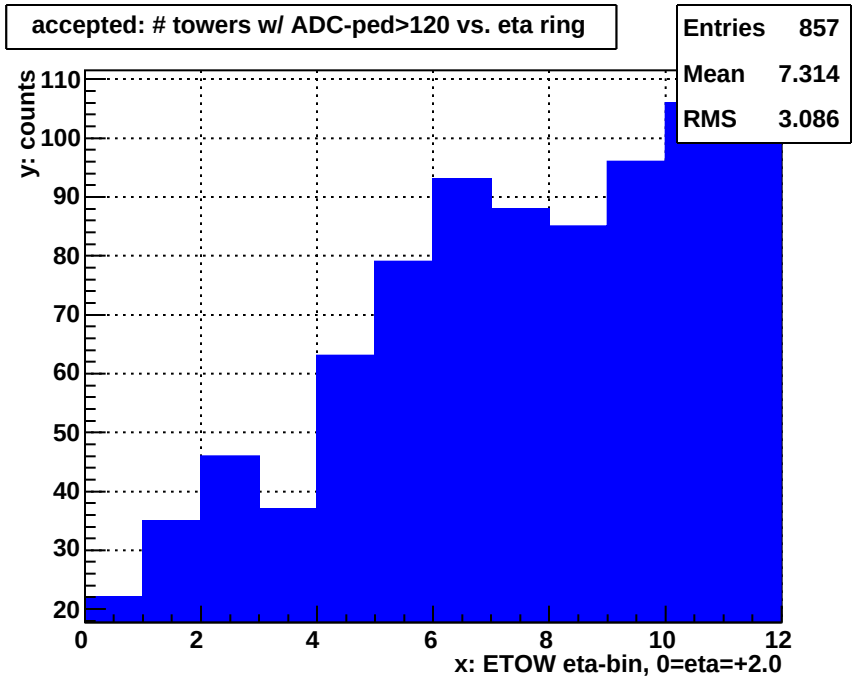

L2 COMP+DECI time per input event
Entries 122925
Mean 2.148
RMS 0.7913
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

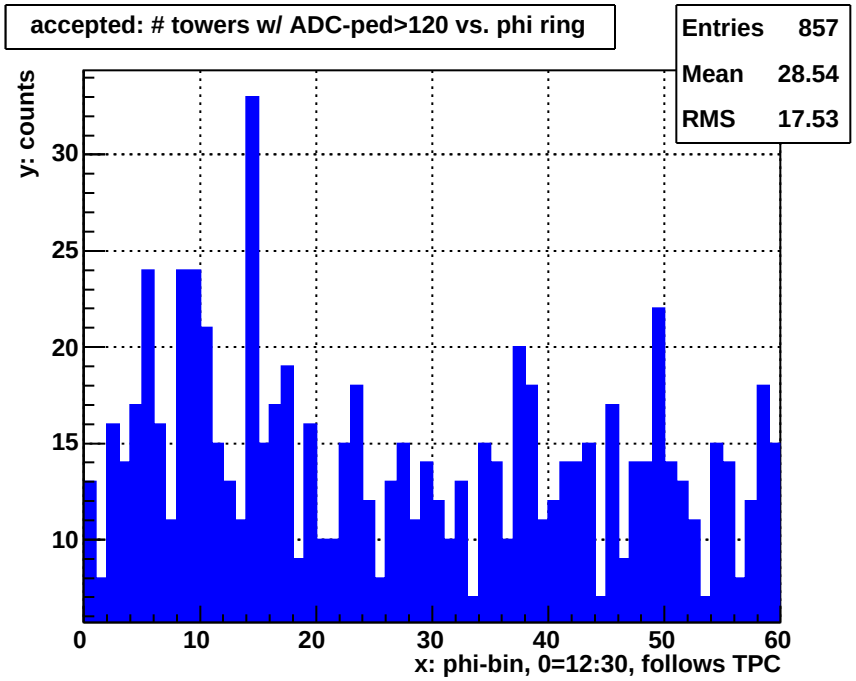

rate of ACCEPT
Entries 122925
Mean 326.3
RMS 152
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 122925
Mean 0.5561
RMS 0.281
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 6893
Mean 5.451
RMS 2.979
χ² / ndf 16.64 / 5
Constant 10.61 ± 0.11
Slope -0.6432 ± 0.0167
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 5746
Mean 6.439
RMS 5.069
χ² / ndf 84.94 / 5
Constant 9.146 ± 0.132
Slope -0.4645 ± 0.0188
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 857
Mean 7.314
RMS 3.086
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 857
Mean 28.54
RMS 17.53
y: counts
x: phi-bin, 0=12:30, follows TPC