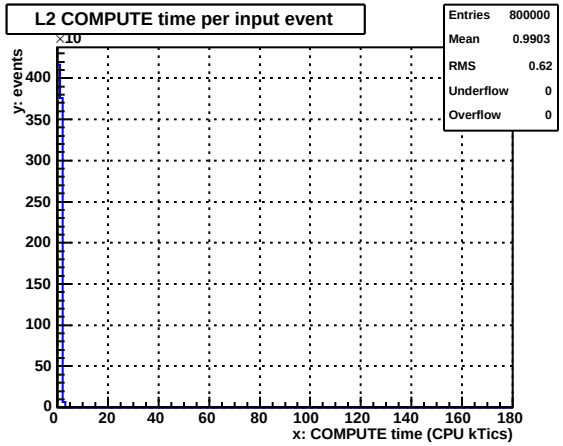
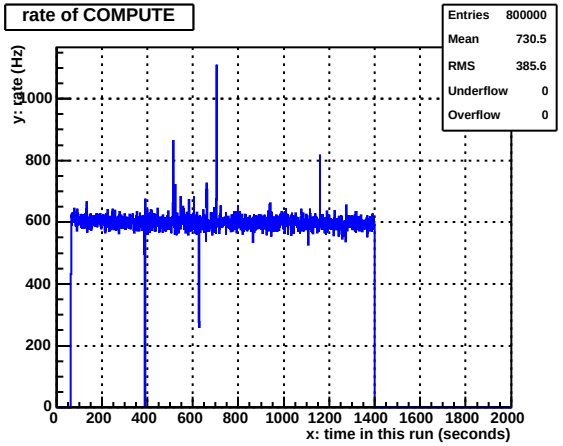
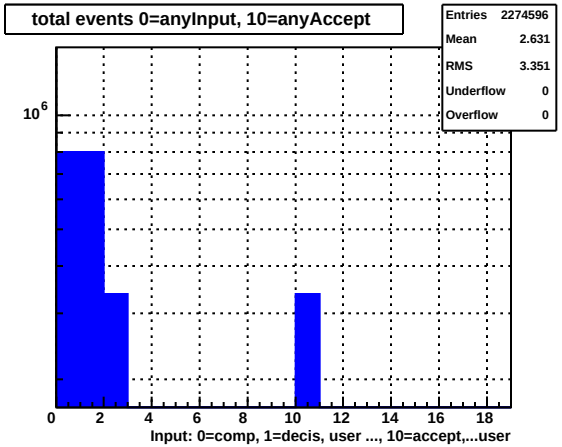
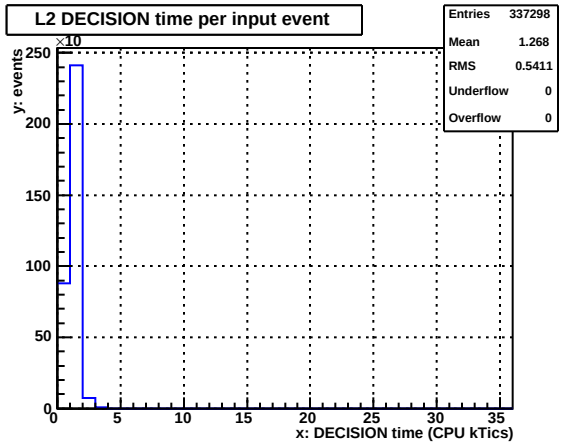
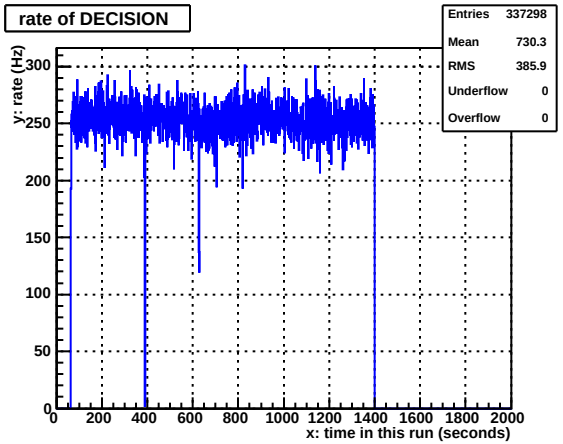
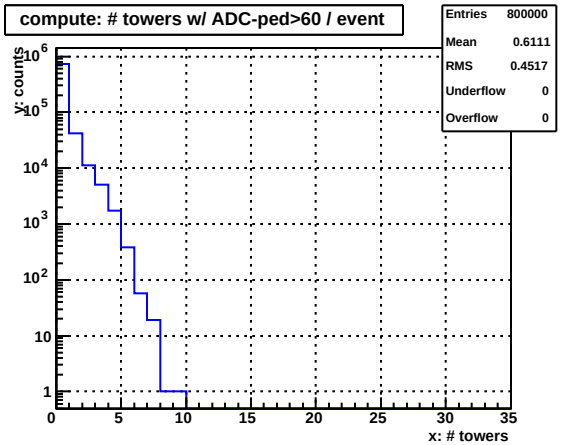

L2 COMPUTE time per input event
Entries 800000
Mean 0.9903
RMS 0.62
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

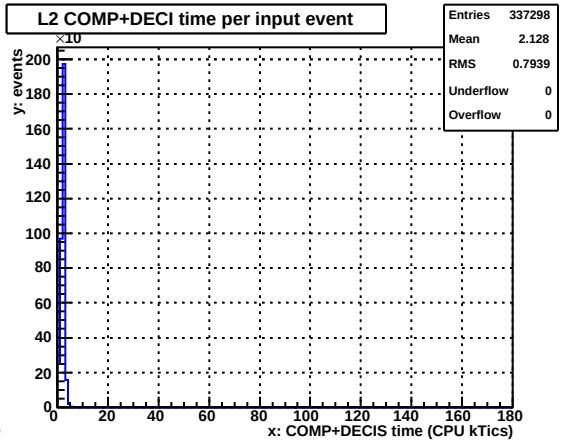

rate of COMPUTE
Entries 800000
Mean 730.5
RMS 385.6
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

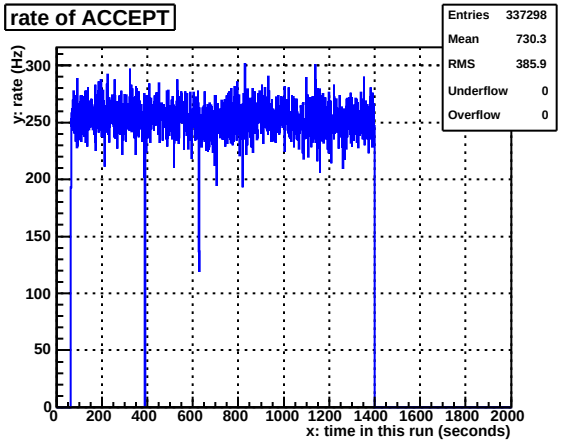

total events 0=anyInput, 10=anyAccept
Entries 2274596
Mean 2.631
RMS 3.351
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

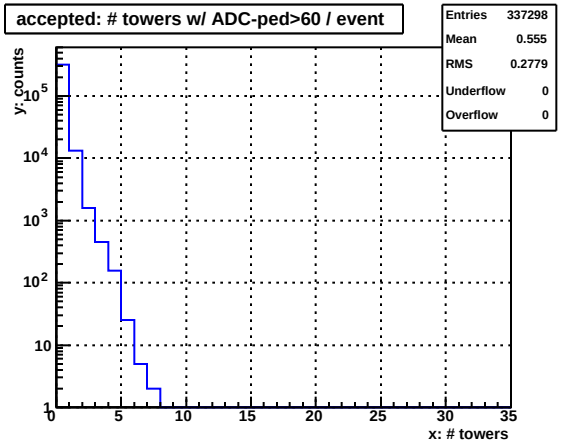

L2 DECISION time per input event
Entries 337298
Mean 1.268
RMS 0.5411
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

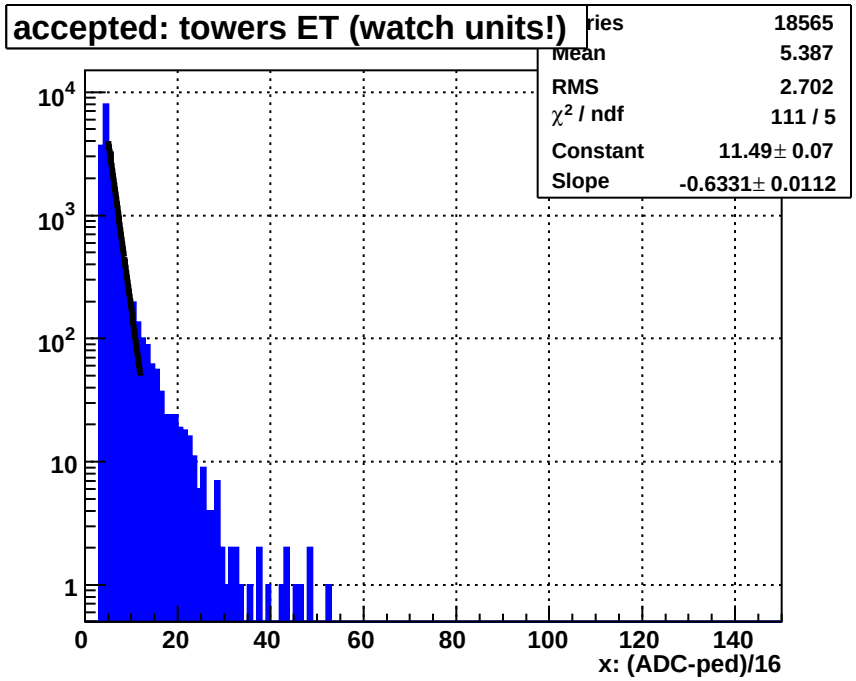

rate of DECISION
Entries 337298
Mean 730.3
RMS 385.9
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

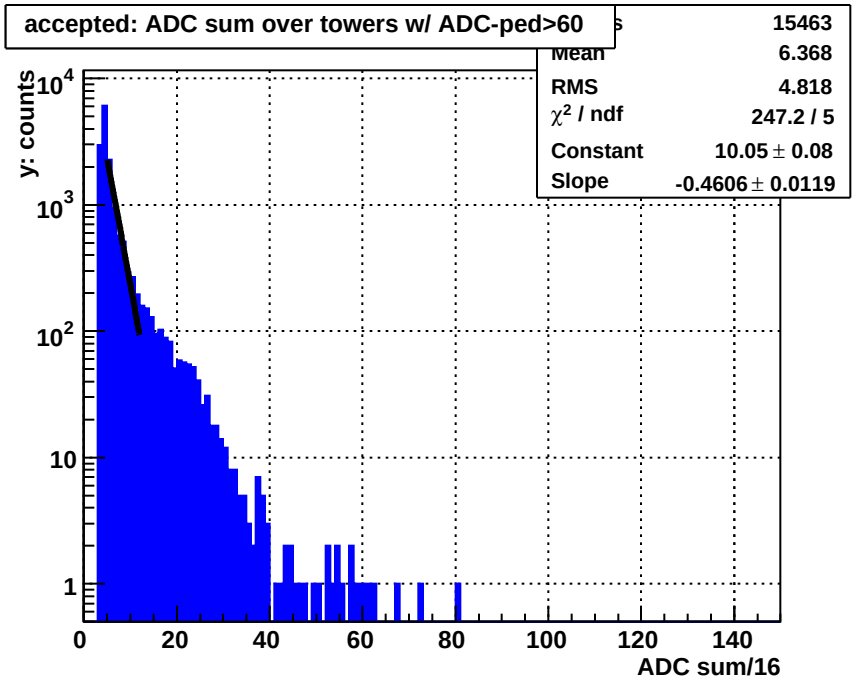

compute: # towers w/ ADC-ped>60 / event
Entries 800000
Mean 0.6111
RMS 0.4517
Underflow 0
Overflow 0
y: counts
x: # towers

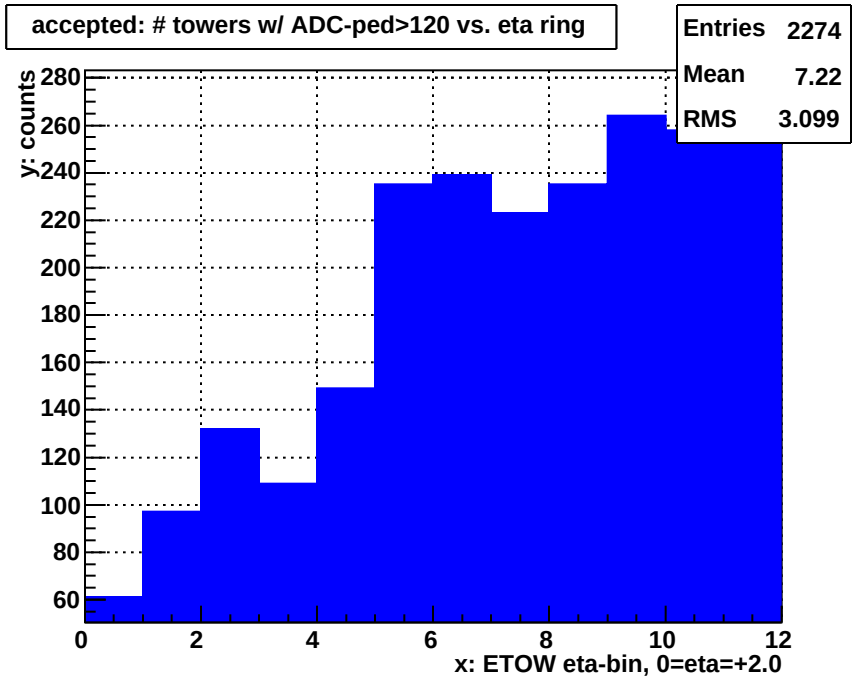

L2 COMP+DECI time per input event
Entries 337298
Mean 2.128
RMS 0.7939
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

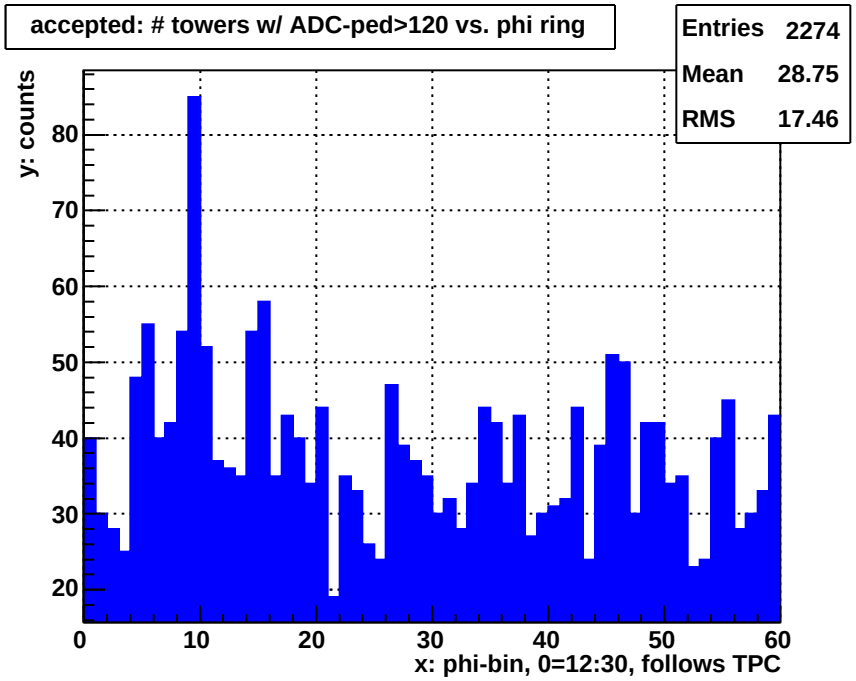

rate of ACCEPT
Entries 337298
Mean 730.3
RMS 385.9
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 337298
Mean 0.555
RMS 0.2779
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 18565
Mean 5.387
RMS 2.702
χ² / ndf 111 / 5
Constant 11.49 ± 0.07
Slope -0.6331 ± 0.0112
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 15463
Mean 6.368
RMS 4.818
χ² / ndf 247.2 / 5
Constant 10.05 ± 0.08
Slope -0.4606 ± 0.0119
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 2274
Mean 7.22
RMS 3.099
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 2274
Mean 28.75
RMS 17.46
y: counts
x: phi-bin, 0=12:30, follows TPC