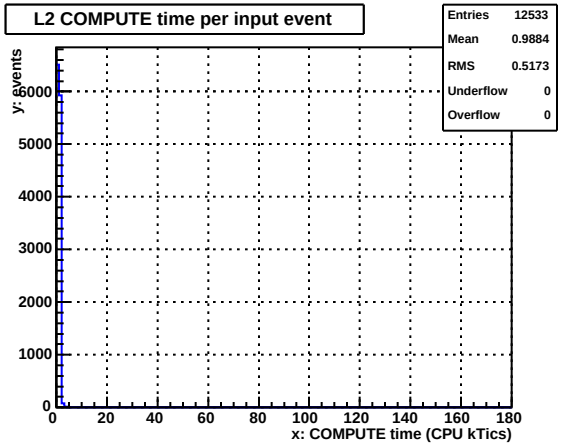
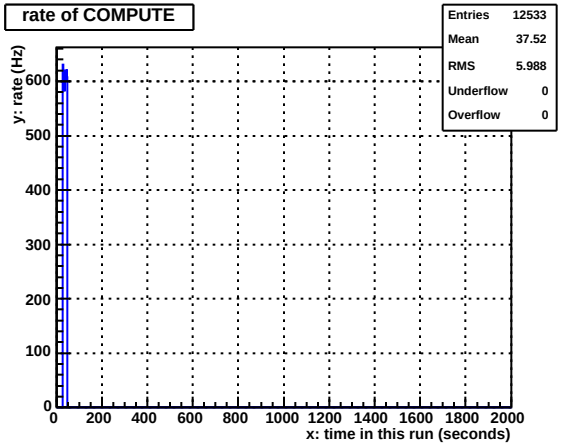
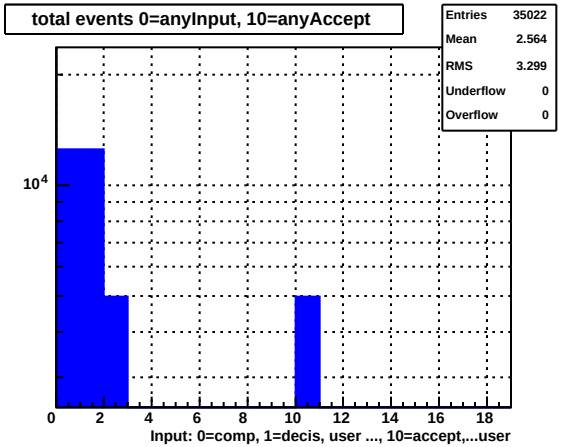
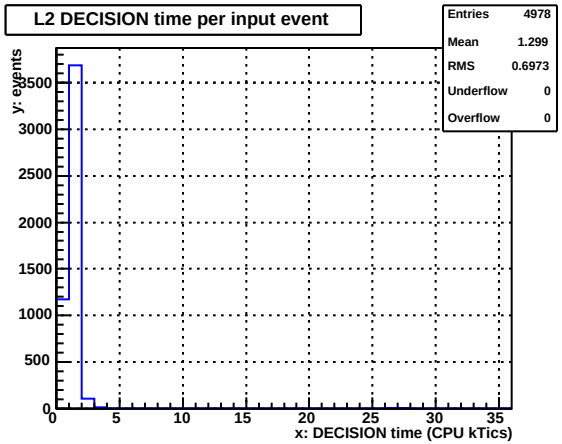
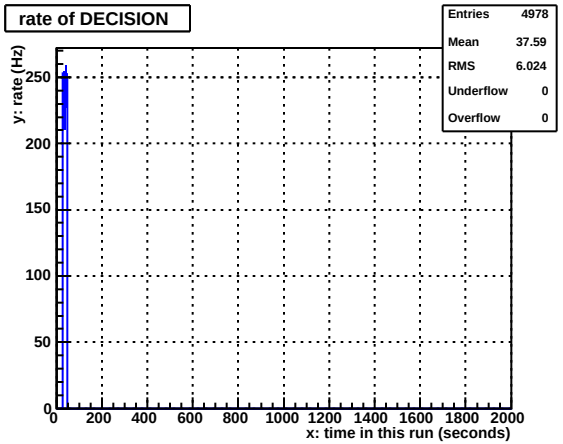
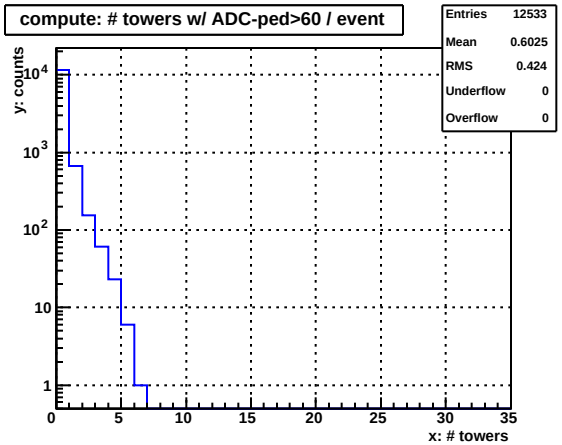

L2 COMPUTE time per input event
Entries 12533
Mean 0.9884
RMS 0.5173
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

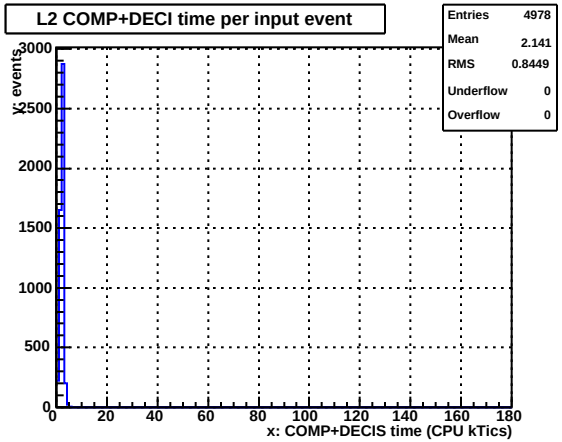

rate of COMPUTE
Entries 12533
Mean 37.52
RMS 5.988
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

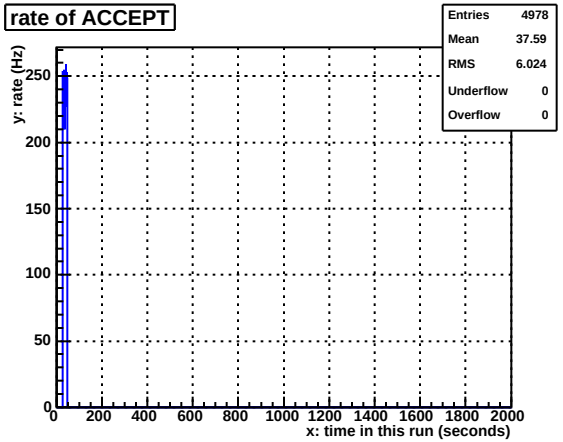

total events 0=anyInput, 10=anyAccept
Entries 35022
Mean 2.564
RMS 3.299
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

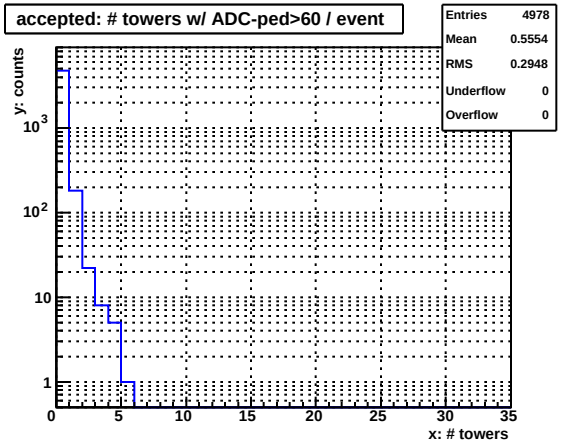

L2 DECISION time per input event
Entries 4978
Mean 1.299
RMS 0.6973
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

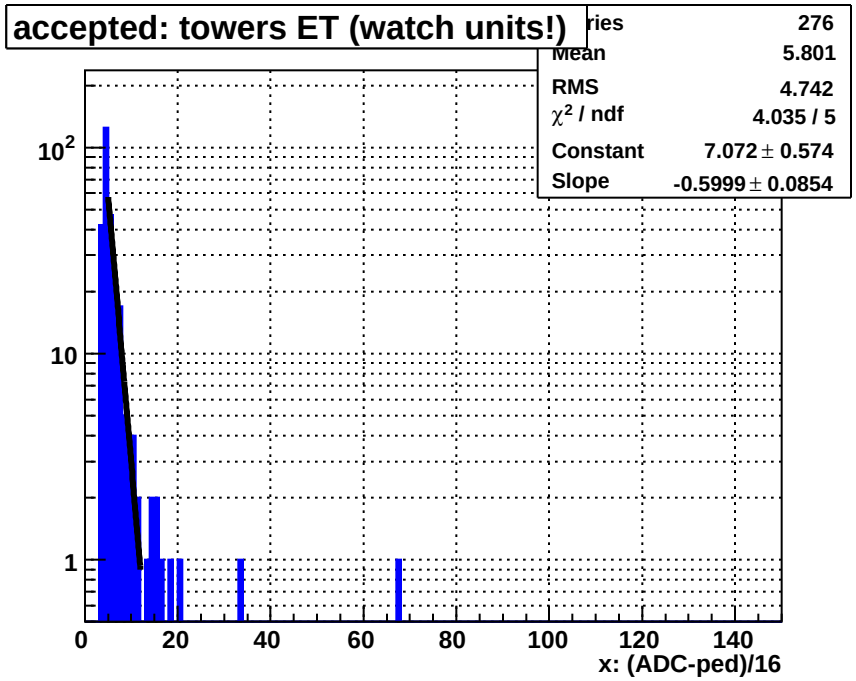

rate of DECISION
Entries 4978
Mean 37.59
RMS 6.024
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

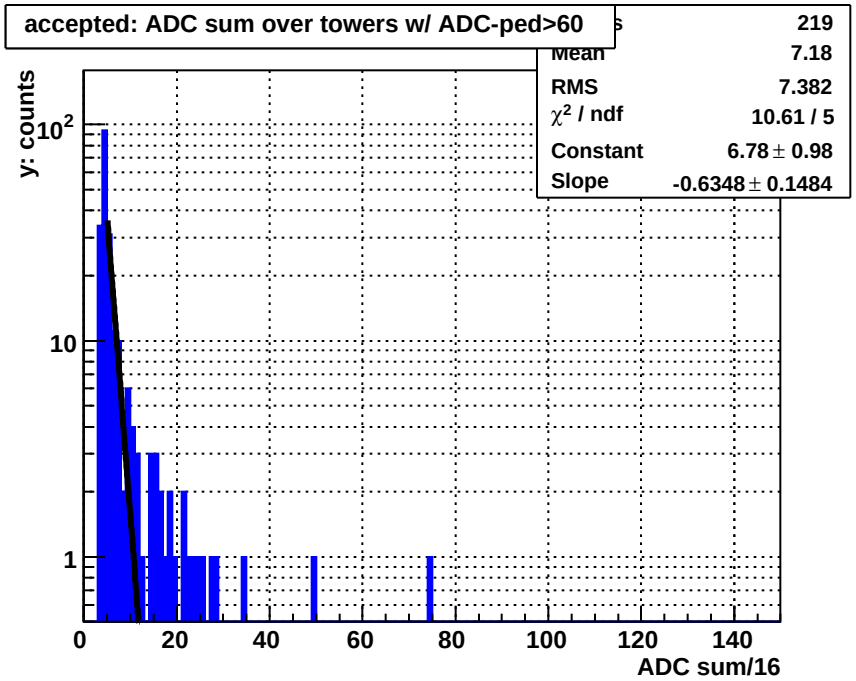

compute: # towers w/ ADC-ped>60 / event
Entries 12533
Mean 0.6025
RMS 0.424
Underflow 0
Overflow 0
y: counts
x: # towers

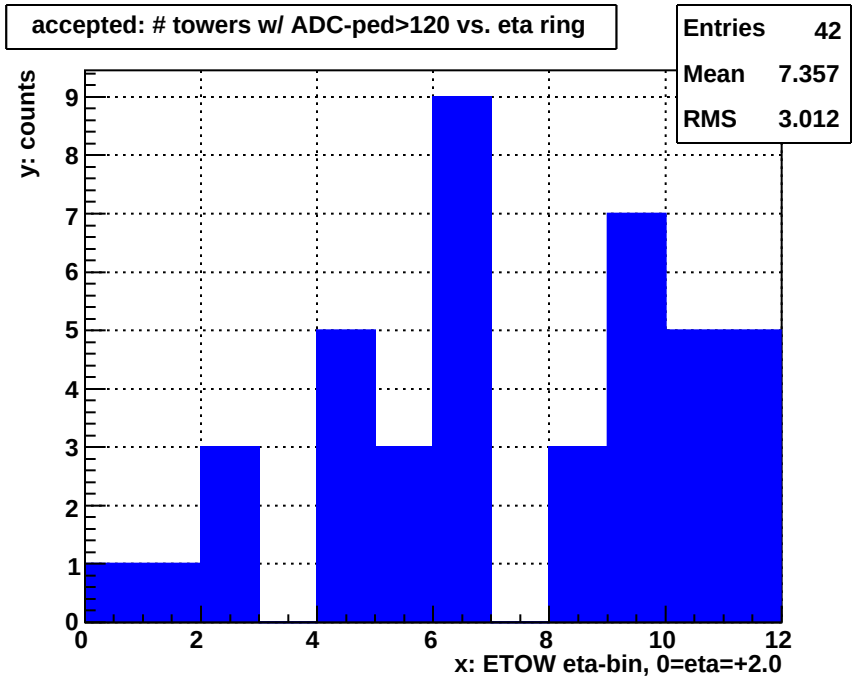

L2 COMP+DECI time per input event
Entries 4978
Mean 2.141
RMS 0.8449
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

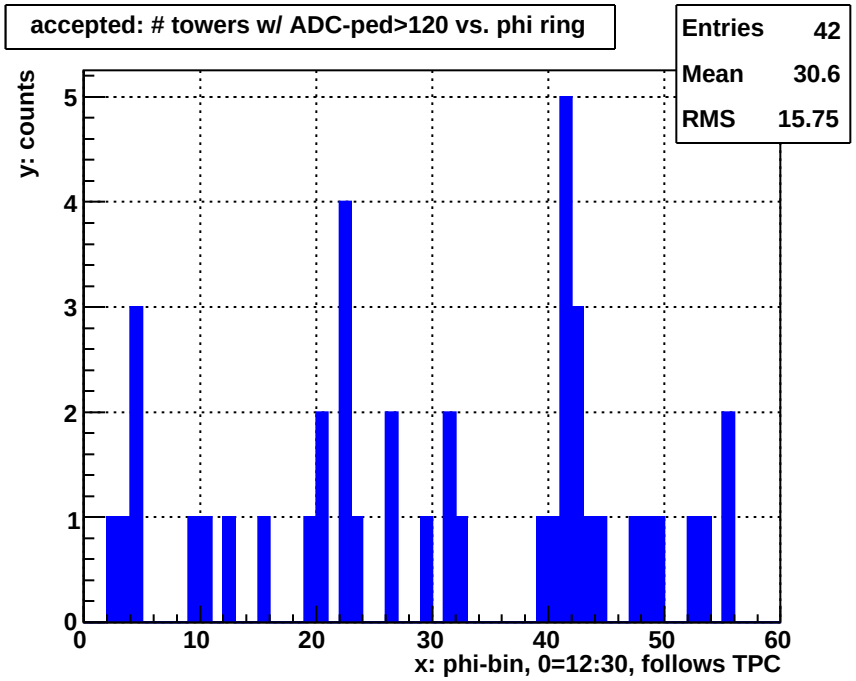

rate of ACCEPT
Entries 4978
Mean 37.59
RMS 6.024
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 4978
Mean 0.5554
RMS 0.2948
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 276
Mean 5.801
RMS 4.742
χ² / ndf 4.035 / 5
Constant 7.072 ± 0.574
Slope -0.5999 ± 0.0854
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 219
Mean 7.18
RMS 7.382
χ² / ndf 10.61 / 5
Constant 6.78 ± 0.98
Slope -0.6348 ± 0.1484
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 42
Mean 7.357
RMS 3.012
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 42
Mean 30.6
RMS 15.75
y: counts
x: phi-bin, 0=12:30, follows TPC