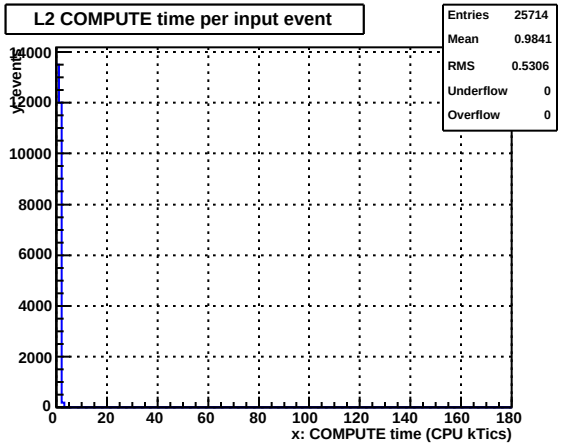
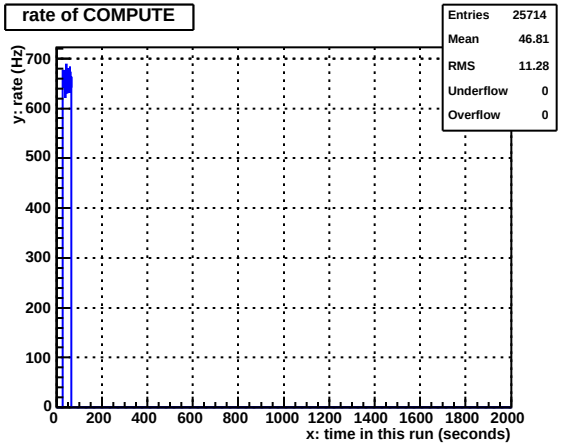
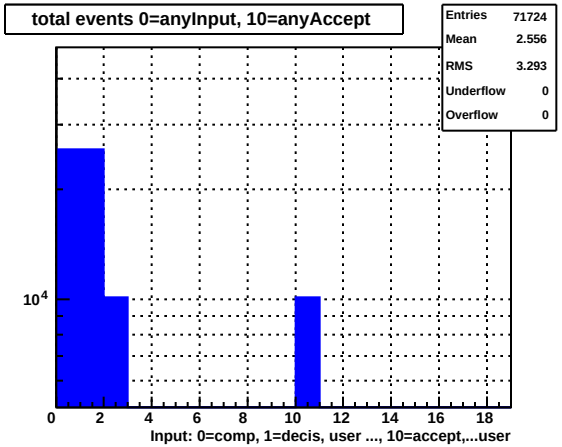
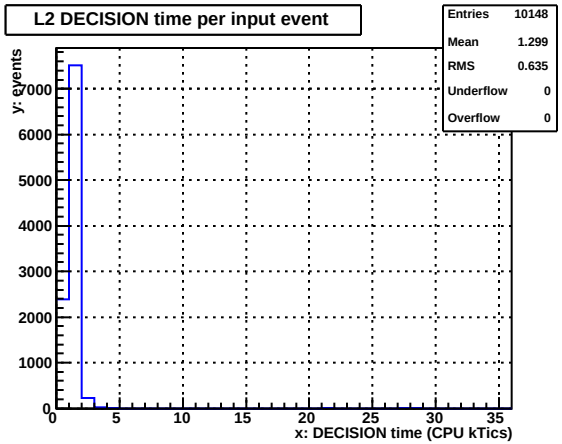
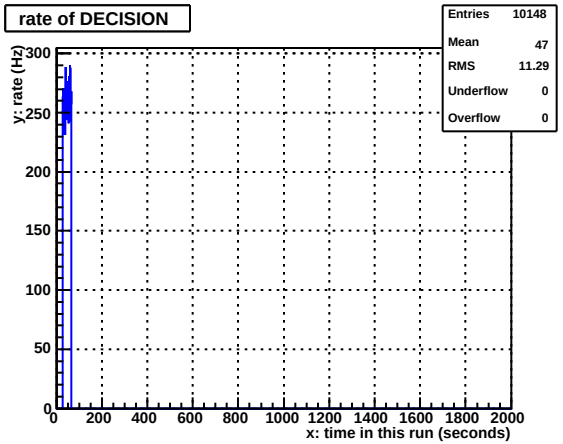
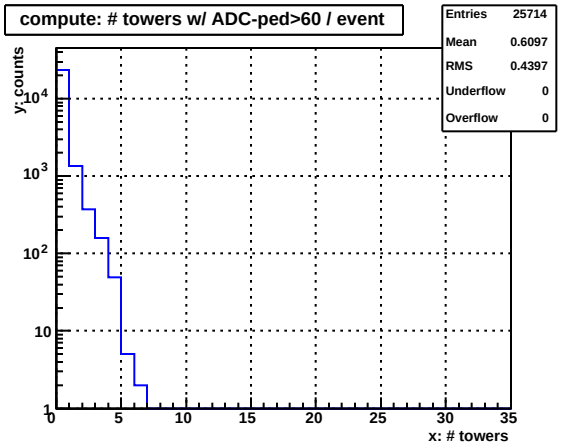

L2 COMPUTE time per input event
Entries 25714
Mean 0.9841
RMS 0.5306
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

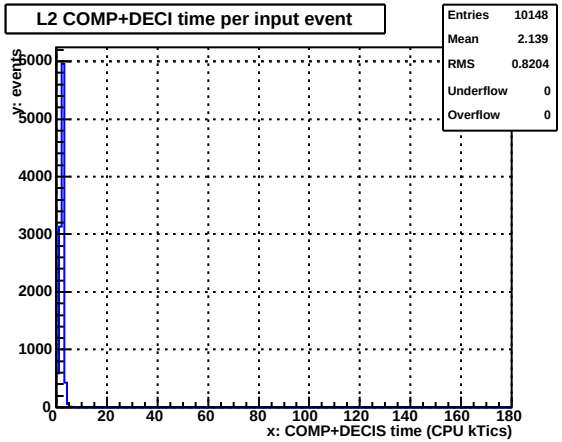

rate of COMPUTE
Entries 25714
Mean 46.81
RMS 11.28
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

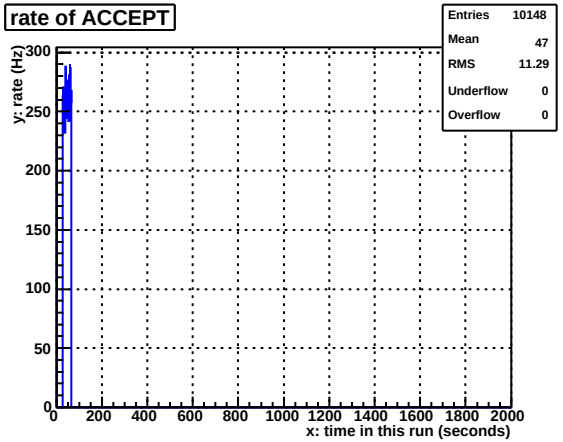

total events 0=anyInput, 10=anyAccept
Entries 71724
Mean 2.556
RMS 3.293
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

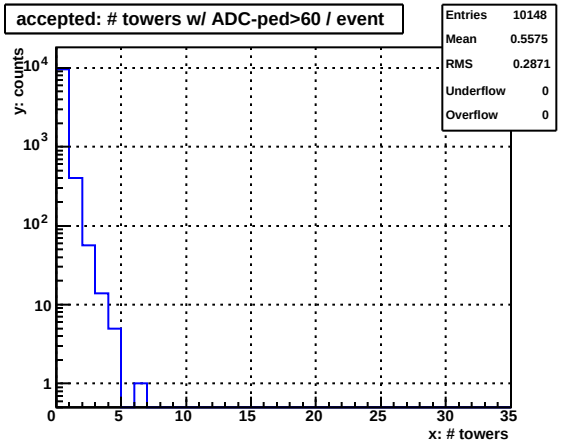

L2 DECISION time per input event
Entries 10148
Mean 1.299
RMS 0.635
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

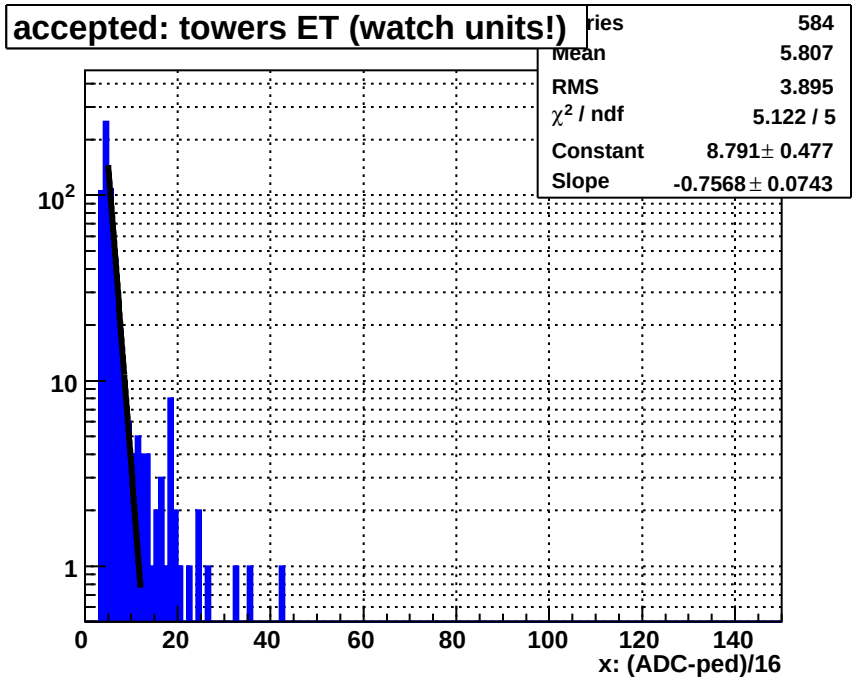

rate of DECISION
Entries 10148
Mean 47
RMS 11.29
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

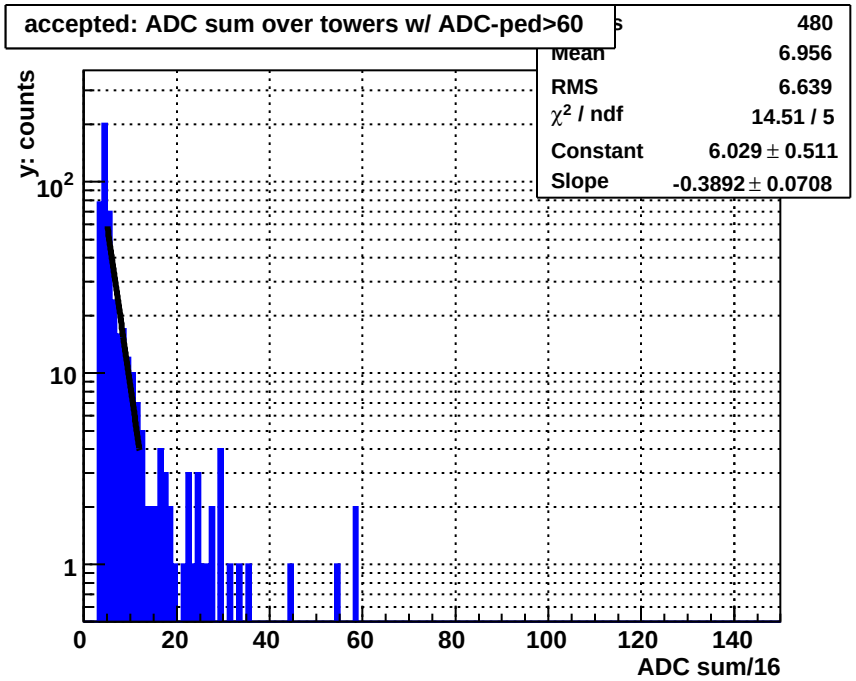

compute: # towers w/ ADC-ped>60 / event
Entries 25714
Mean 0.6097
RMS 0.4397
Underflow 0
Overflow 0
y: counts
x: # towers

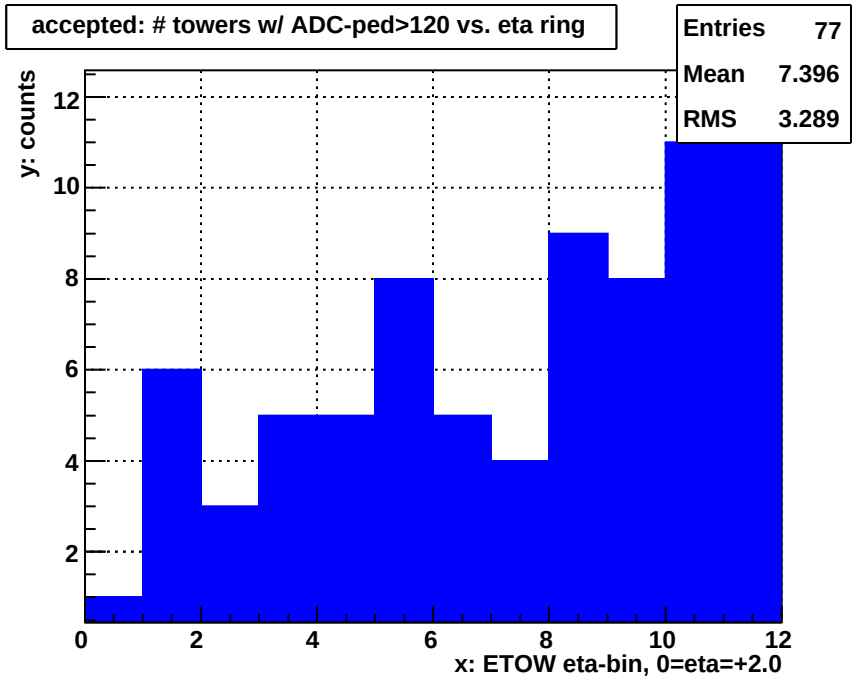

L2 COMP+DECI time per input event
Entries 10148
Mean 2.139
RMS 0.8204
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

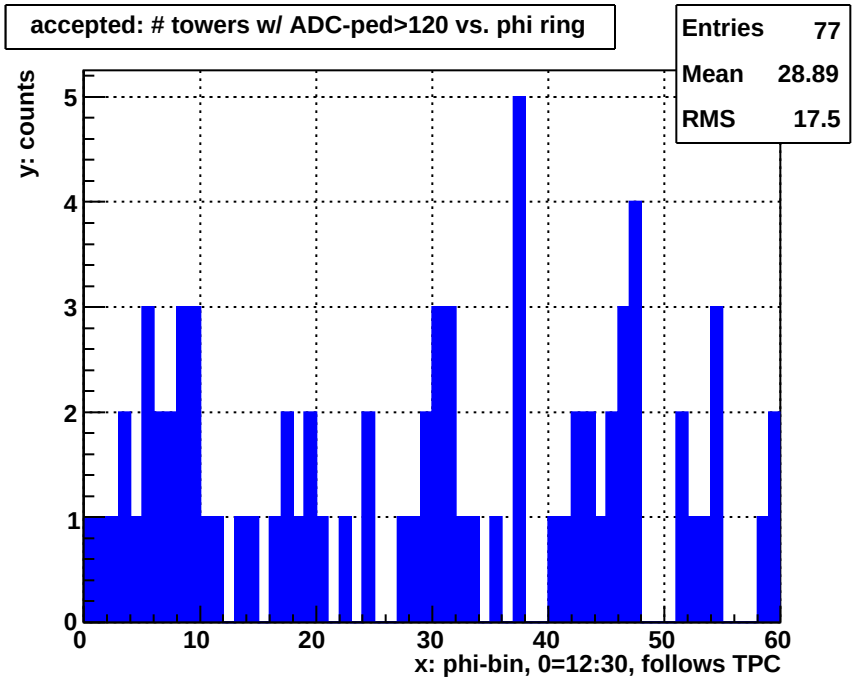

rate of ACCEPT
Entries 10148
Mean 47
RMS 11.29
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 10148
Mean 0.5575
RMS 0.2871
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 584
Mean 5.807
RMS 3.895
χ² / ndf 5.122 / 5
Constant 8.791 ± 0.477
Slope -0.7568 ± 0.0743
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 480
Mean 6.956
RMS 6.639
χ² / ndf 14.51 / 5
Constant 6.029 ± 0.511
Slope -0.3892 ± 0.0708
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 77
Mean 7.396
RMS 3.289
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 77
Mean 28.89
RMS 17.5
y: counts
x: phi-bin, 0=12:30, follows TPC