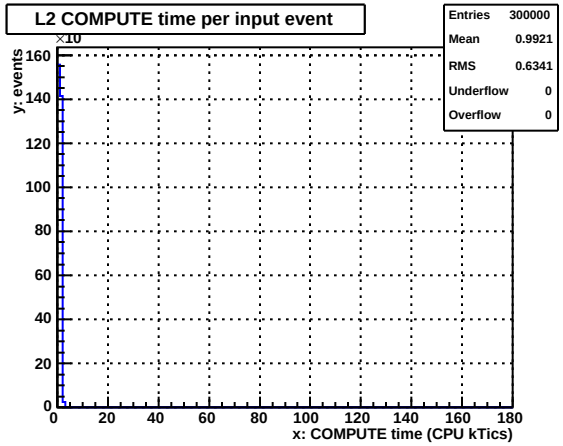
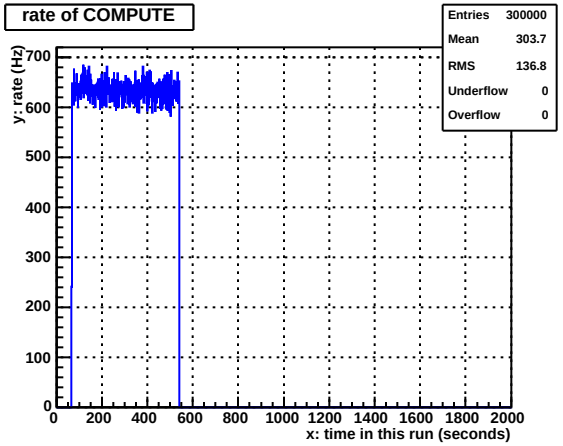
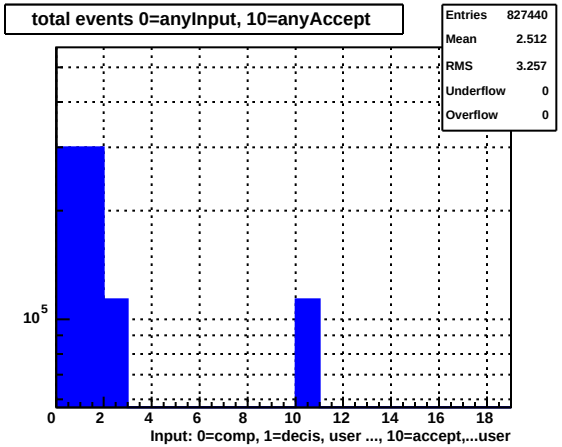
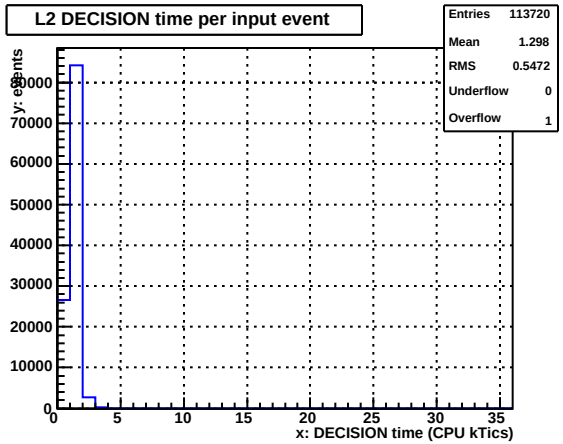
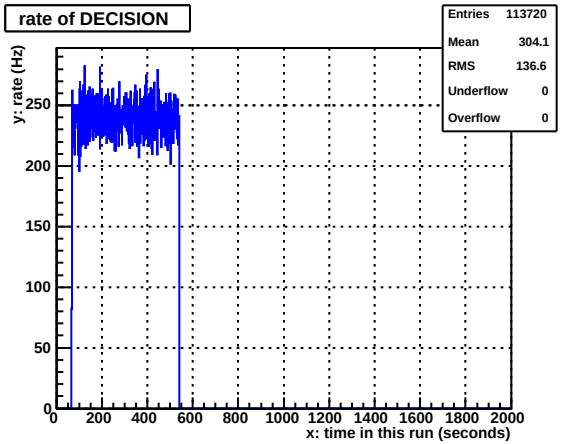
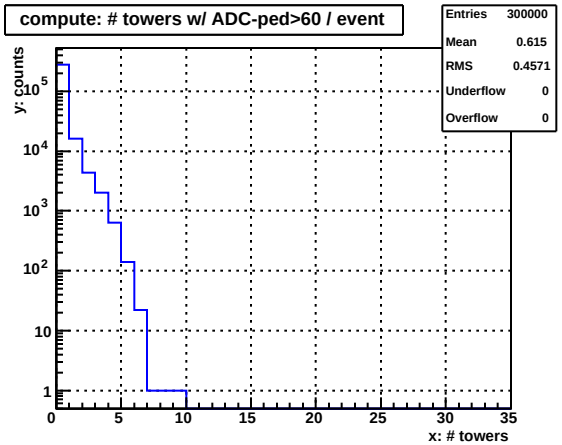

L2 COMPUTE time per input event
Entries 300000
Mean 0.9921
RMS 0.6341
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

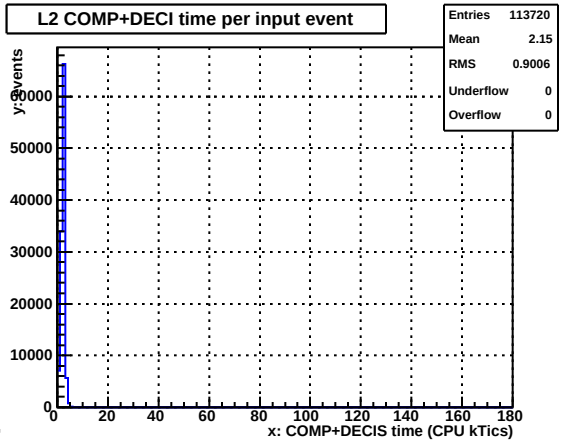

rate of COMPUTE
Entries 300000
Mean 303.7
RMS 136.8
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

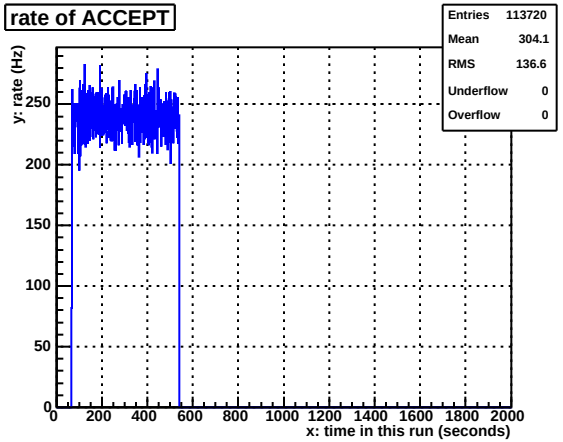

total events 0=anyInput, 10=anyAccept
Entries 827440
Mean 2.512
RMS 3.257
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

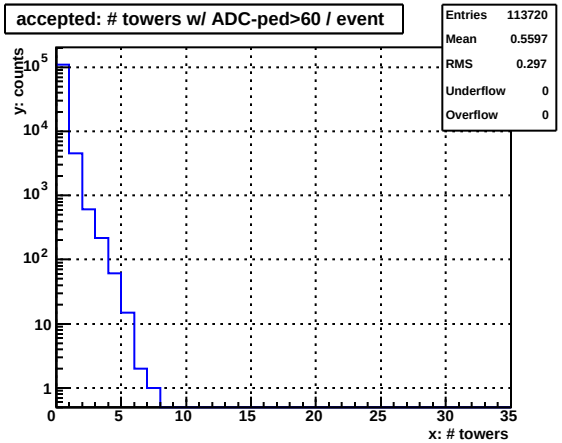

L2 DECISION time per input event
Entries 113720
Mean 1.298
RMS 0.5472
Underflow 0
Overflow 1
y: events
x: DECISION time (CPU kTics)

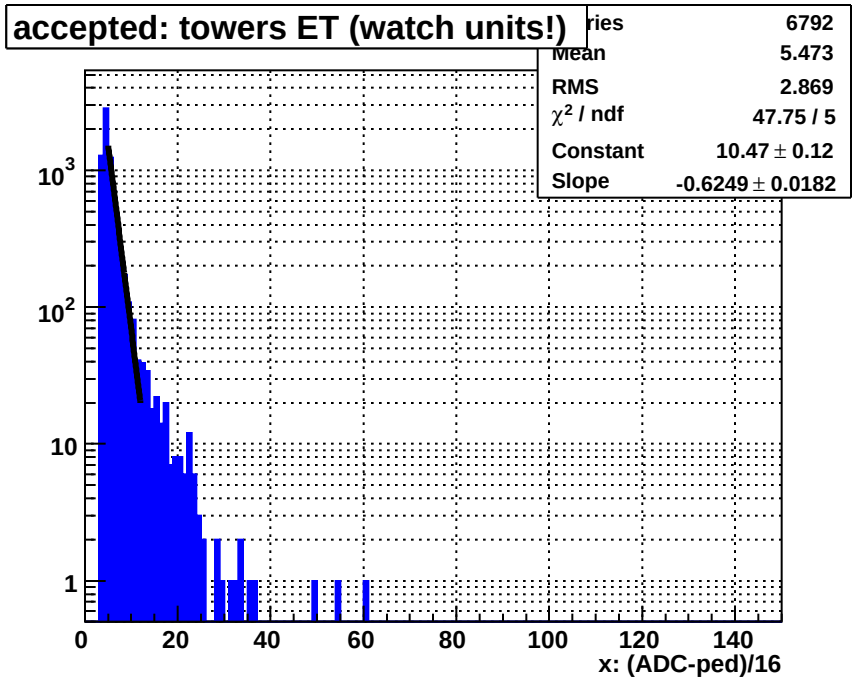

rate of DECISION
Entries 113720
Mean 304.1
RMS 136.6
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

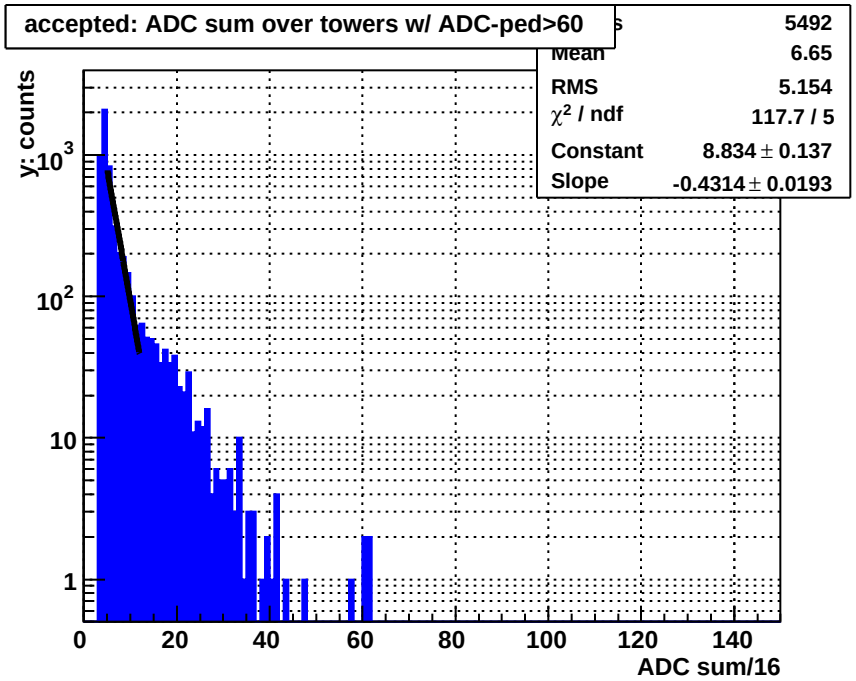

compute: # towers w/ ADC-ped>60 / event
Entries 300000
Mean 0.615
RMS 0.4571
Underflow 0
Overflow 0
y: counts
x: # towers

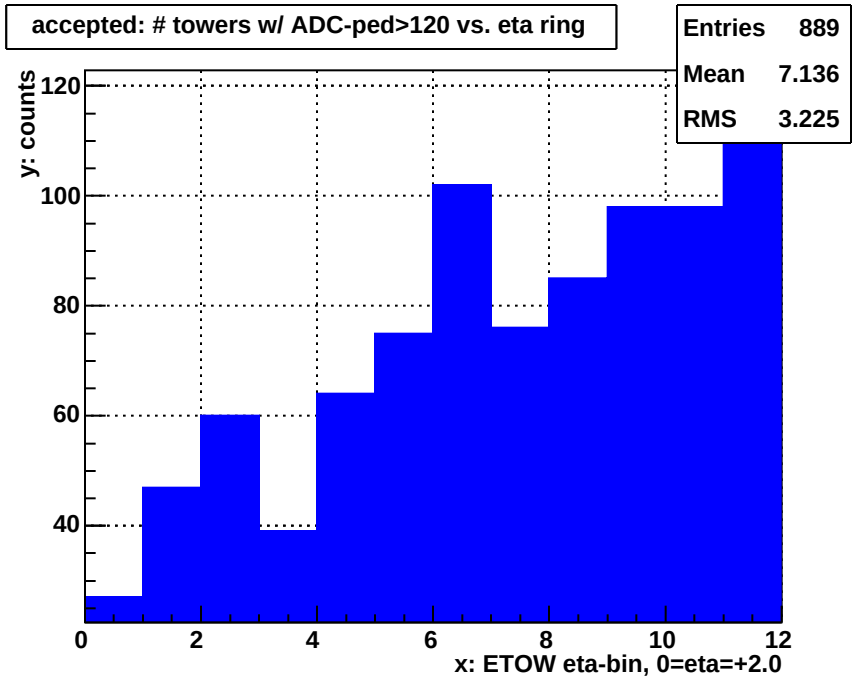

L2 COMP+DECI time per input event
Entries 113720
Mean 2.15
RMS 0.9006
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

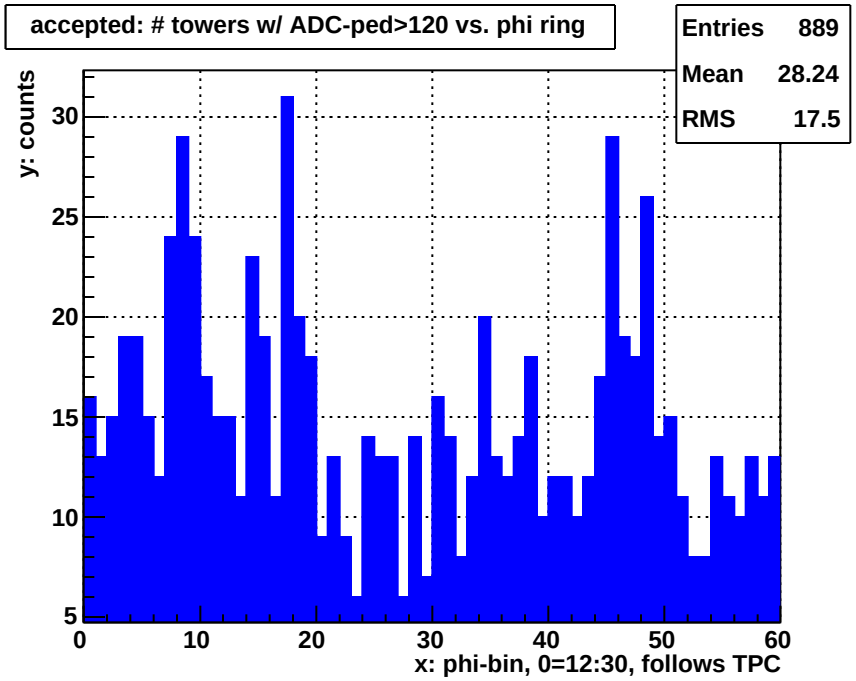

rate of ACCEPT
Entries 113720
Mean 304.1
RMS 136.6
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 113720
Mean 0.5597
RMS 0.297
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 6792
Mean 5.473
RMS 2.869
χ² / ndf 47.75 / 5
Constant 10.47 ± 0.12
Slope -0.6249 ± 0.0182
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 5492
Mean 6.65
RMS 5.154
χ² / ndf 117.7 / 5
Constant 8.834 ± 0.137
Slope -0.4314 ± 0.0193
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 889
Mean 7.136
RMS 3.225
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 889
Mean 28.24
RMS 17.5
y: counts
x: phi-bin, 0=12:30, follows TPC