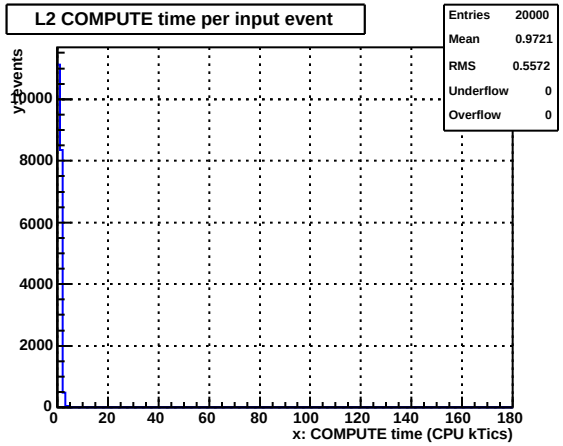
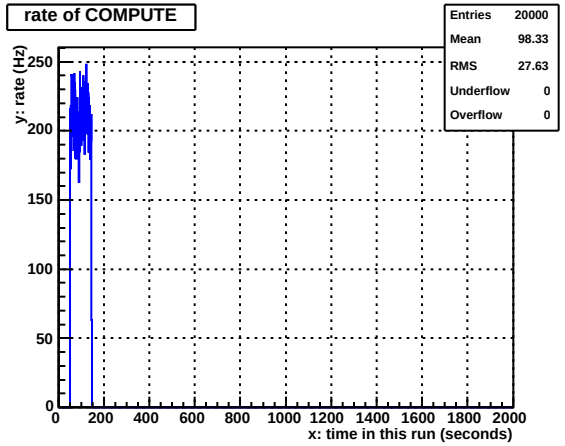
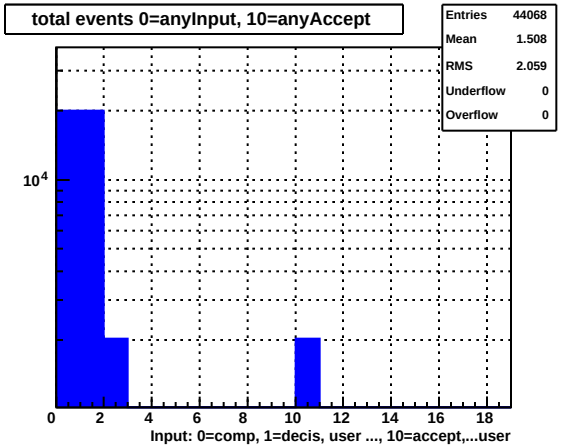
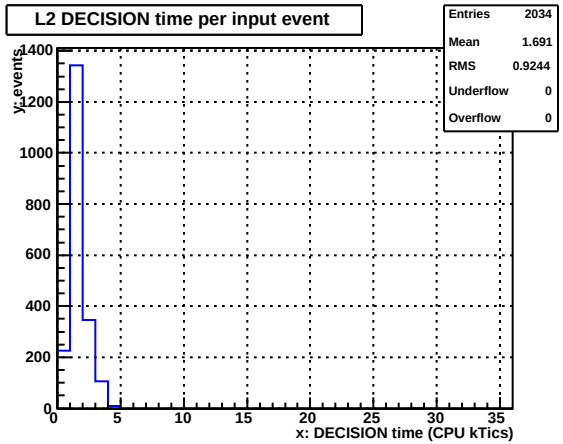
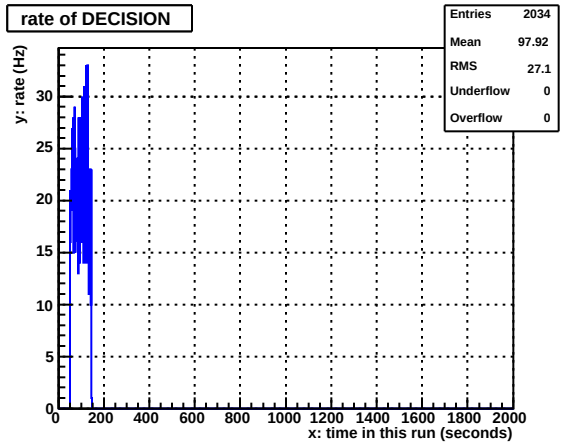
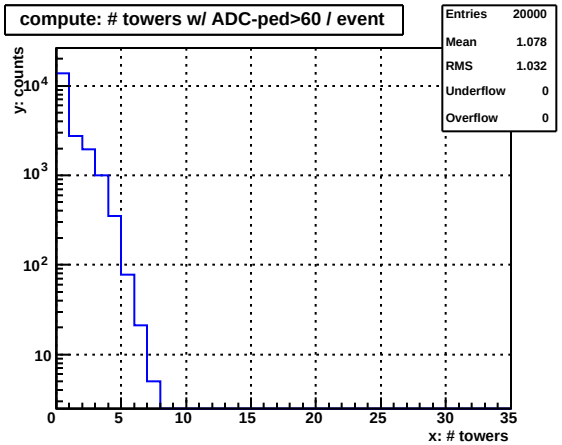

L2 COMPUTE time per input event
Entries 20000
Mean 0.9721
RMS 0.5572
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

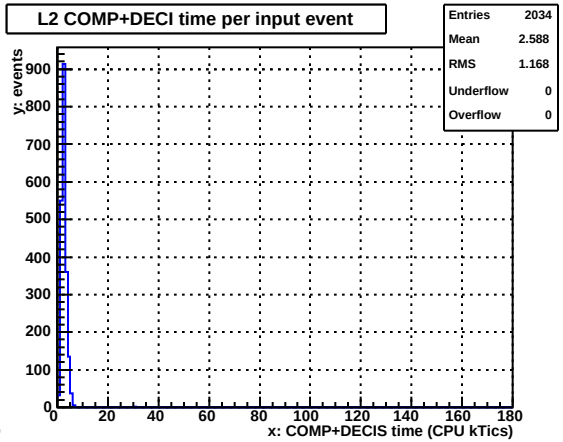

rate of COMPUTE
Entries 20000
Mean 98.33
RMS 27.63
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

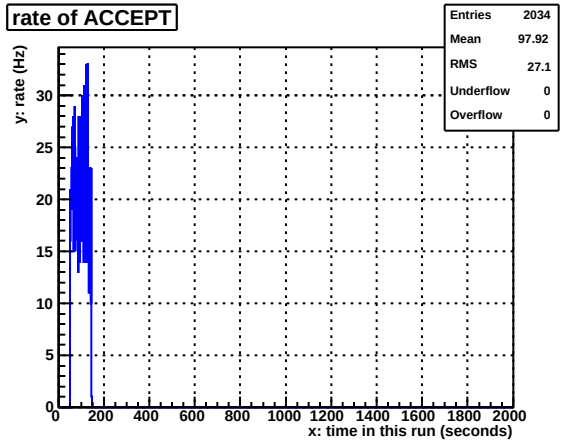

total events 0=anyInput, 10=anyAccept
Entries 44068
Mean 1.508
RMS 2.059
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

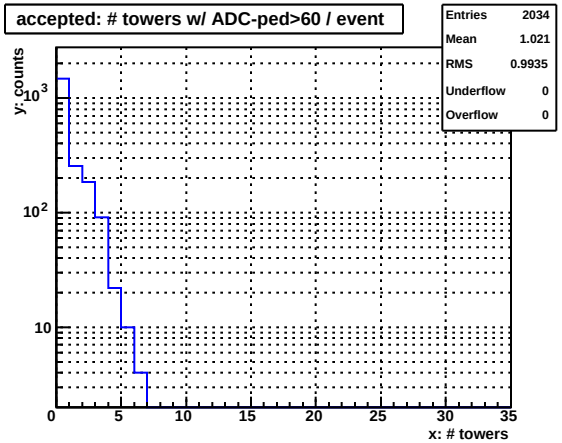

L2 DECISION time per input event
Entries 2034
Mean 1.691
RMS 0.9244
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

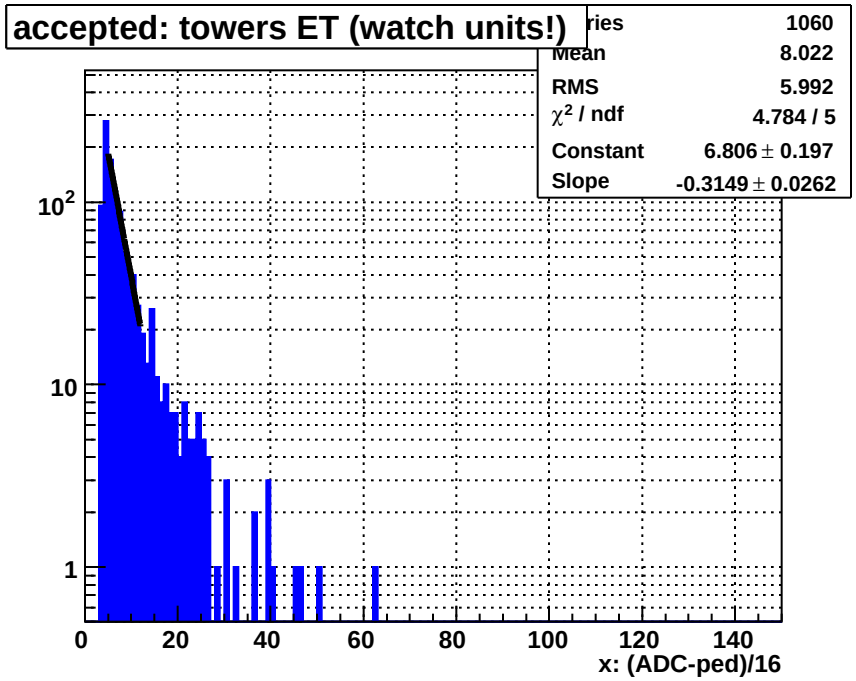

rate of DECISION
Entries 2034
Mean 97.92
RMS 27.1
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

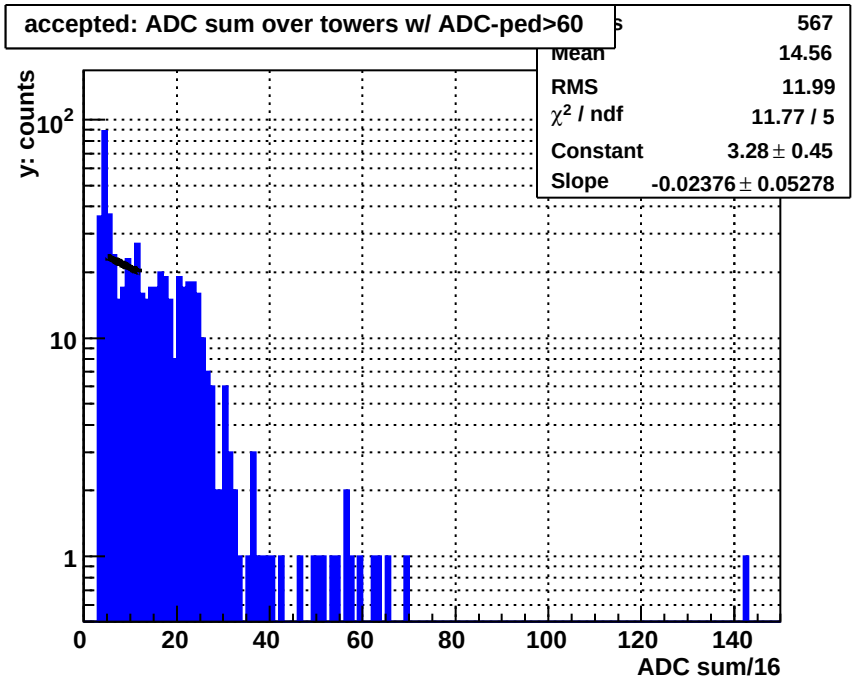

compute: # towers w/ ADC-ped>60 / event
Entries 20000
Mean 1.078
RMS 1.032
Underflow 0
Overflow 0
y: counts
x: # towers

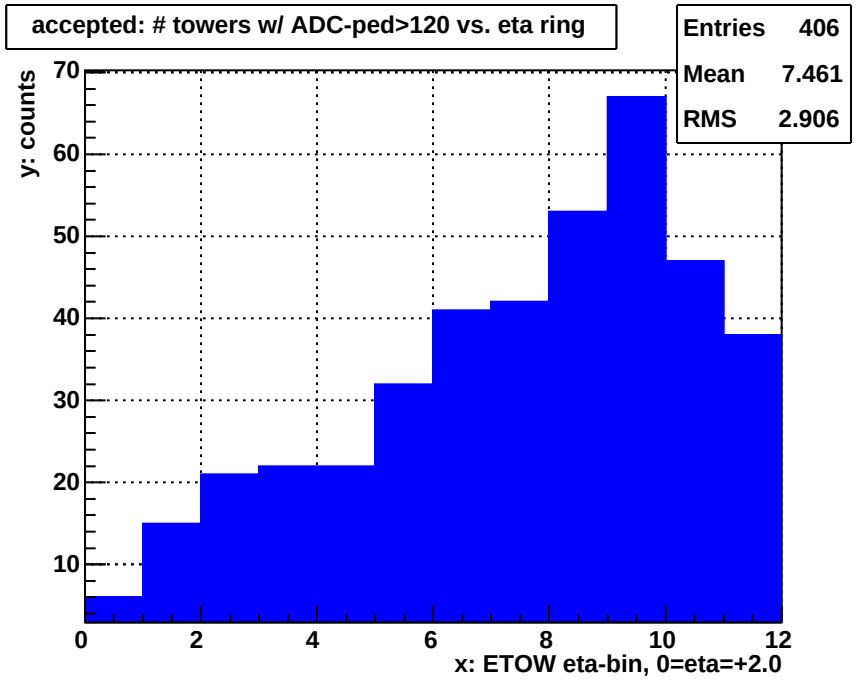

L2 COMP+DECI time per input event
Entries 2034
Mean 2.588
RMS 1.168
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

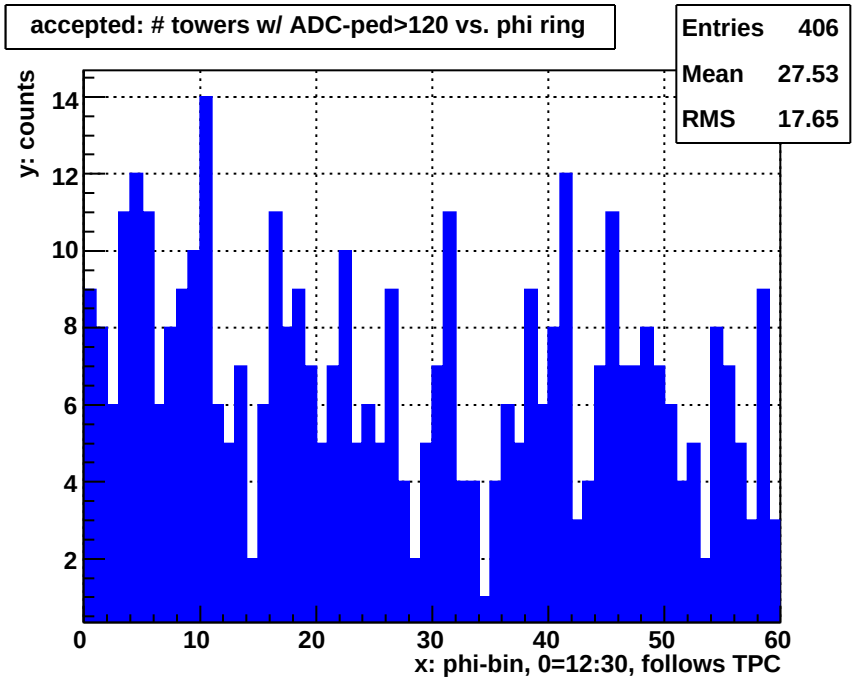

rate of ACCEPT
Entries 2034
Mean 97.92
RMS 27.1
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 2034
Mean 1.021
RMS 0.9935
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 1060
Mean 8.022
RMS 5.992
χ² / ndf 4.784 / 5
Constant 6.806 ± 0.197
Slope -0.3149 ± 0.0262
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 567
Mean 14.56
RMS 11.99
χ² / ndf 11.77 / 5
Constant 3.28 ± 0.45
Slope -0.02376 ± 0.05278
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 406
Mean 7.461
RMS 2.906
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 406
Mean 27.53
RMS 17.65
y: counts
x: phi-bin, 0=12:30, follows TPC