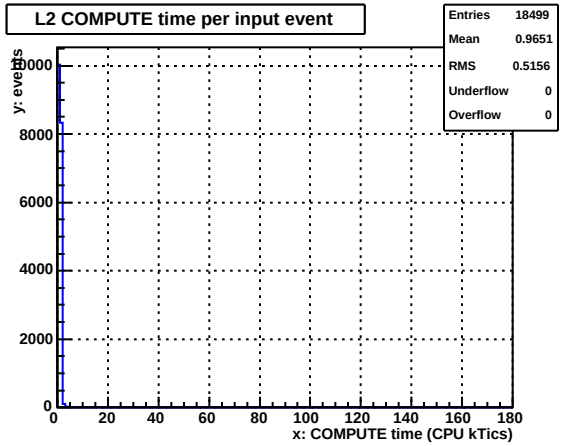
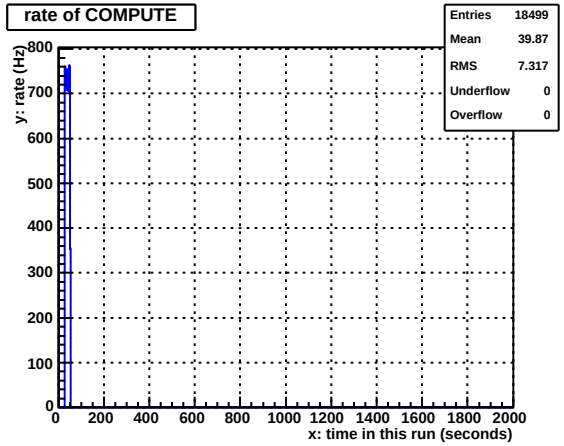
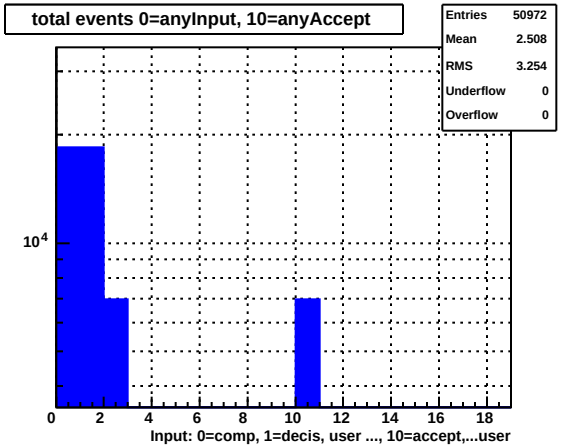
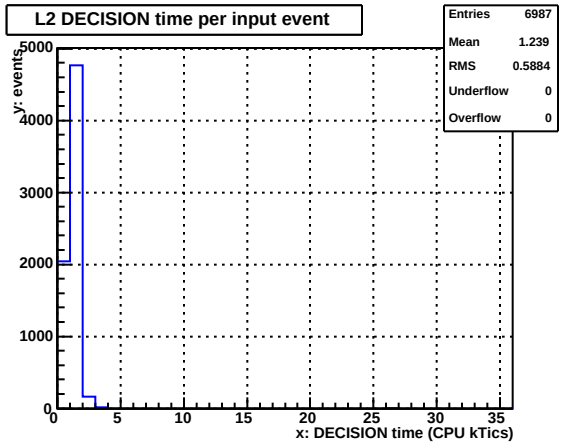
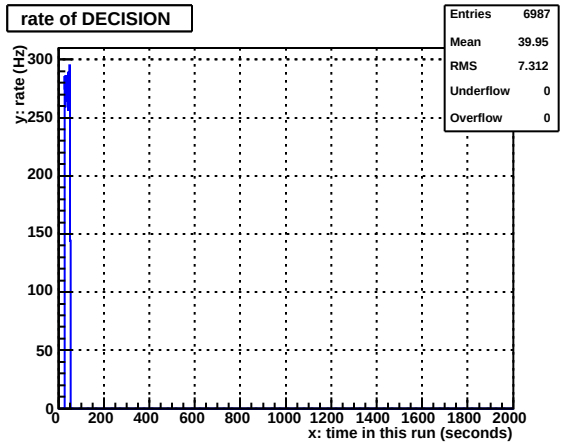
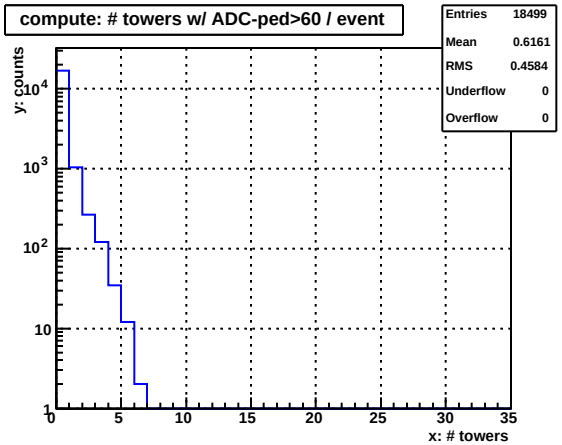

L2 COMPUTE time per input event
Entries 18499
Mean 0.9651
RMS 0.5156
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

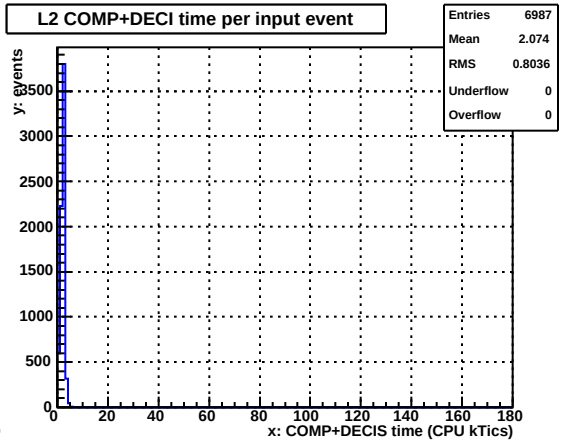

rate of COMPUTE
Entries 18499
Mean 39.87
RMS 7.317
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

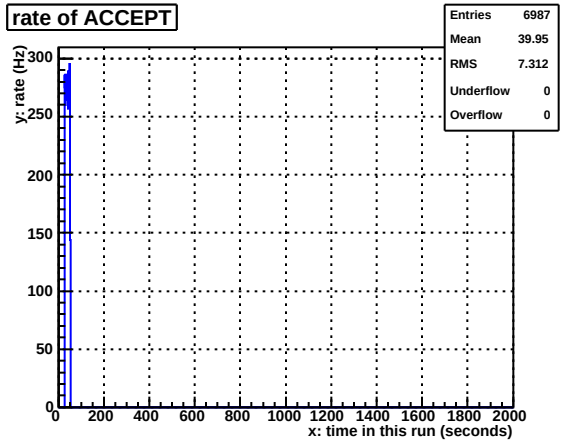

total events 0=anyInput, 10=anyAccept
Entries 50972
Mean 2.508
RMS 3.254
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

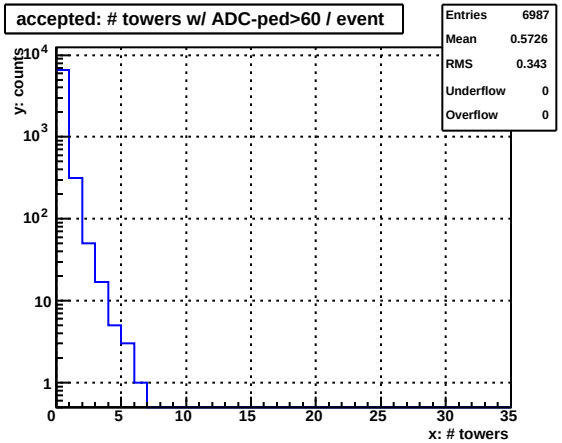

L2 DECISION time per input event
Entries 6987
Mean 1.239
RMS 0.5884
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

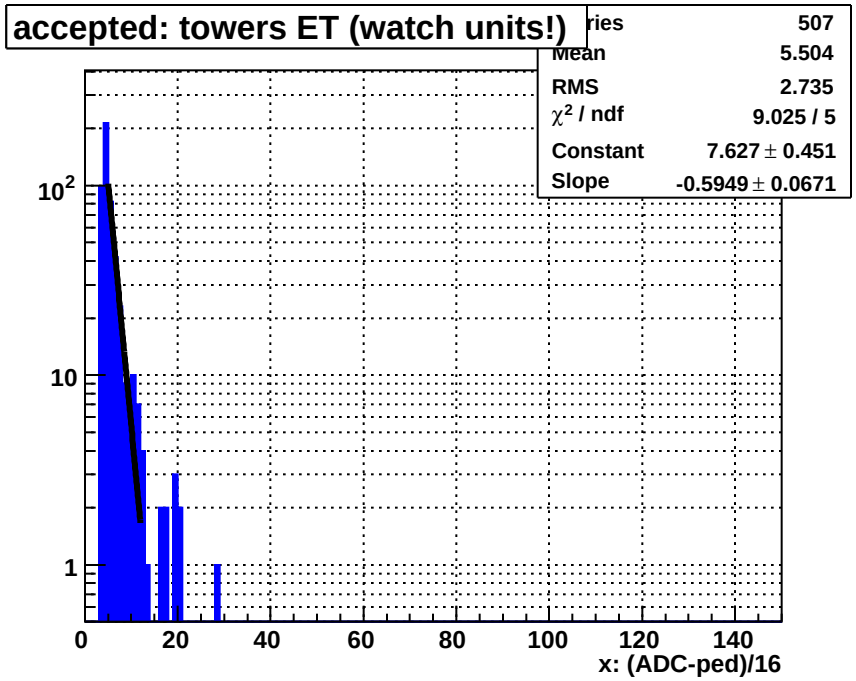

rate of DECISION
Entries 6987
Mean 39.95
RMS 7.312
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

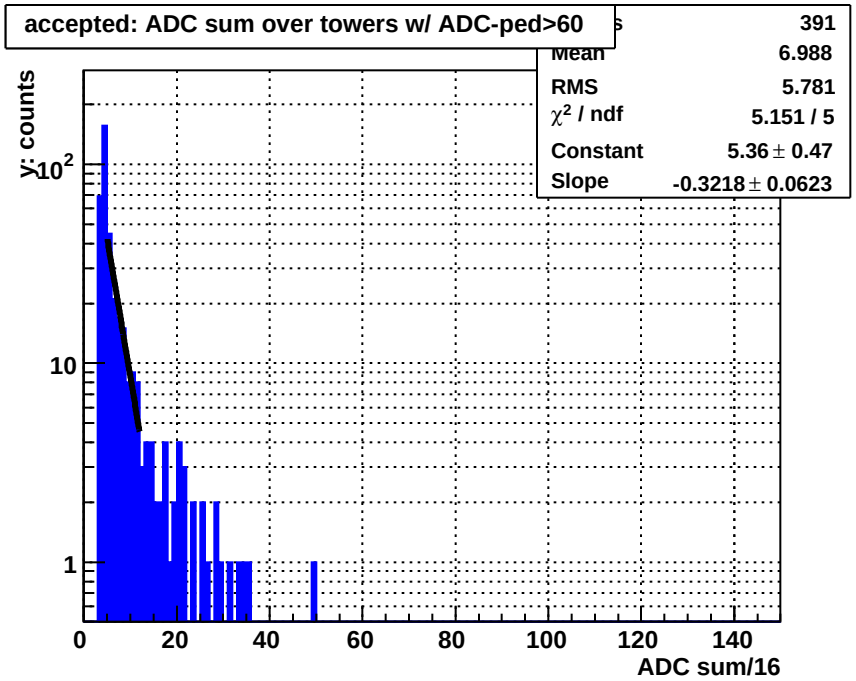

compute: # towers w/ ADC-ped>60 / event
Entries 18499
Mean 0.6161
RMS 0.4584
Underflow 0
Overflow 0
y: counts
x: # towers

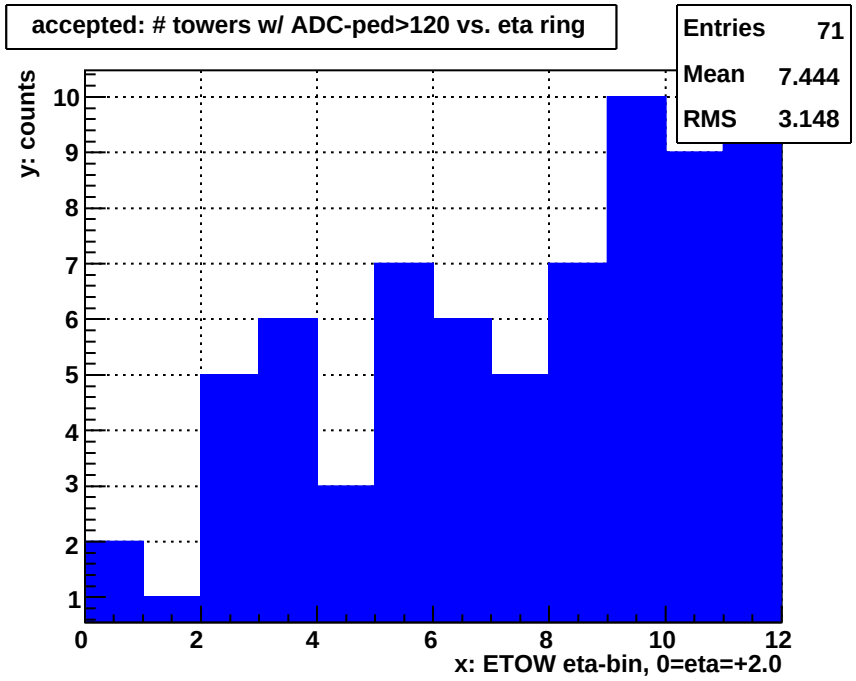

L2 COMP+DECI time per input event
Entries 6987
Mean 2.074
RMS 0.8036
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

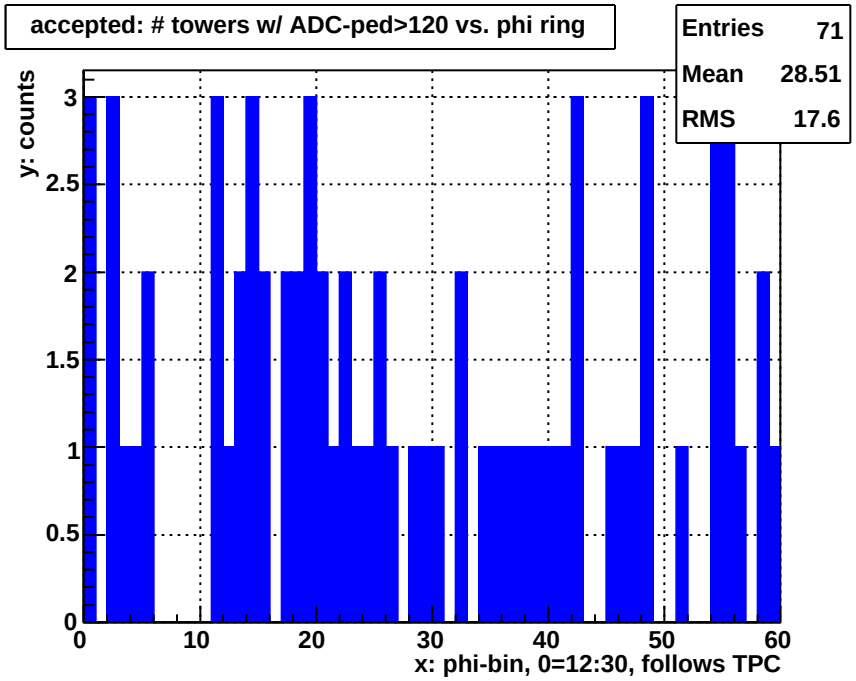

rate of ACCEPT
Entries 6987
Mean 39.95
RMS 7.312
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 6987
Mean 0.5726
RMS 0.343
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 507
Mean 5.504
RMS 2.735
χ² / ndf 9.025 / 5
Constant 7.627 ± 0.451
Slope -0.5949 ± 0.0671
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 391
Mean 6.988
RMS 5.781
χ² / ndf 5.151 / 5
Constant 5.36 ± 0.47
Slope -0.3218 ± 0.0623
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 71
Mean 7.444
RMS 3.148
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 71
Mean 28.51
RMS 17.6
y: counts
x: phi-bin, 0=12:30, follows TPC