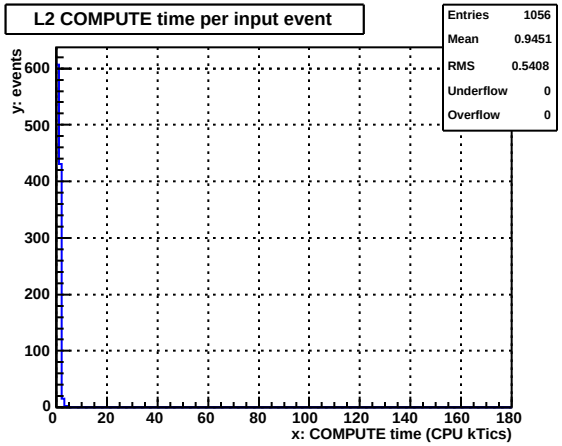
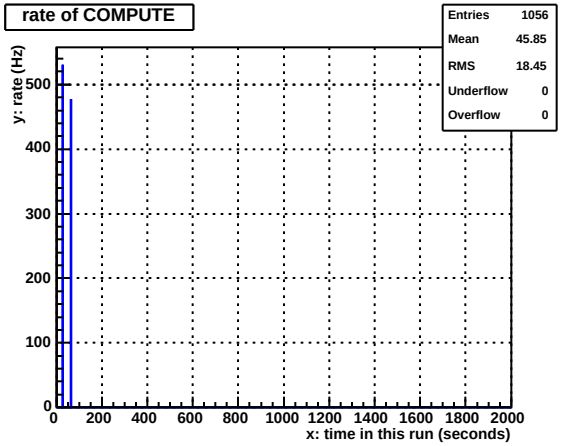
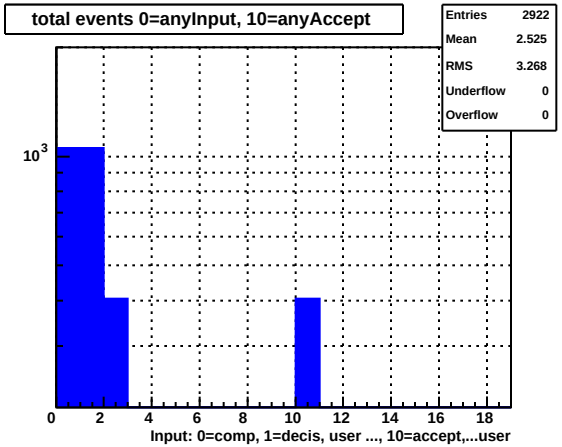
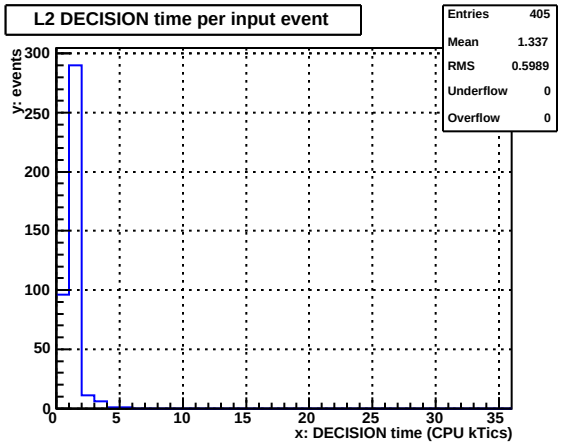
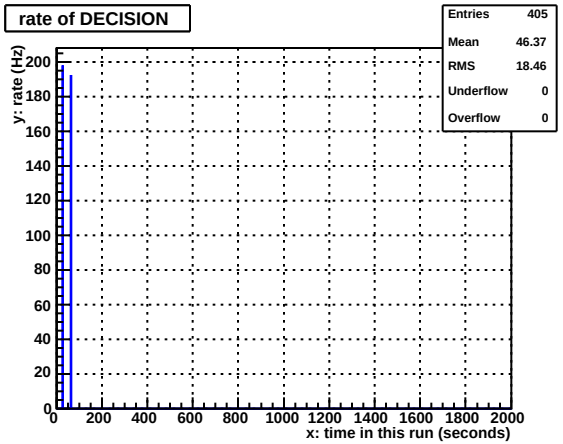
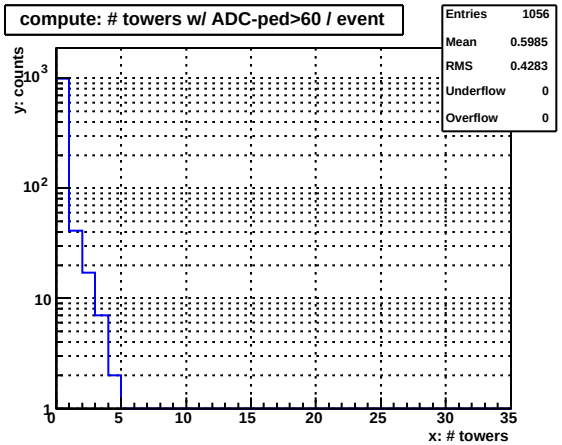

L2 COMPUTE time per input event
Entries 1056
Mean 0.9451
RMS 0.5408
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

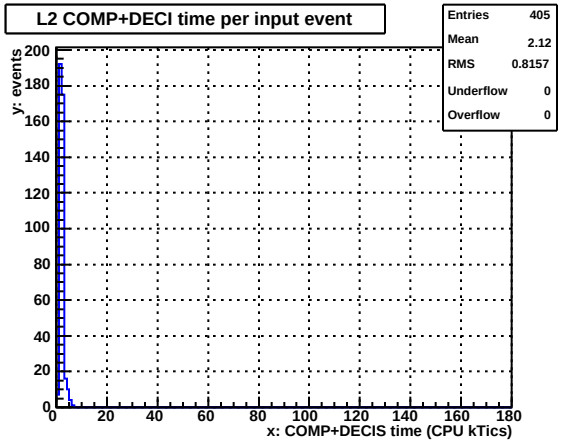

rate of COMPUTE
Entries 1056
Mean 45.85
RMS 18.45
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

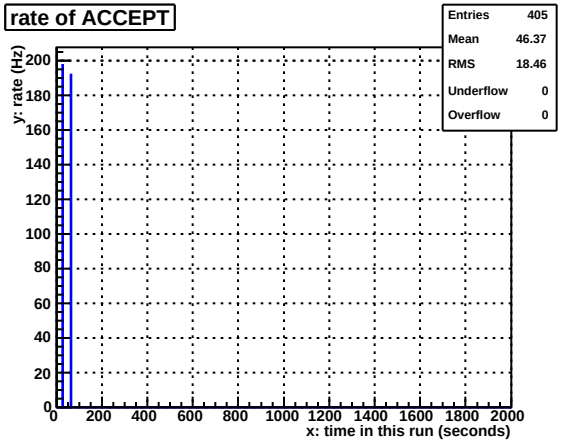

total events 0=anyInput, 10=anyAccept
Entries 2922
Mean 2.525
RMS 3.268
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

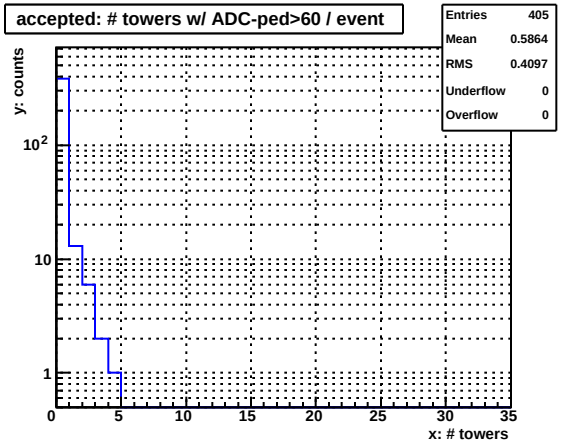

L2 DECISION time per input event
Entries 405
Mean 1.337
RMS 0.5989
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

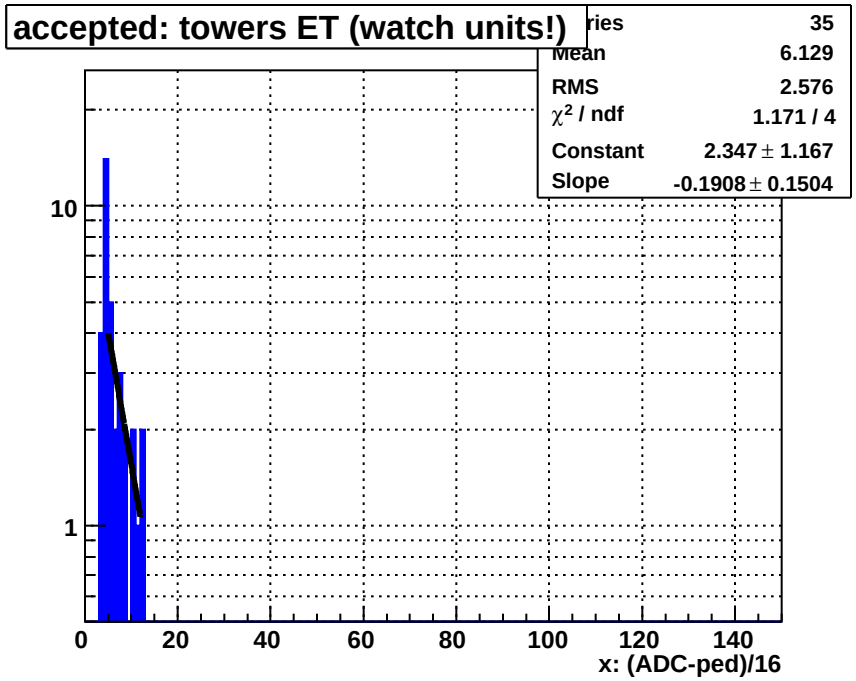

rate of DECISION
Entries 405
Mean 46.37
RMS 18.46
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

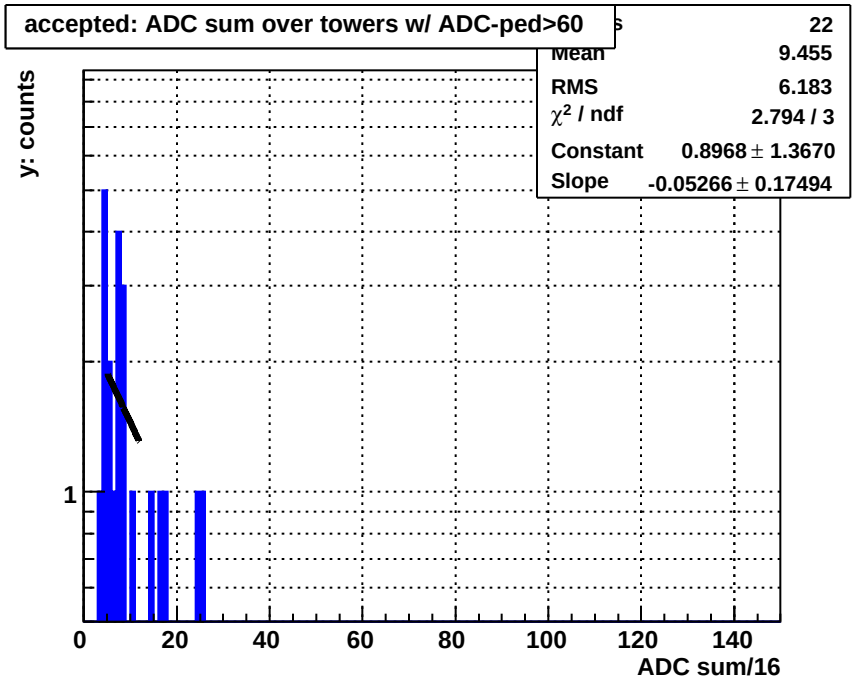

compute: # towers w/ ADC-ped>60 / event
Entries 1056
Mean 0.5985
RMS 0.4283
Underflow 0
Overflow 0
y: counts
x: # towers

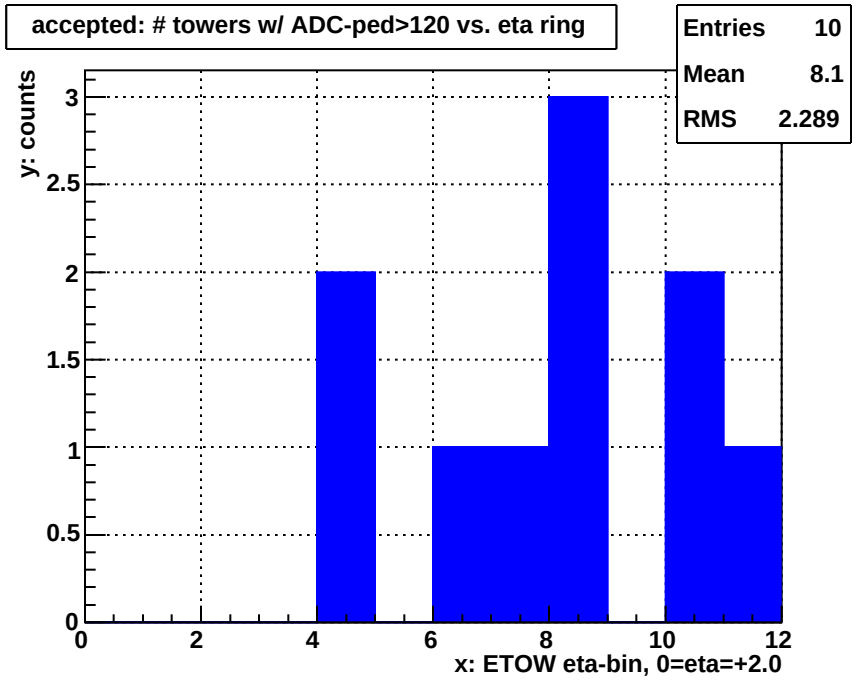

L2 COMP+DECI time per input event
Entries 405
Mean 2.12
RMS 0.8157
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

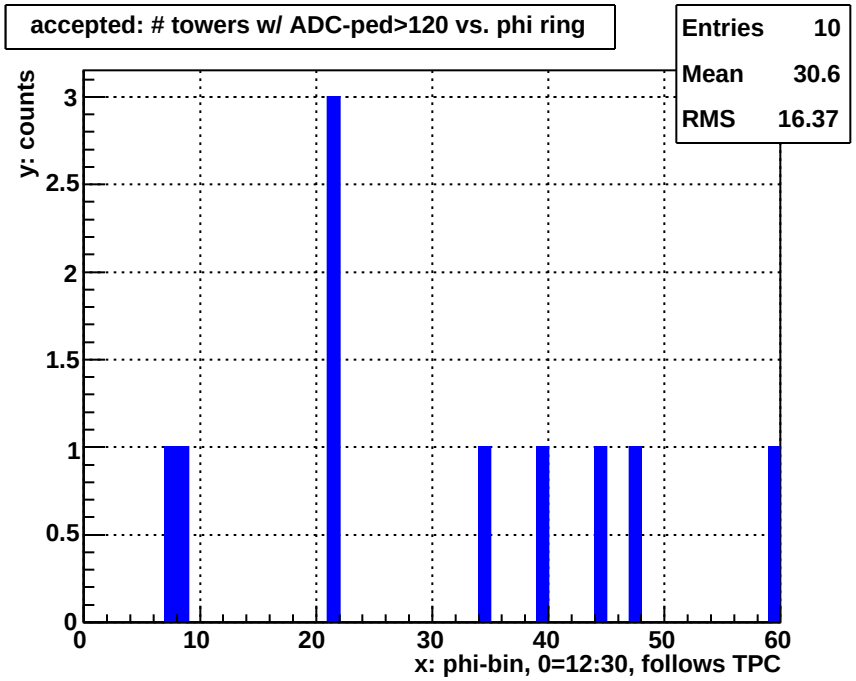

rate of ACCEPT
Entries 405
Mean 46.37
RMS 18.46
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 405
Mean 0.5864
RMS 0.4097
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 35
Mean 6.129
RMS 2.576
χ² / ndf 1.171 / 4
Constant 2.347 ± 1.167
Slope -0.1908 ± 0.1504
10
1
0 20 40 60 80 100 120 140
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 22
Mean 9.455
RMS 6.183
χ² / ndf 2.794 / 3
Constant 0.8968 ± 1.3670
Slope -0.05266 ± 0.17494
y: counts
1
0 20 40 60 80 100 120 140
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 10
Mean 8.1
RMS 2.289
y: counts
0 0.5 1 1.5 2 2.5 3
0 2 4 6 8 10 12
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 10
Mean 30.6
RMS 16.37
y: counts
0 0.5 1 1.5 2 2.5 3
0 10 20 30 40 50 60
x: phi-bin, 0=12:30, follows TPC