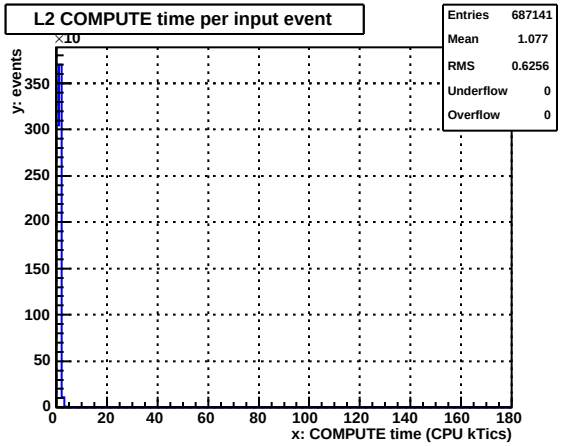
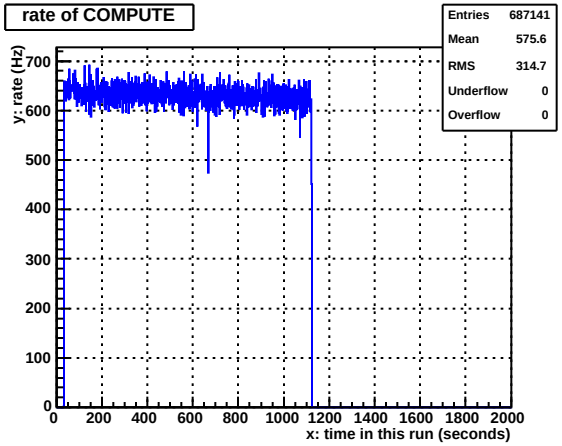
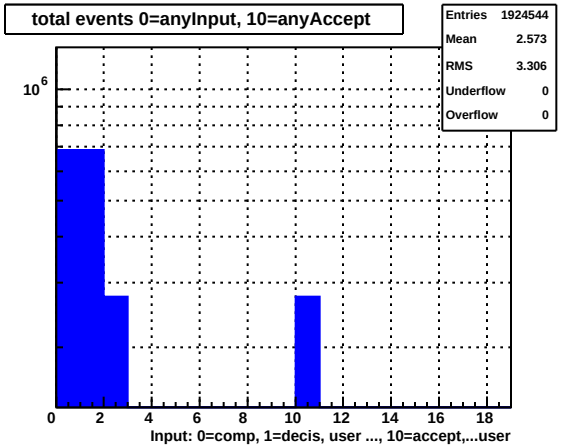
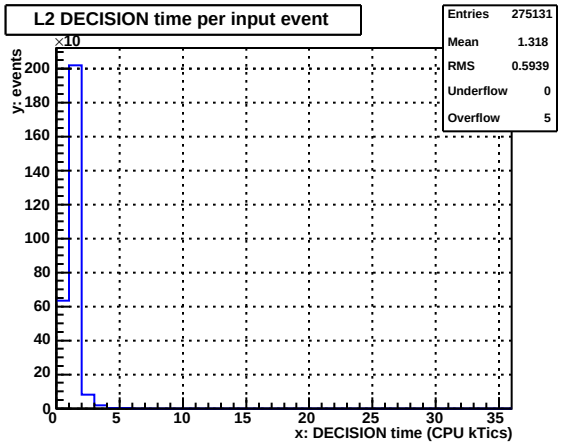
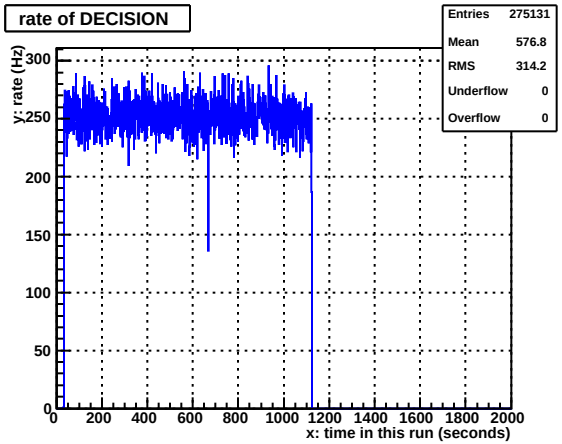
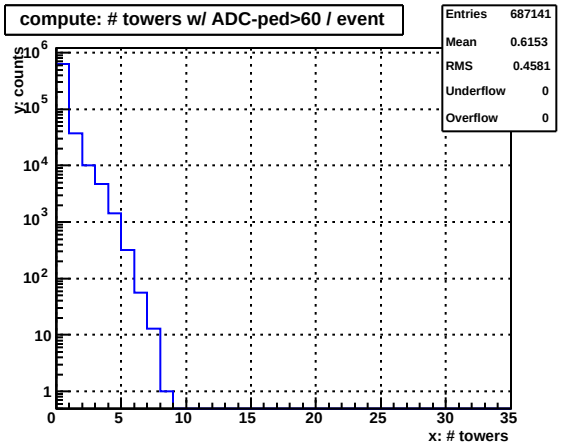

L2 COMPUTE time per input event
Entries 687141
Mean 1.077
RMS 0.6256
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

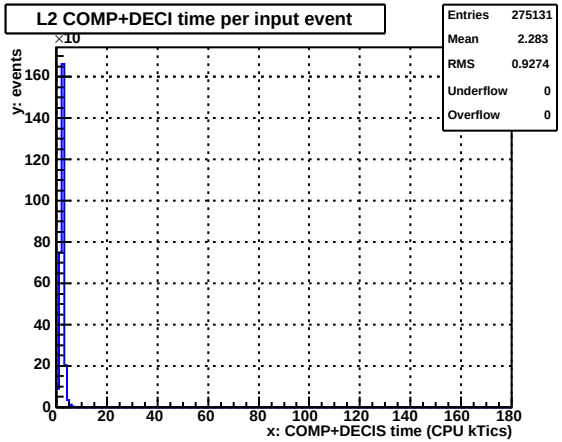

rate of COMPUTE
Entries 687141
Mean 575.6
RMS 314.7
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

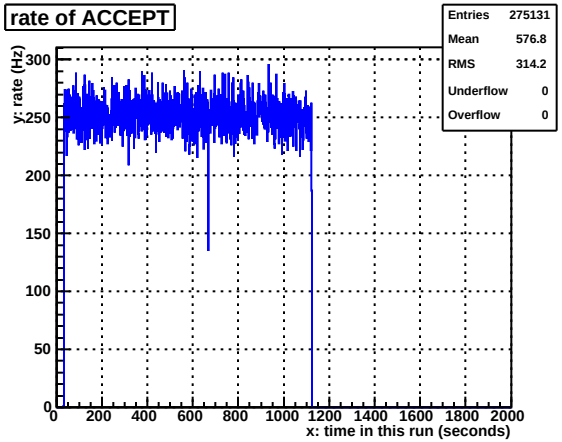

total events 0=anyInput, 10=anyAccept
Entries 1924544
Mean 2.573
RMS 3.306
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

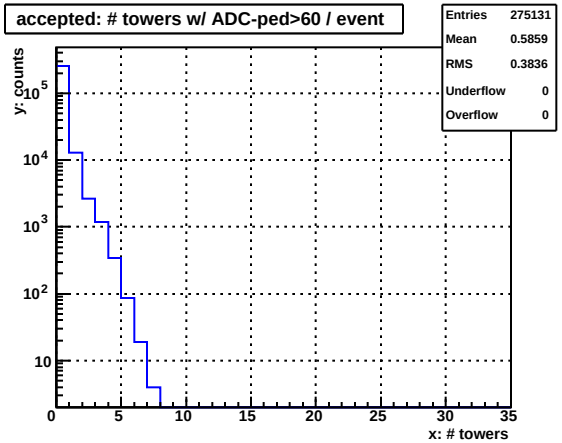

L2 DECISION time per input event
Entries 275131
Mean 1.318
RMS 0.5939
Underflow 0
Overflow 5
y: events
x: DECISION time (CPU kTics)

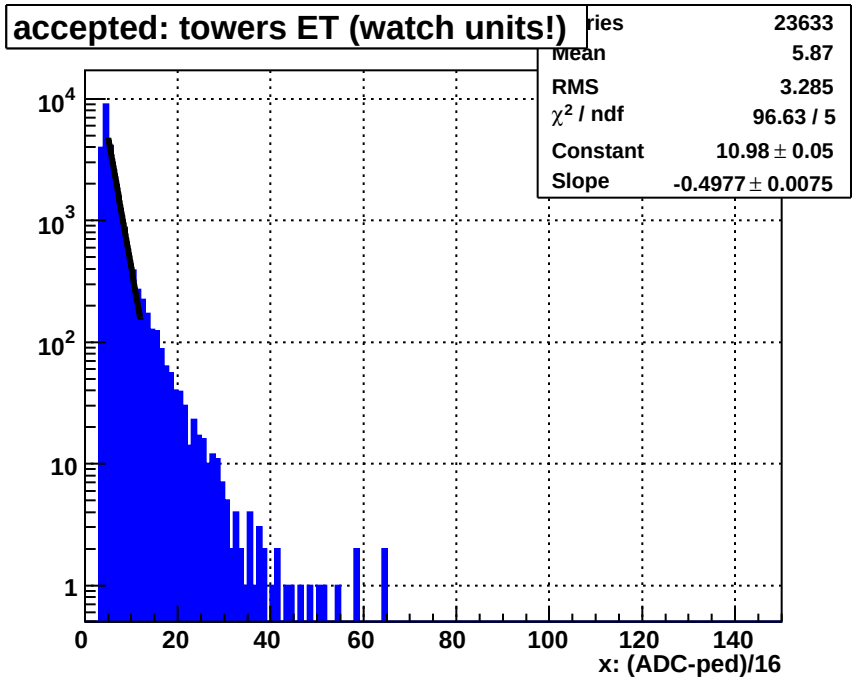

rate of DECISION
Entries 275131
Mean 576.8
RMS 314.2
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

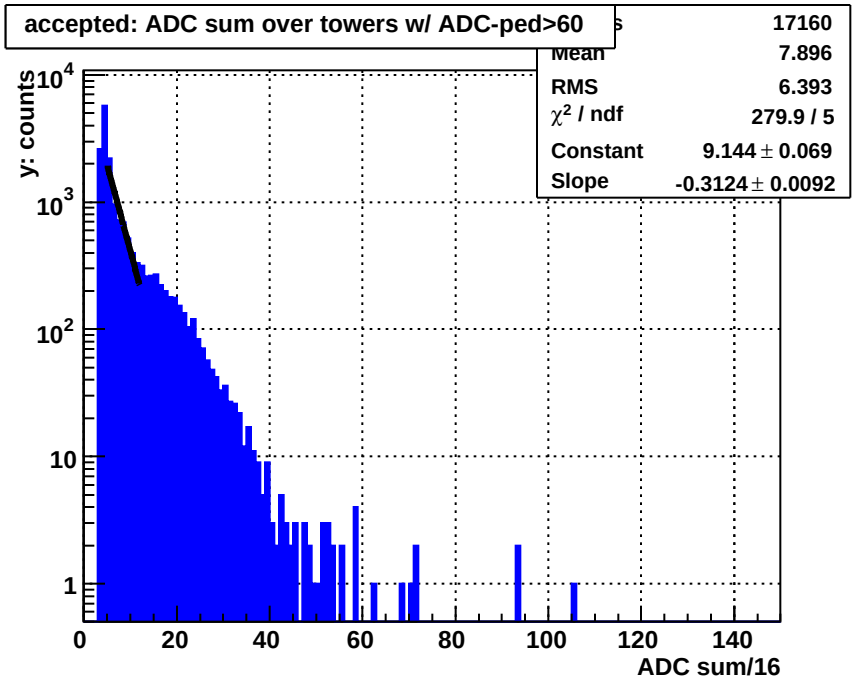

compute: # towers w/ ADC-ped>60 / event
Entries 687141
Mean 0.6153
RMS 0.4581
Underflow 0
Overflow 0
y: counts
x: # towers

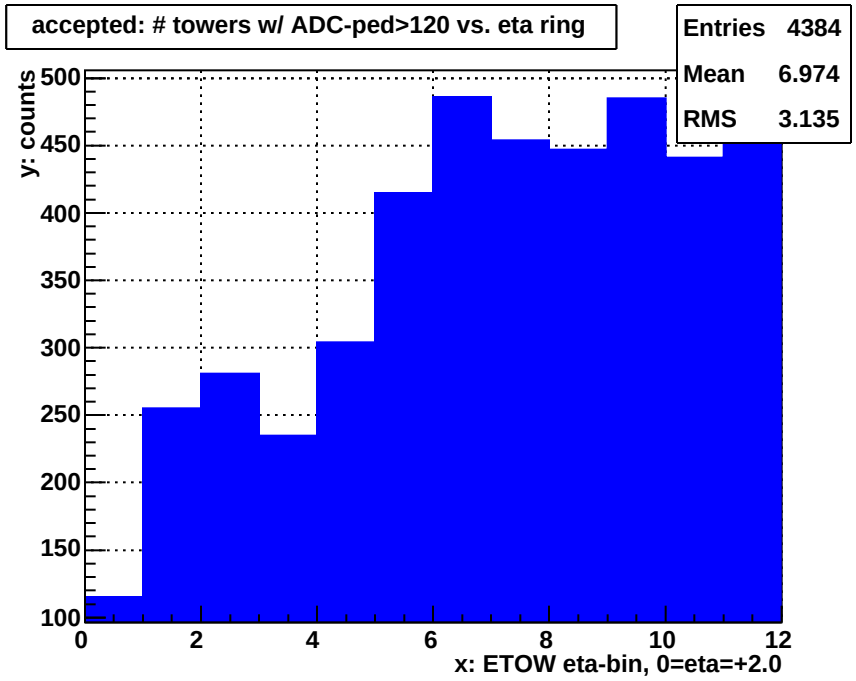

L2 COMP+DECI time per input event
Entries 275131
Mean 2.283
RMS 0.9274
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

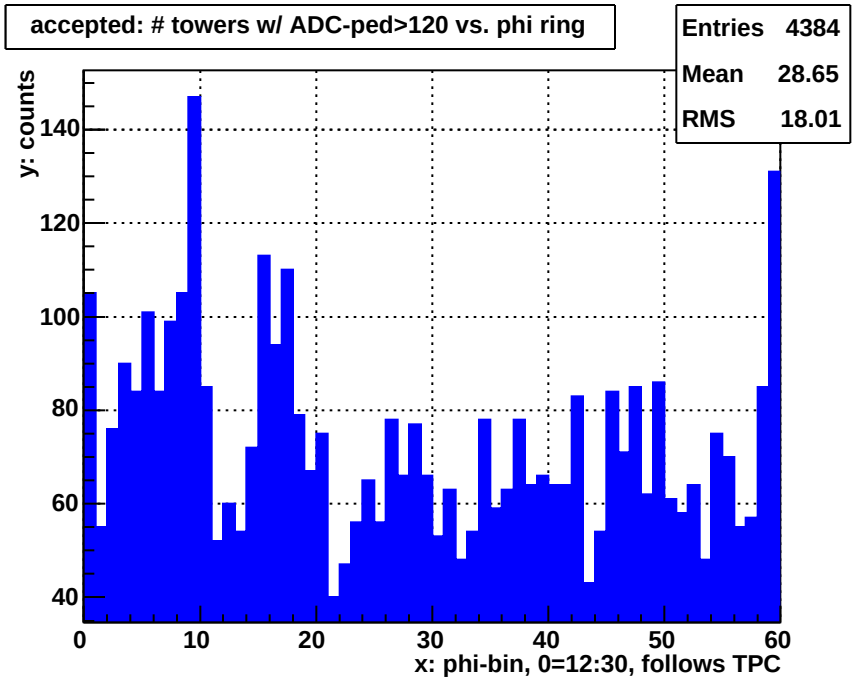

rate of ACCEPT
Entries 275131
Mean 576.8
RMS 314.2
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 275131
Mean 0.5859
RMS 0.3836
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
Entries 23633
Mean 5.87
RMS 3.285
χ² / ndf 96.63 / 5
Constant 10.98 ± 0.05
Slope -0.4977 ± 0.0075
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
Entries 17160
Mean 7.896
RMS 6.393
χ² / ndf 279.9 / 5
Constant 9.144 ± 0.069
Slope -0.3124 ± 0.0092
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 4384
Mean 6.974
RMS 3.135
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 4384
Mean 28.65
RMS 18.01
y: counts
x: phi-bin, 0=12:30, follows TPC