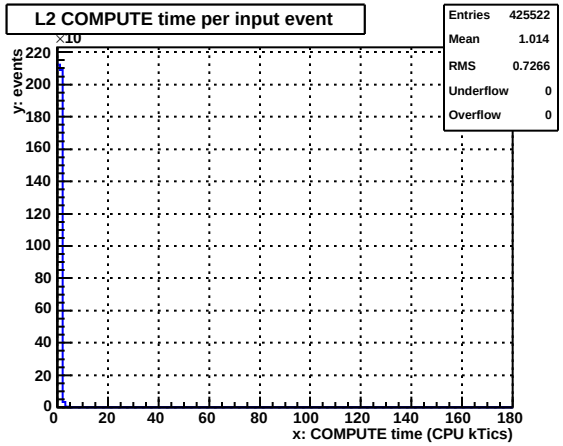
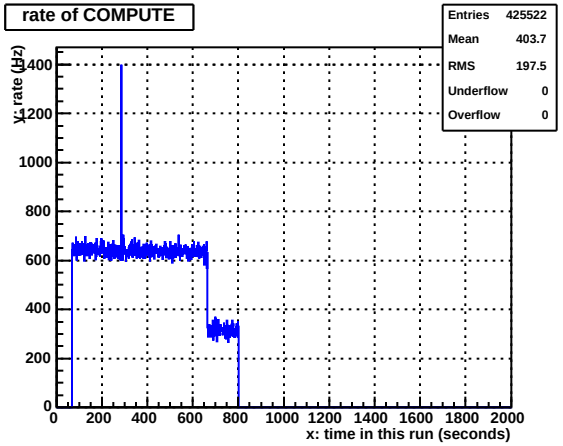
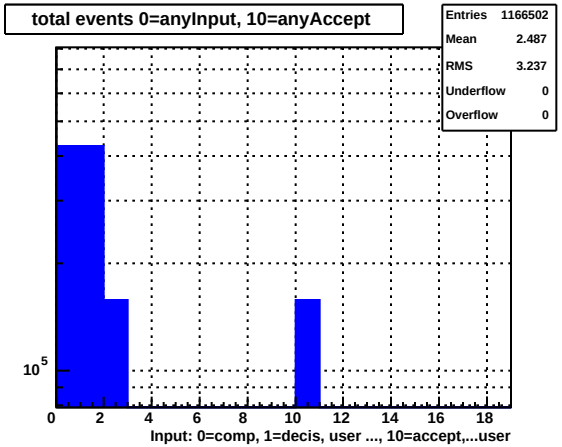
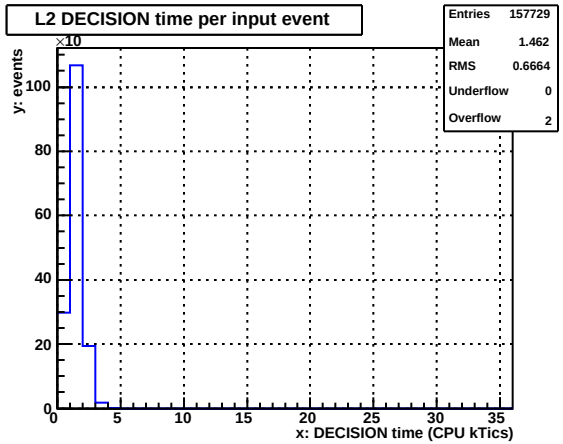
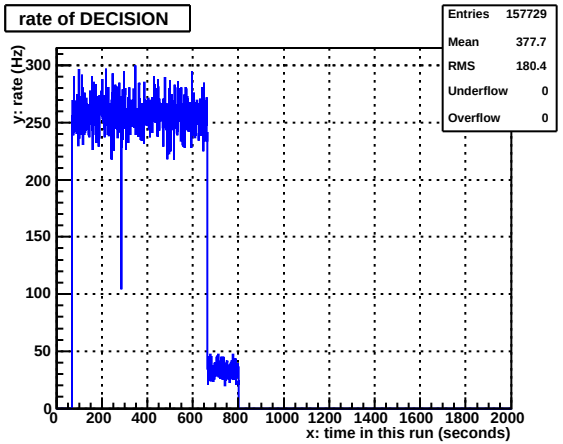
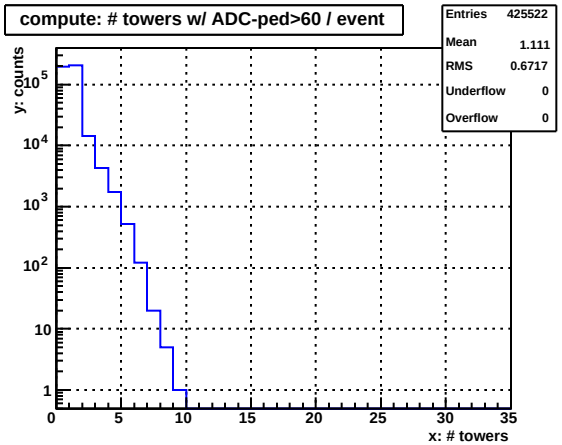

L2 COMPUTE time per input event
Entries 425522
Mean 1.014
RMS 0.7266
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

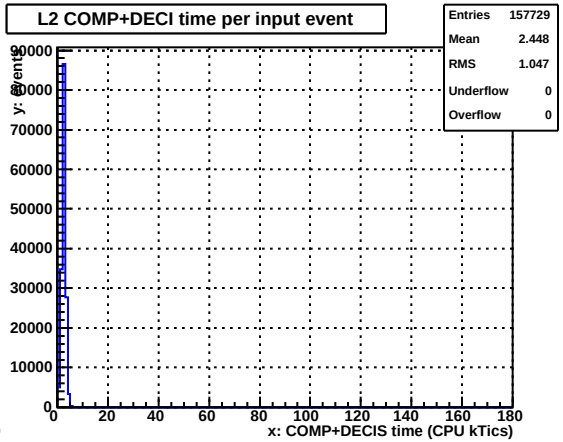

rate of COMPUTE
Entries 425522
Mean 403.7
RMS 197.5
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

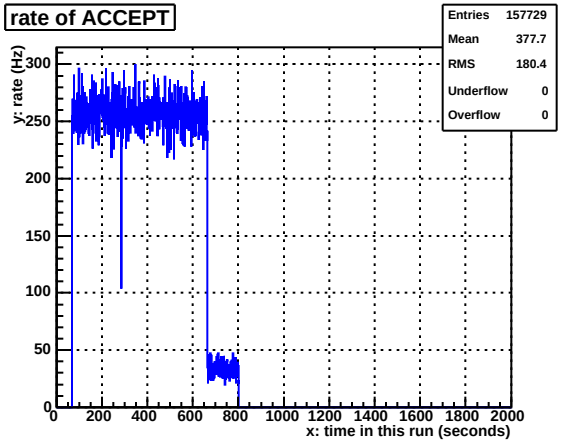

total events 0=anyInput, 10=anyAccept
Entries 1166502
Mean 2.487
RMS 3.237
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

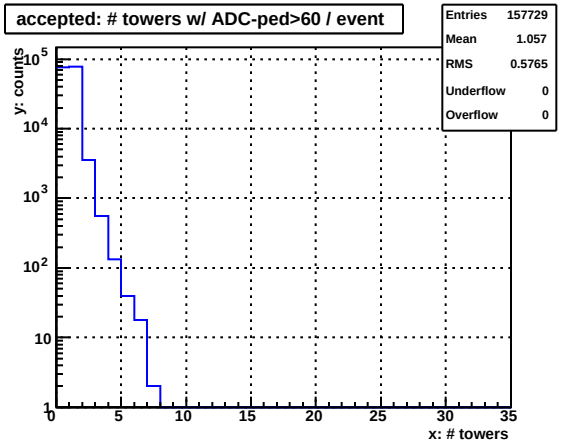

L2 DECISION time per input event
Entries 157729
Mean 1.462
RMS 0.6664
Underflow 0
Overflow 2
y: events
x: DECISION time (CPU kTics)

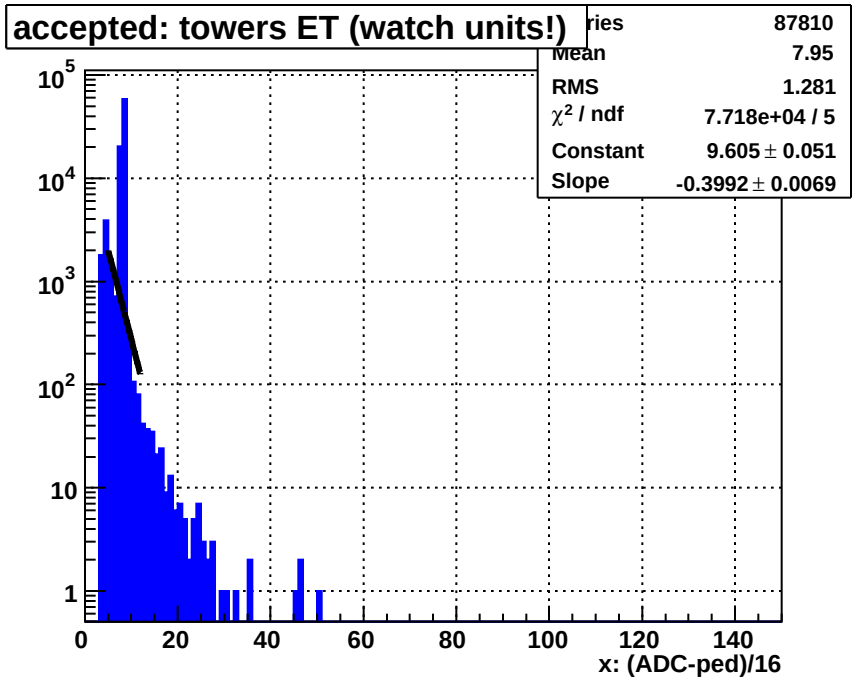

rate of DECISION
Entries 157729
Mean 377.7
RMS 180.4
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

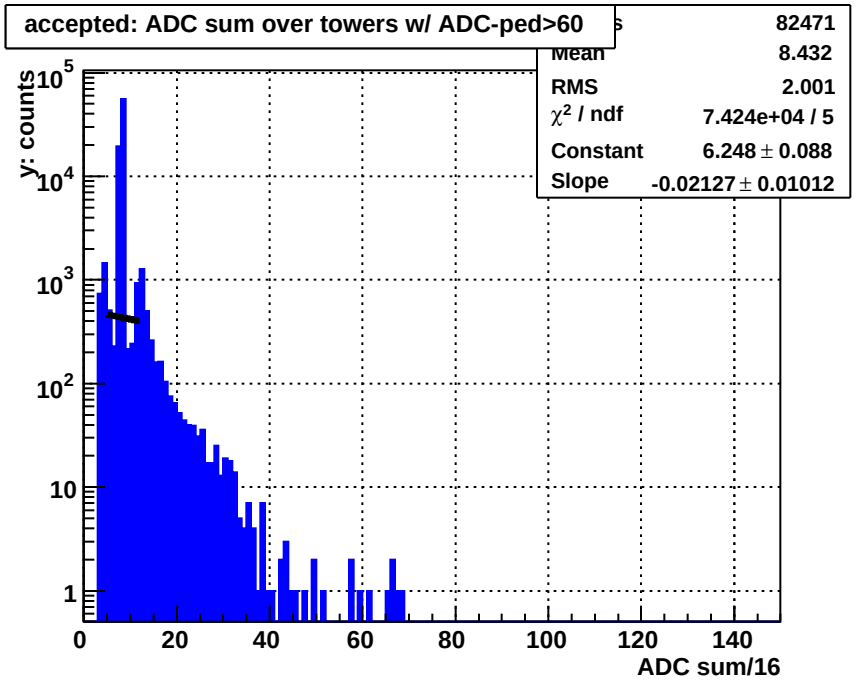

compute: # towers w/ ADC-ped>60 / event
Entries 425522
Mean 1.111
RMS 0.6717
Underflow 0
Overflow 0
y: counts
x: # towers

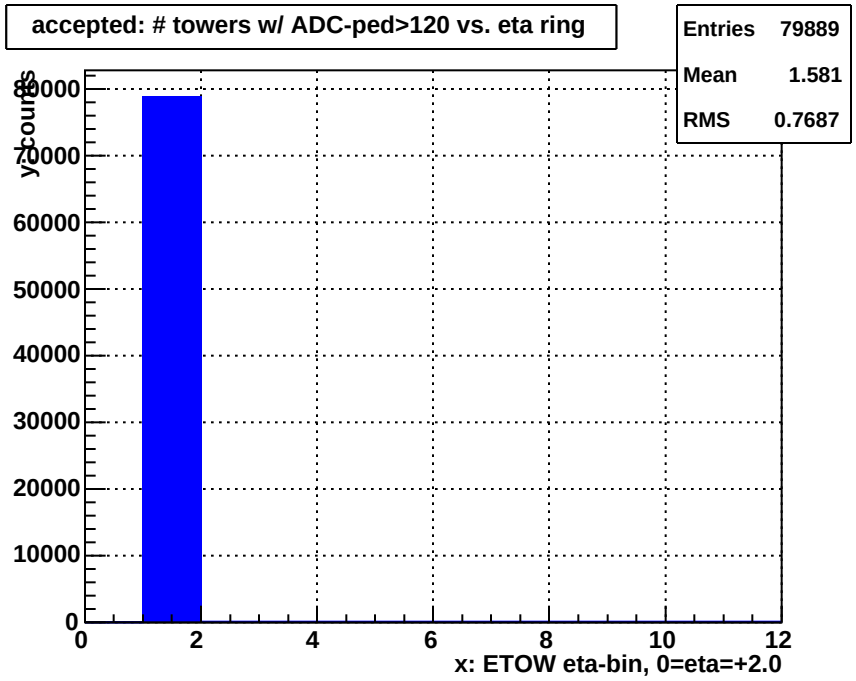

L2 COMP+DECI time per input event
Entries 157729
Mean 2.448
RMS 1.047
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

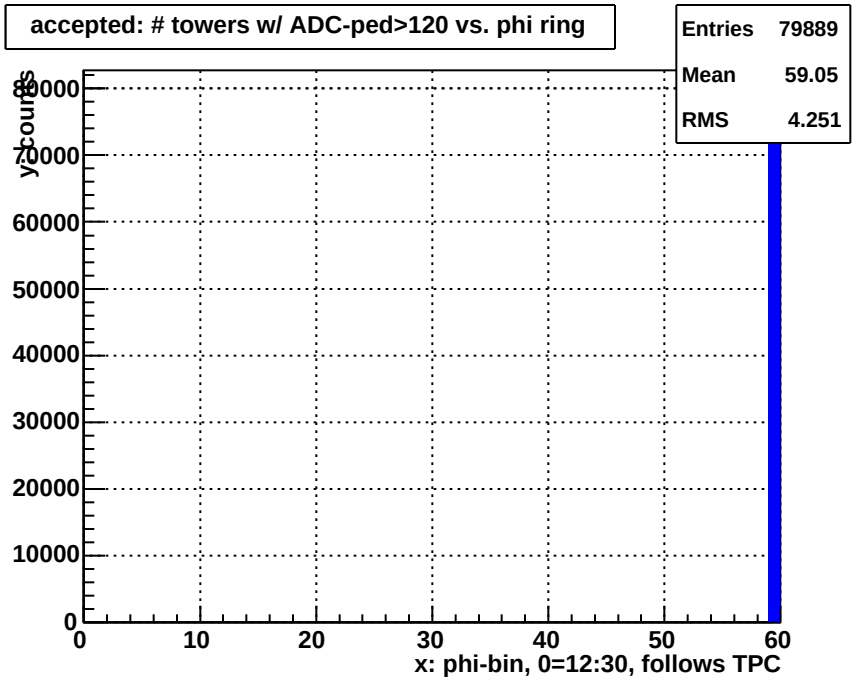

rate of ACCEPT
Entries 157729
Mean 377.7
RMS 180.4
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 157729
Mean 1.057
RMS 0.5765
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 87810
Mean 7.95
RMS 1.281
χ² / ndf 7.718e+04 / 5
Constant 9.605 ± 0.051
Slope -0.3992 ± 0.0069
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 82471
Mean 8.432
RMS 2.001
χ² / ndf 7.424e+04 / 5
Constant 6.248 ± 0.088
Slope -0.02127 ± 0.01012
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 79889
Mean 1.581
RMS 0.7687
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 79889
Mean 59.05
RMS 4.251
y: counts
x: phi-bin, 0=12:30, follows TPC