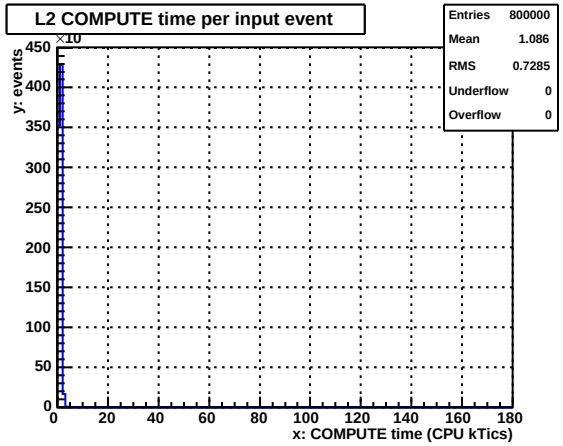
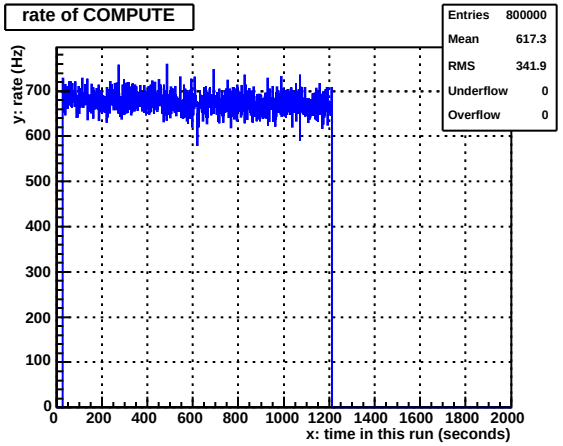
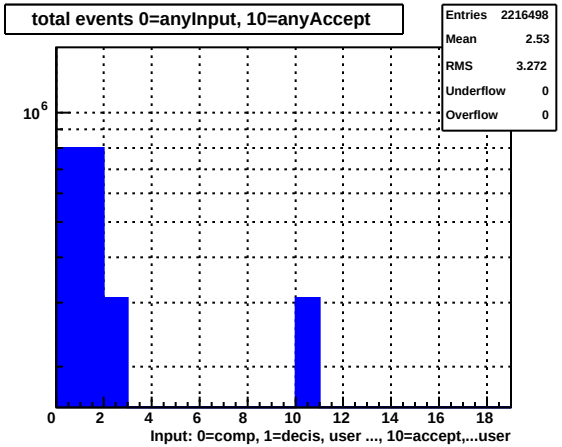
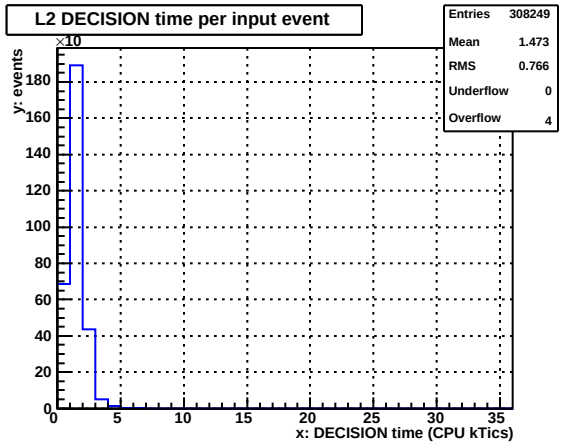
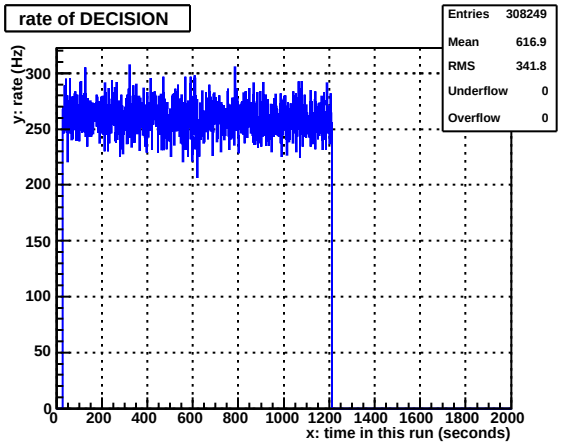
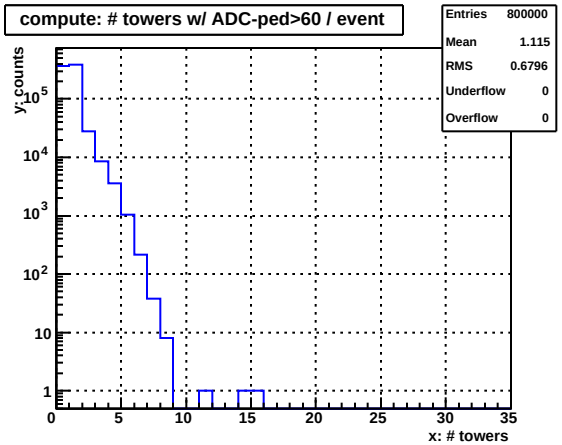

L2 COMPUTE time per input event
Entries 800000
Mean 1.086
RMS 0.7285
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

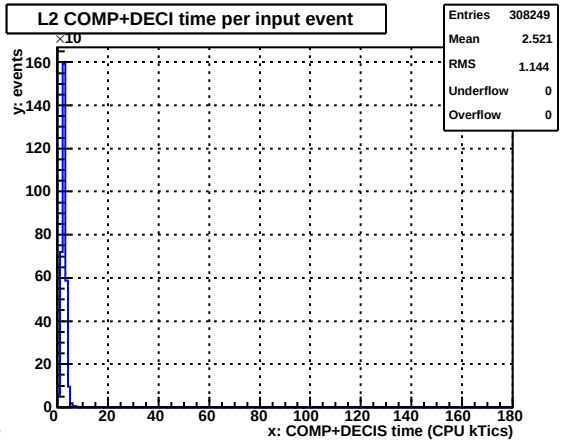

rate of COMPUTE
Entries 800000
Mean 617.3
RMS 341.9
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

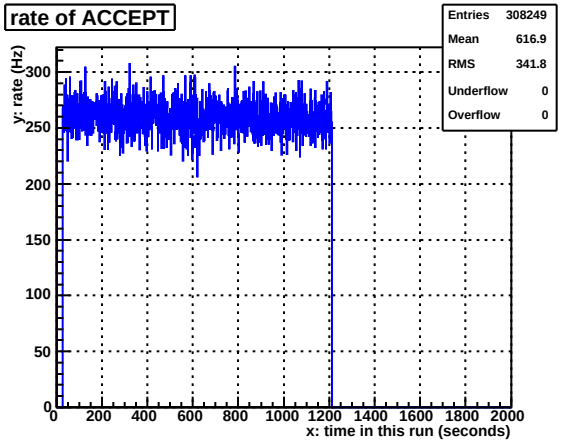

total events 0=anyInput, 10=anyAccept
Entries 2216498
Mean 2.53
RMS 3.272
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

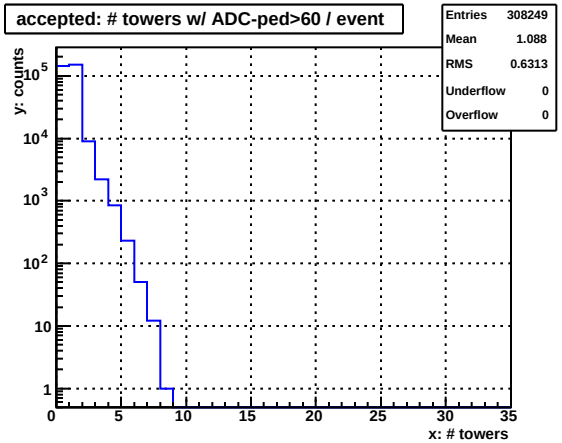

L2 DECISION time per input event
Entries 308249
Mean 1.473
RMS 0.766
Underflow 0
Overflow 4
y: events
x: DECISION time (CPU kTics)

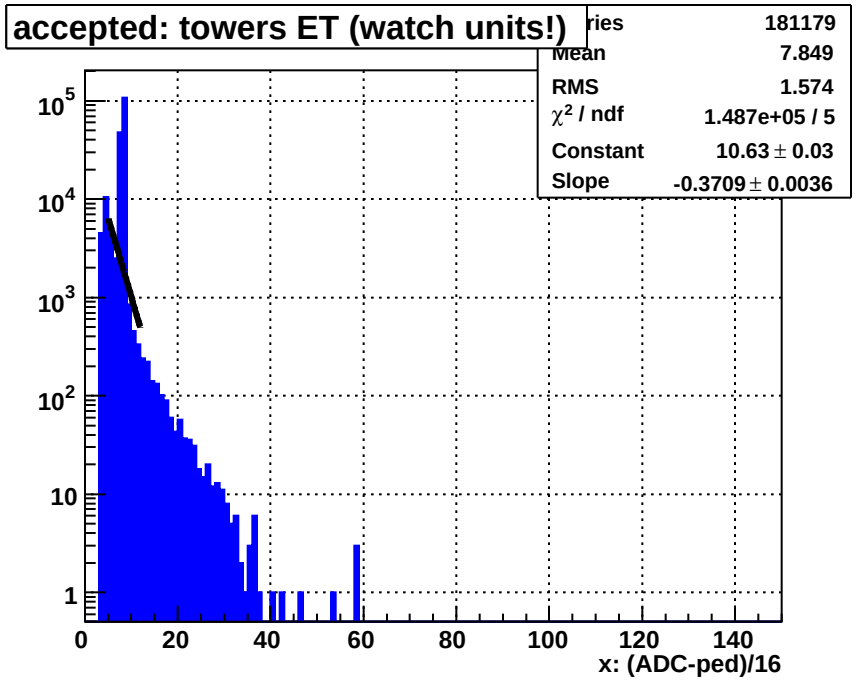

rate of DECISION
Entries 308249
Mean 616.9
RMS 341.8
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

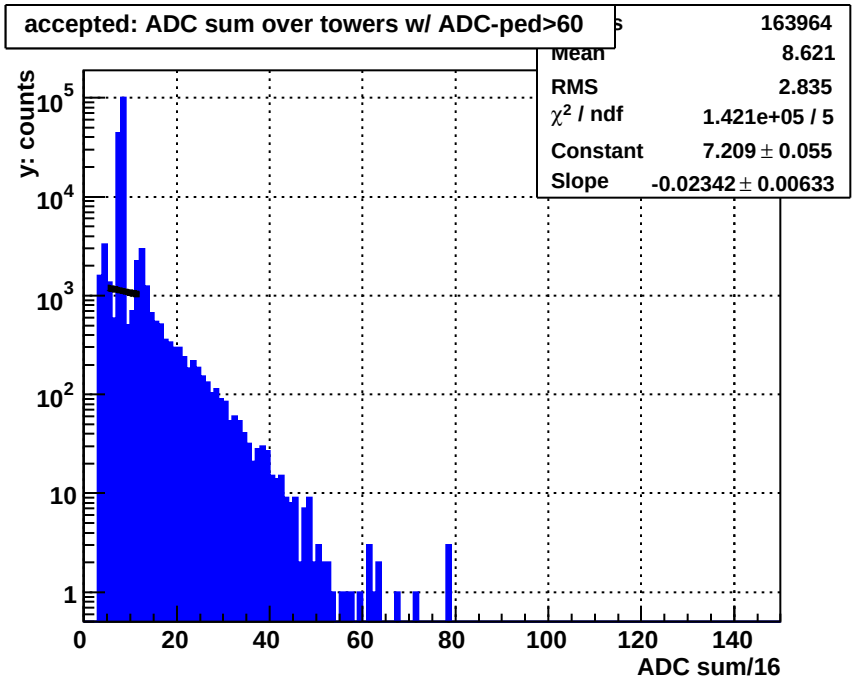

compute: # towers w/ ADC-ped>60 / event
Entries 800000
Mean 1.115
RMS 0.6796
Underflow 0
Overflow 0
y: counts
x: # towers

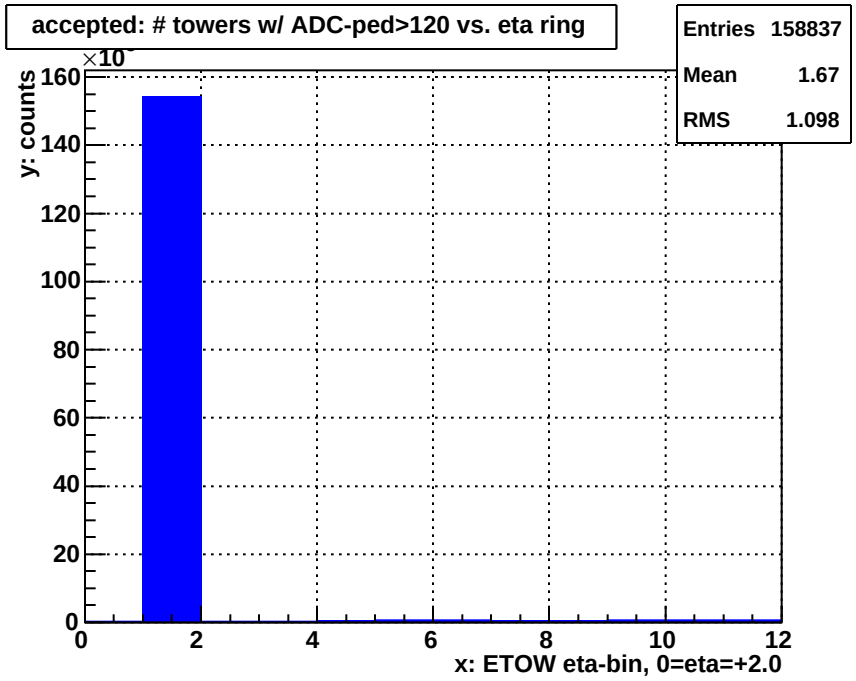

L2 COMP+DECI time per input event
Entries 308249
Mean 2.521
RMS 1.144
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

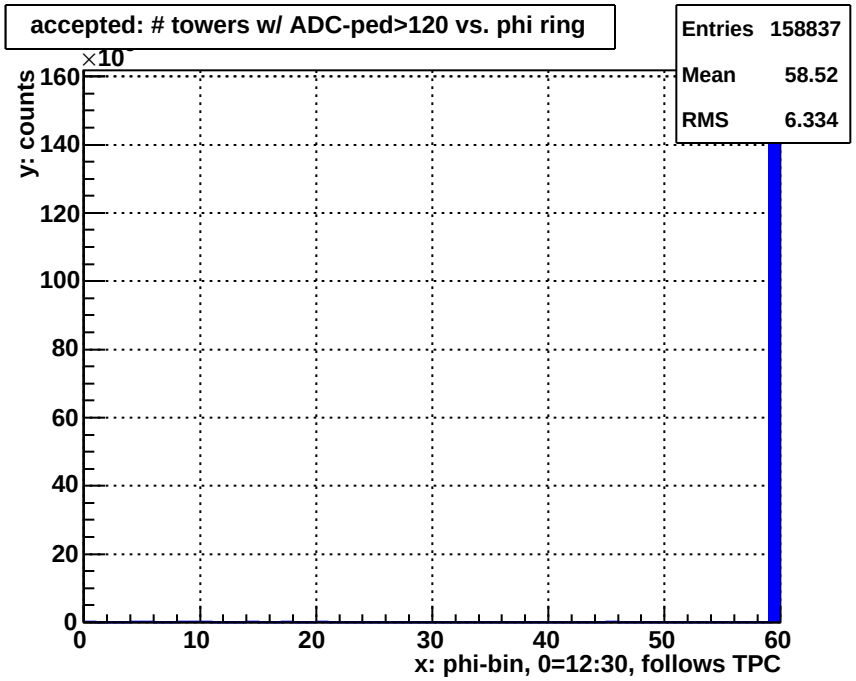

rate of ACCEPT
Entries 308249
Mean 616.9
RMS 341.8
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 308249
Mean 1.088
RMS 0.6313
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 181179
Mean 7.849
RMS 1.574
χ² / ndf 1.487e+05 / 5
Constant 10.63 ± 0.03
Slope -0.3709 ± 0.0036
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 163964
Mean 8.621
RMS 2.835
χ² / ndf 1.421e+05 / 5
Constant 7.209 ± 0.055
Slope -0.02342 ± 0.00633
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 158837
Mean 1.67
RMS 1.098
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 158837
Mean 58.52
RMS 6.334
y: counts
x: phi-bin, 0=12:30, follows TPC