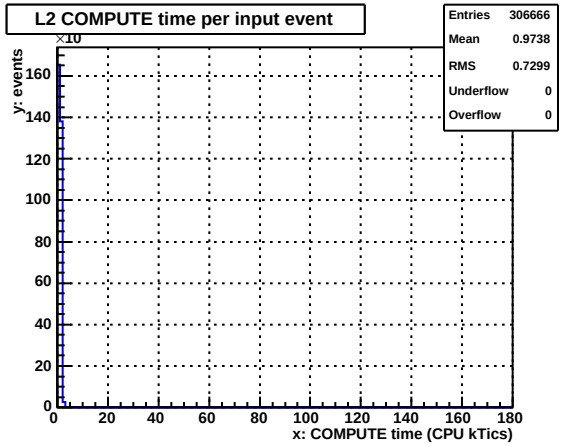
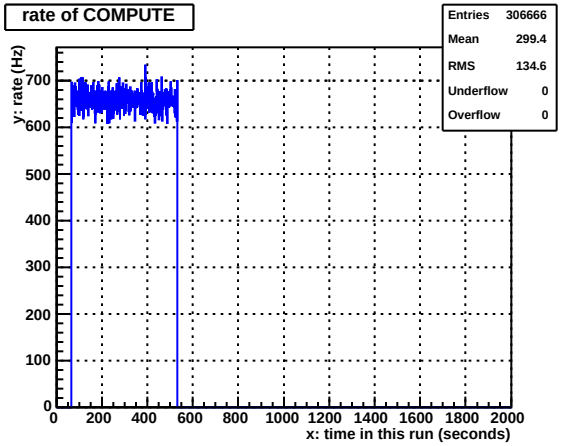
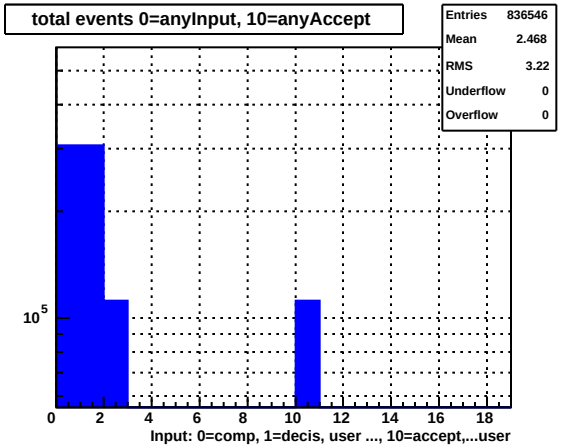
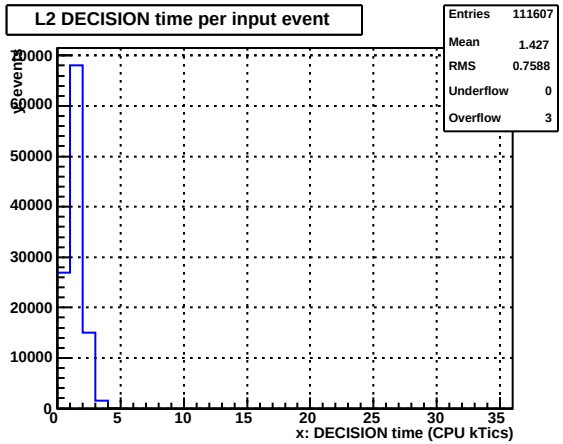
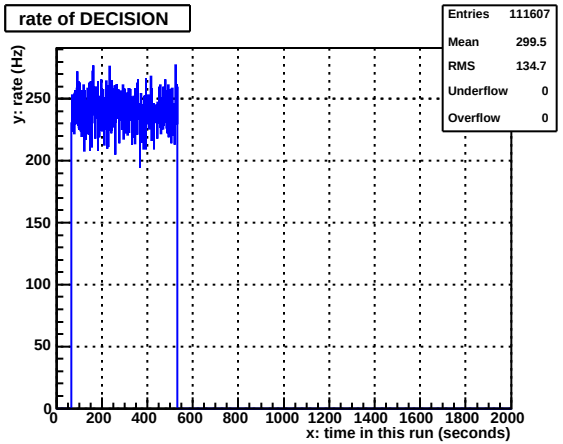
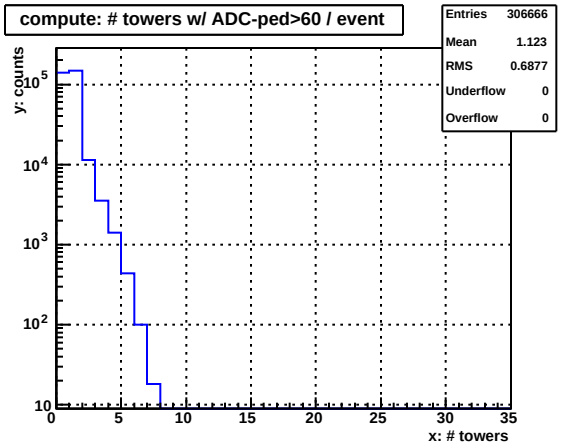

L2 COMPUTE time per input event
Entries 306666
Mean 0.9738
RMS 0.7299
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

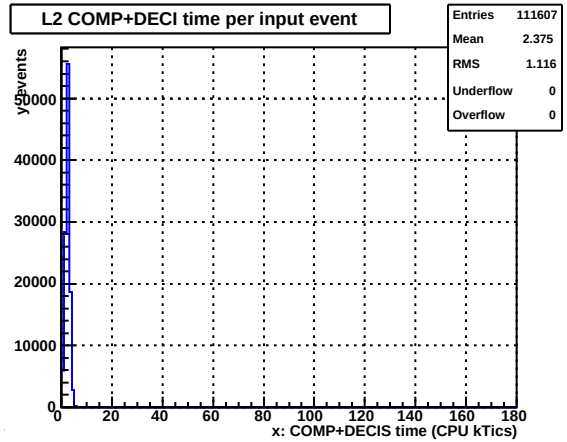

rate of COMPUTE
Entries 306666
Mean 299.4
RMS 134.6
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

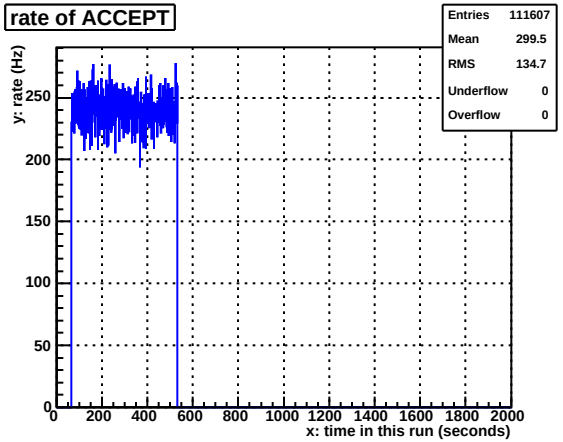

total events 0=anyInput, 10=anyAccept
Entries 836546
Mean 2.468
RMS 3.22
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

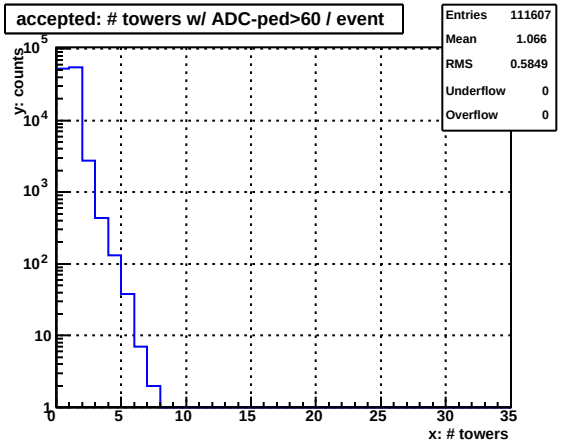

L2 DECISION time per input event
Entries 111607
Mean 1.427
RMS 0.7588
Underflow 0
Overflow 3
y: events
x: DECISION time (CPU kTics)

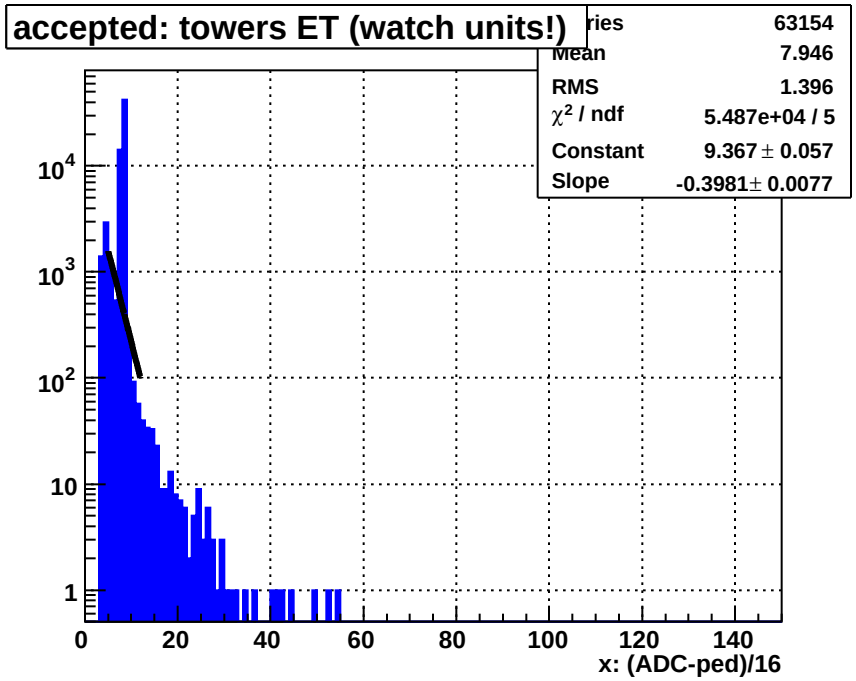

rate of DECISION
Entries 111607
Mean 299.5
RMS 134.7
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

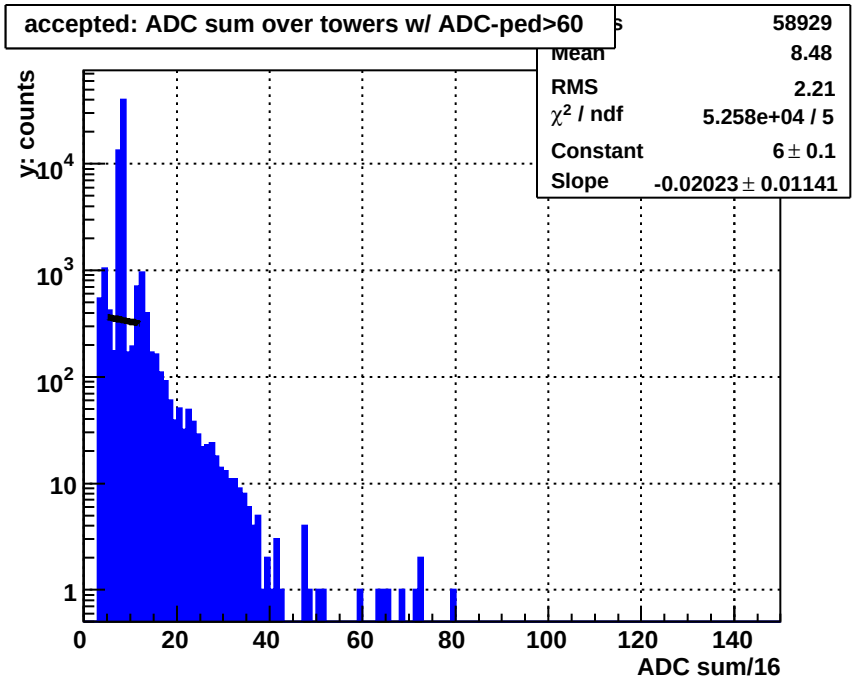

compute: # towers w/ ADC-ped>60 / event
Entries 306666
Mean 1.123
RMS 0.6877
Underflow 0
Overflow 0
y: counts
x: # towers

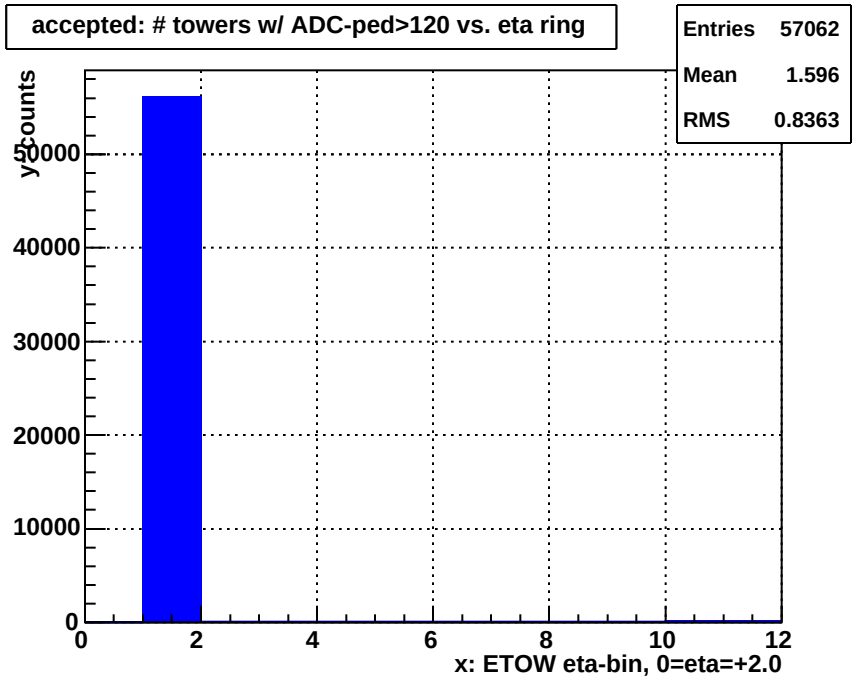

L2 COMP+DECI time per input event
Entries 111607
Mean 2.375
RMS 1.116
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

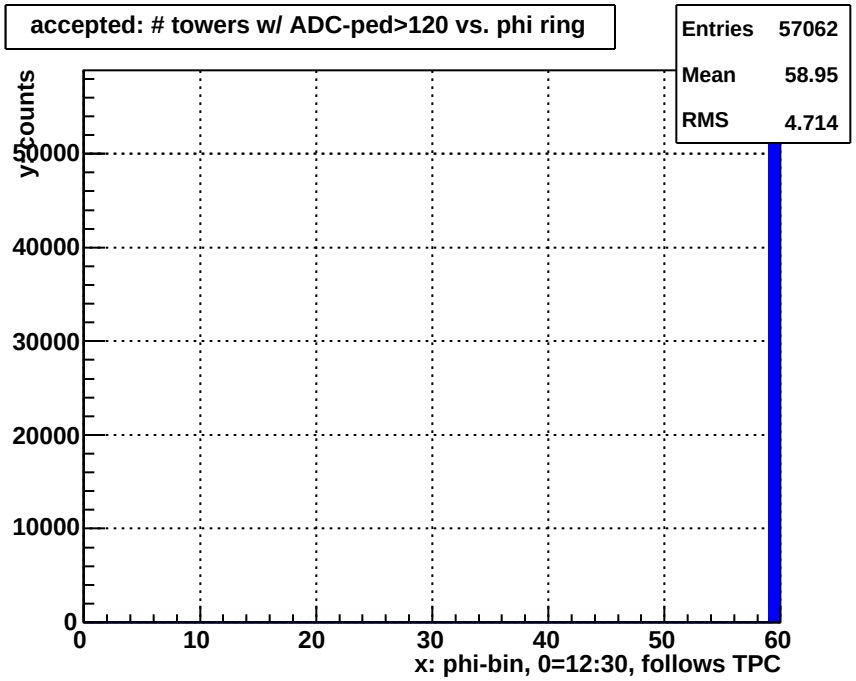

rate of ACCEPT
Entries 111607
Mean 299.5
RMS 134.7
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 111607
Mean 1.066
RMS 0.5849
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 63154
Mean 7.946
RMS 1.396
χ² / ndf 5.487e+04 / 5
Constant 9.367 ± 0.057
Slope -0.3981 ± 0.0077
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 58929
Mean 8.48
RMS 2.21
χ² / ndf 5.258e+04 / 5
Constant 6 ± 0.1
Slope -0.02023 ± 0.01141
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 57062
Mean 1.596
RMS 0.8363
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 57062
Mean 58.95
RMS 4.714
y: counts
x: phi-bin, 0=12:30, follows TPC