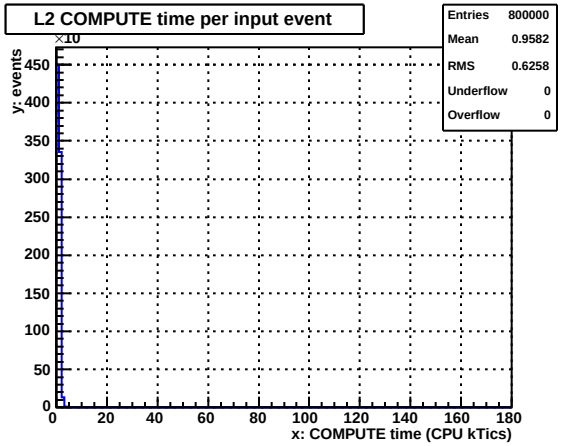
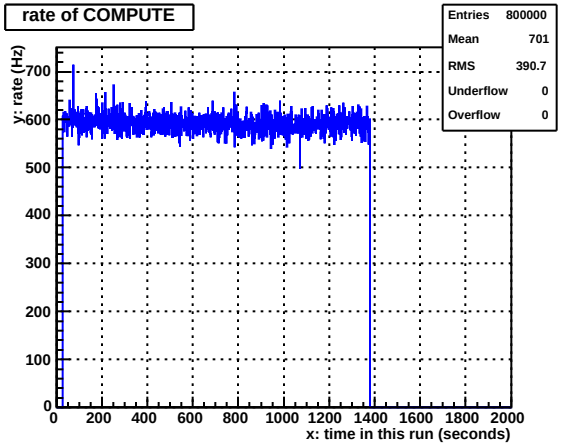
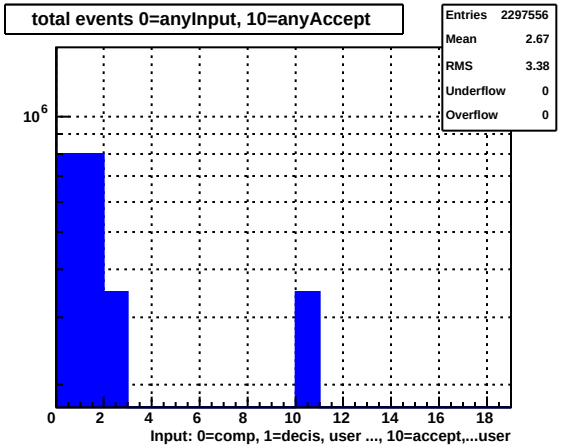
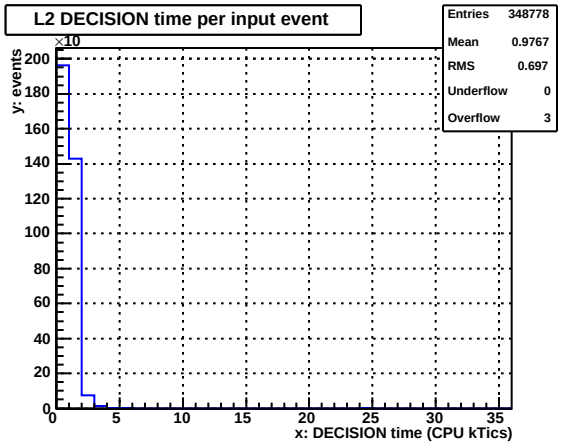
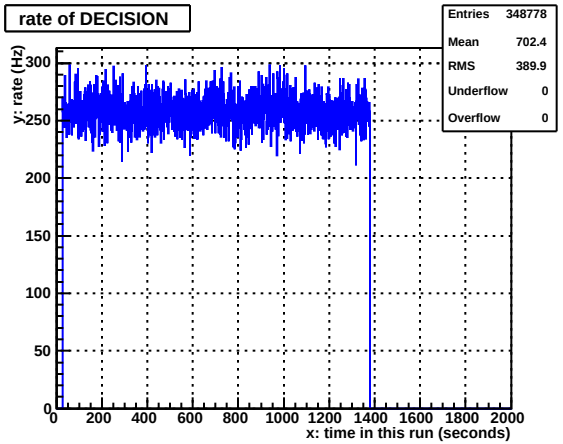
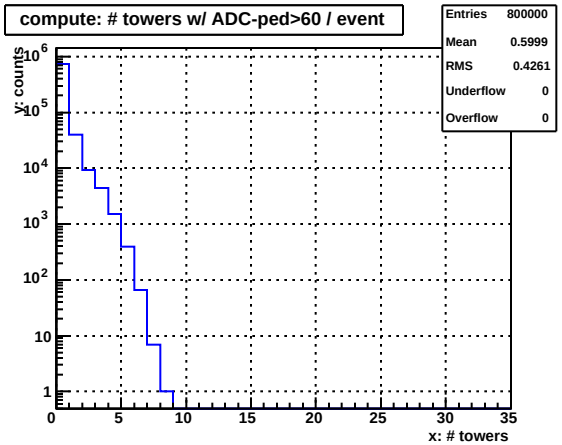

L2 COMPUTE time per input event
Entries 800000
Mean 0.9582
RMS 0.6258
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

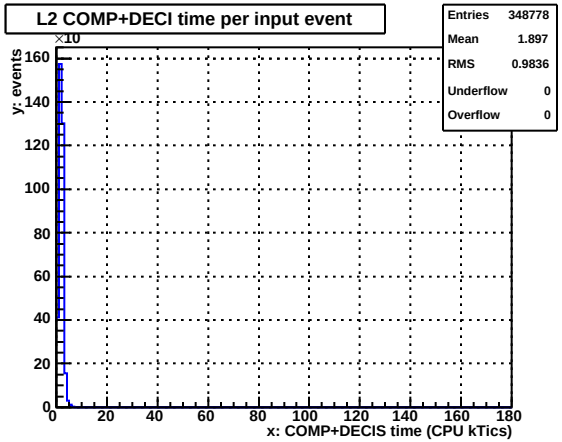

rate of COMPUTE
Entries 800000
Mean 701
RMS 390.7
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

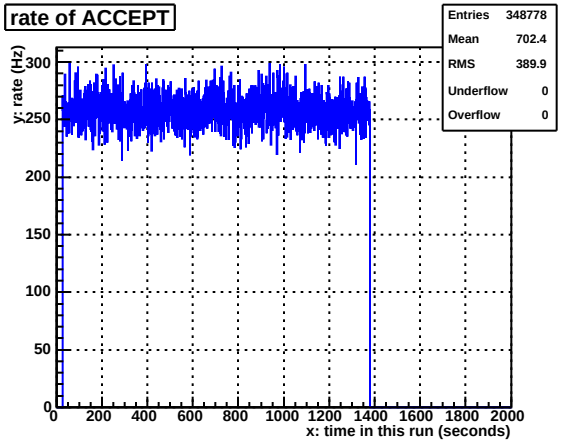

total events 0=anyInput, 10=anyAccept
Entries 2297556
Mean 2.67
RMS 3.38
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

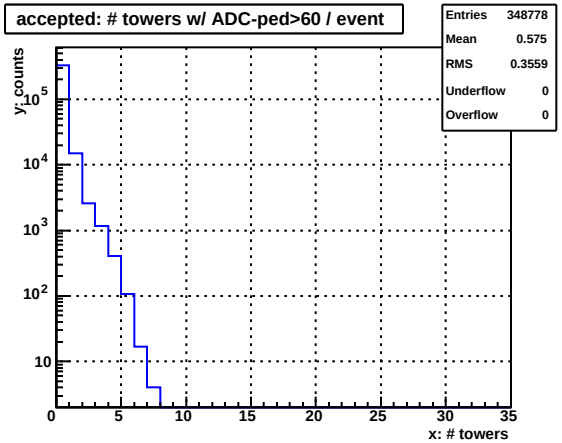

L2 DECISION time per input event
Entries 348778
Mean 0.9767
RMS 0.697
Underflow 0
Overflow 3
y: events
x: DECISION time (CPU kTics)

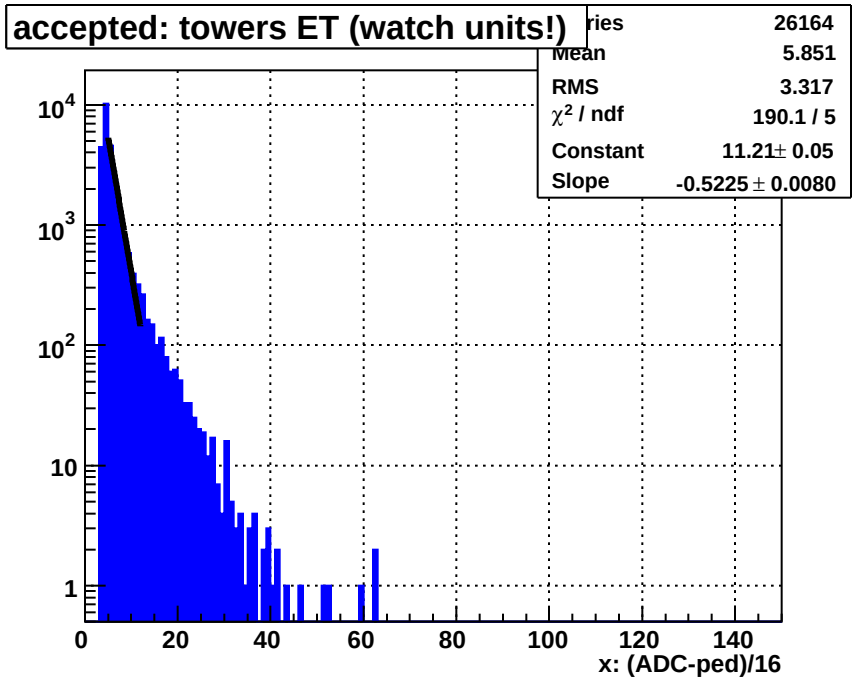

rate of DECISION
Entries 348778
Mean 702.4
RMS 389.9
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

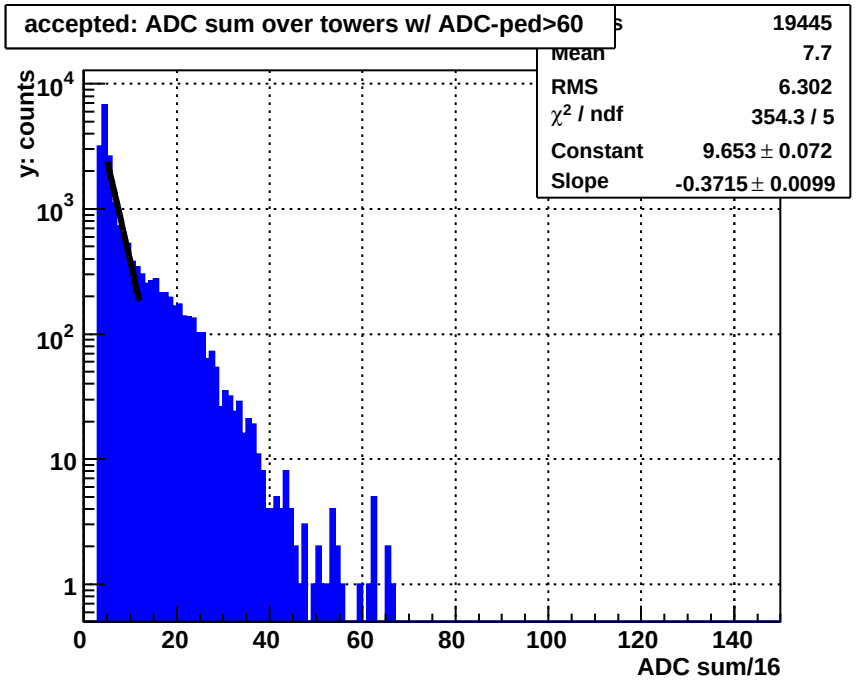

compute: # towers w/ ADC-ped>60 / event
Entries 800000
Mean 0.5999
RMS 0.4261
Underflow 0
Overflow 0
y: counts
x: # towers

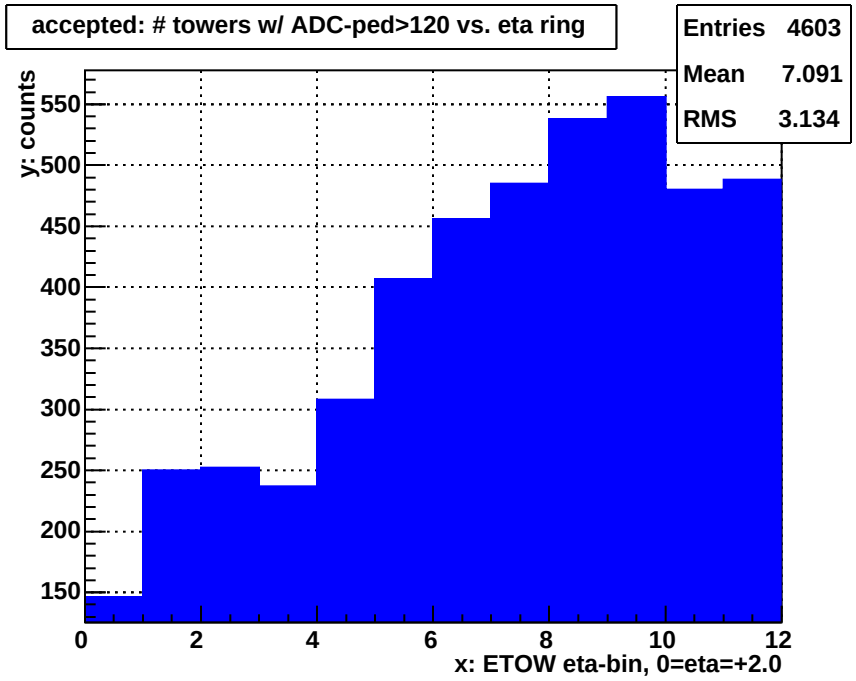

L2 COMP+DECI time per input event
Entries 348778
Mean 1.897
RMS 0.9836
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

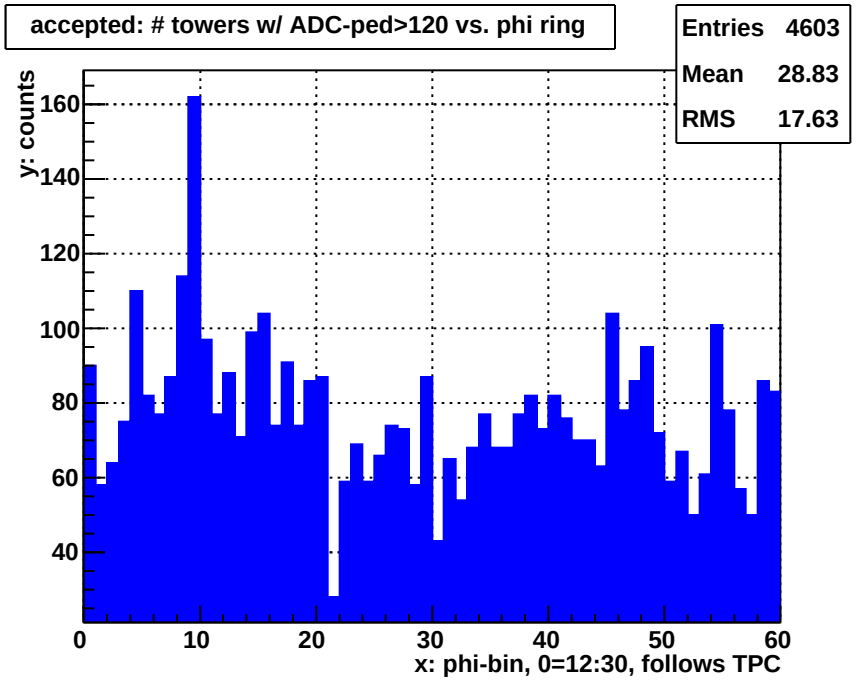

rate of ACCEPT
Entries 348778
Mean 702.4
RMS 389.9
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 348778
Mean 0.575
RMS 0.3559
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 26164
Mean 5.851
RMS 3.317
χ² / ndf 190.1 / 5
Constant 11.21 ± 0.05
Slope -0.5225 ± 0.0080
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 19445
Mean 7.7
RMS 6.302
χ² / ndf 354.3 / 5
Constant 9.653 ± 0.072
Slope -0.3715 ± 0.0099
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 4603
Mean 7.091
RMS 3.134
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 4603
Mean 28.83
RMS 17.63
y: counts
x: phi-bin, 0=12:30, follows TPC