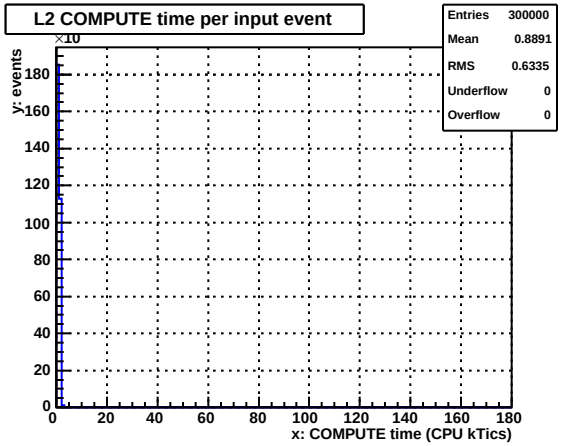
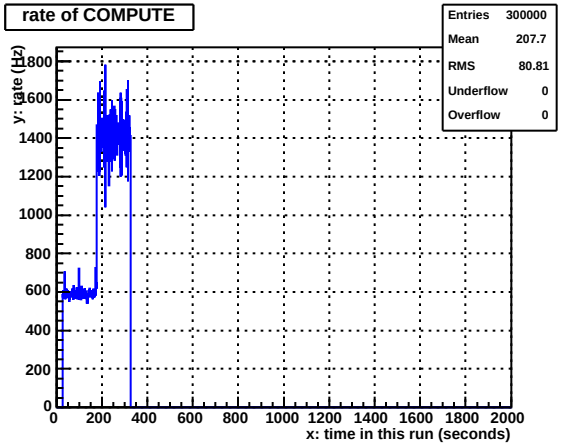
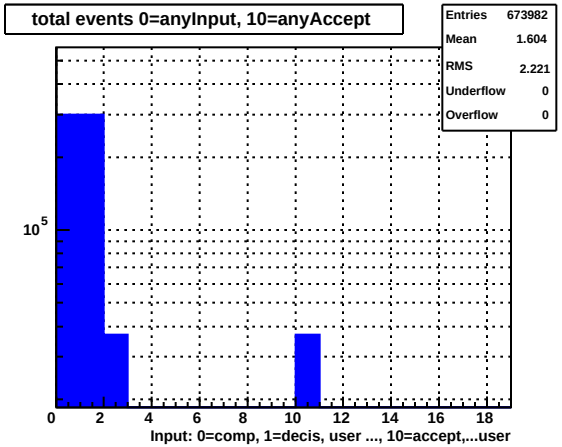
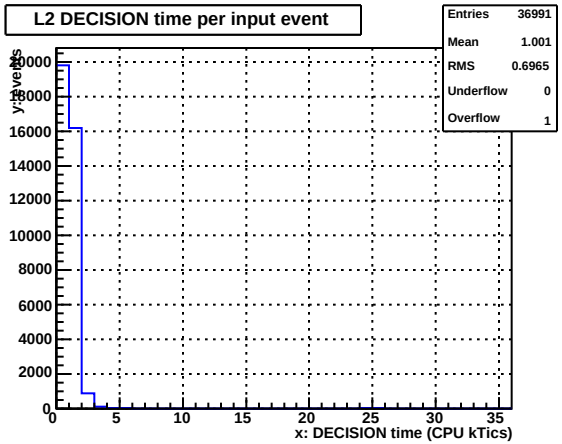
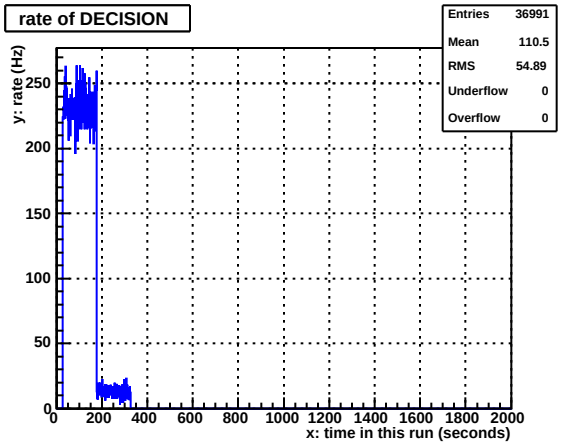
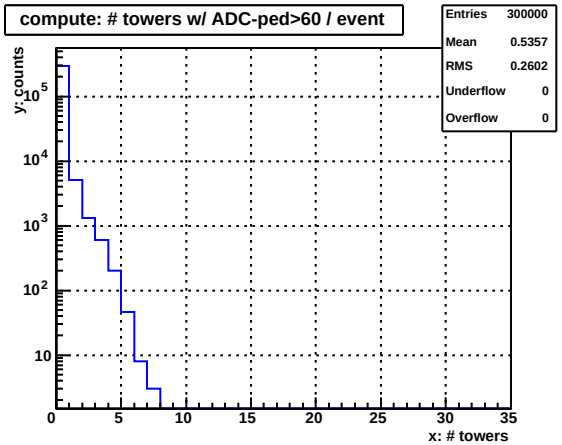

L2 COMPUTE time per input event
Entries 300000
Mean 0.8891
RMS 0.6335
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

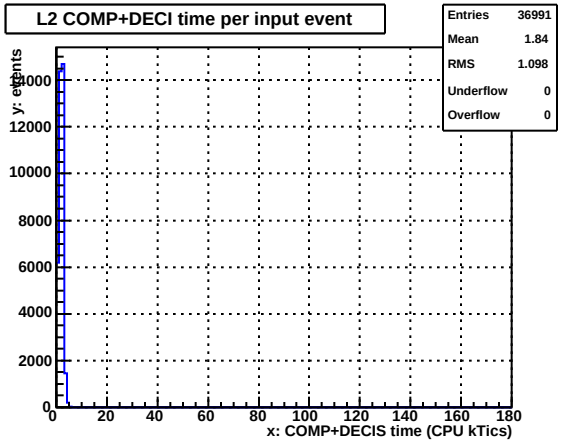

rate of COMPUTE
Entries 300000
Mean 207.7
RMS 80.81
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

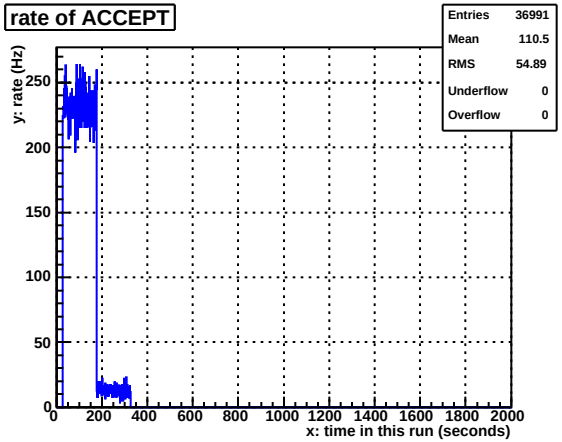

total events 0=anyInput, 10=anyAccept
Entries 673982
Mean 1.604
RMS 2.221
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

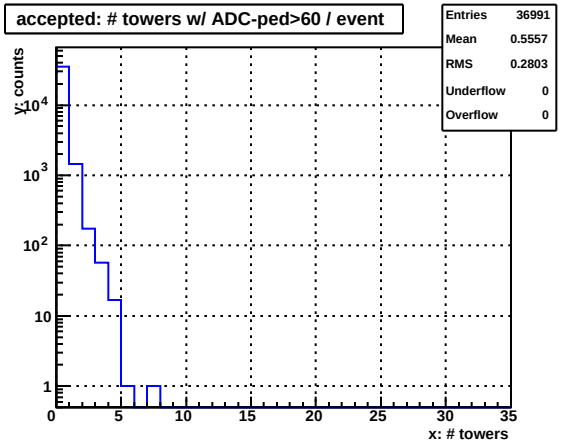

L2 DECISION time per input event
Entries 36991
Mean 1.001
RMS 0.6965
Underflow 0
Overflow 1
y: events
x: DECISION time (CPU kTics)

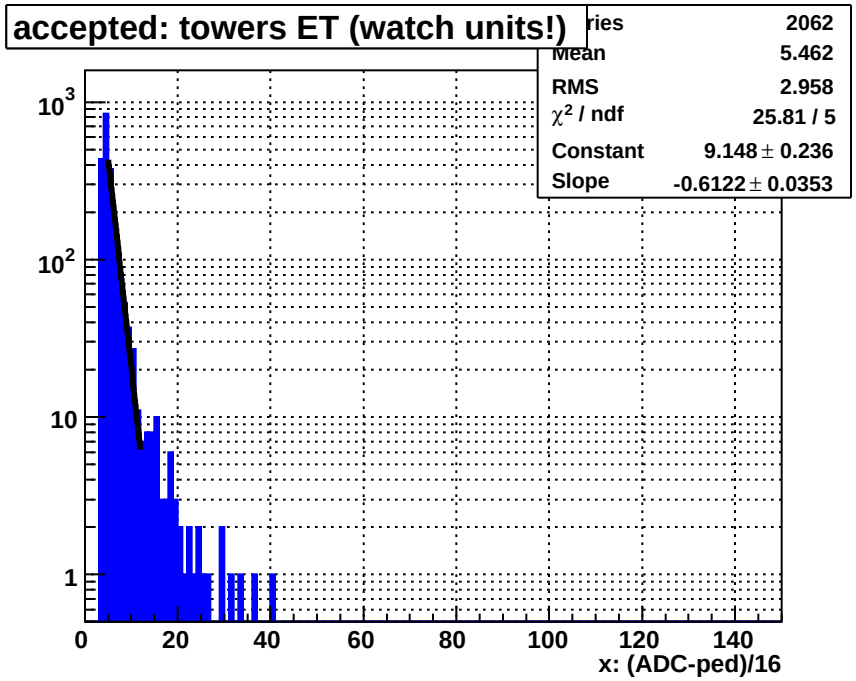

rate of DECISION
Entries 36991
Mean 110.5
RMS 54.89
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

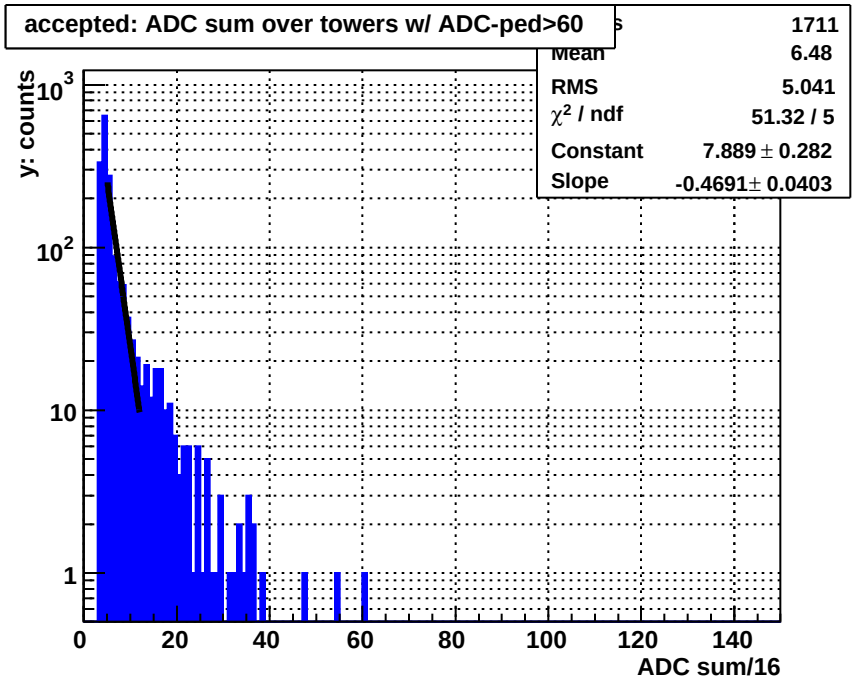

compute: # towers w/ ADC-ped>60 / event
Entries 300000
Mean 0.5357
RMS 0.2602
Underflow 0
Overflow 0
y: counts
x: # towers

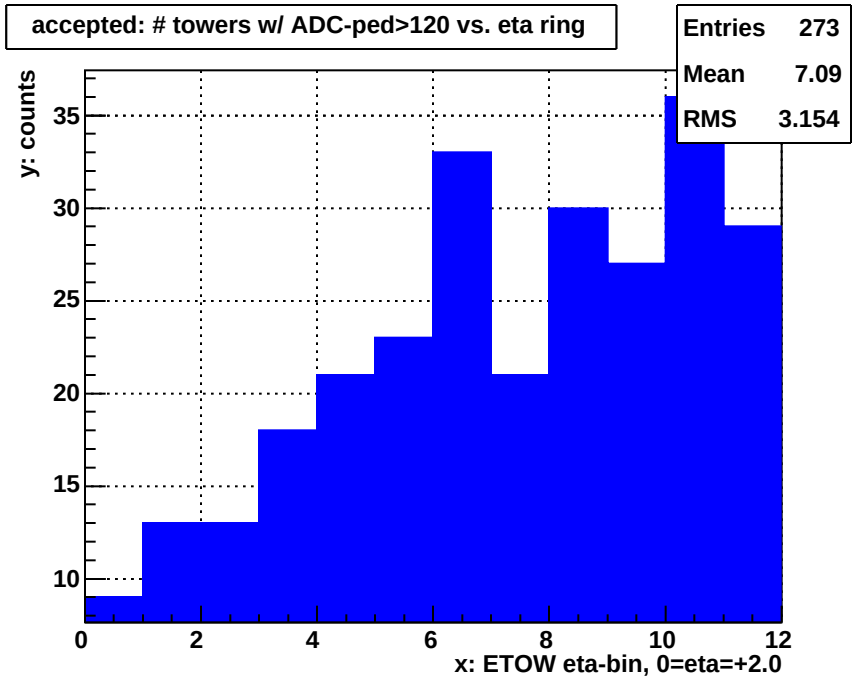

L2 COMP+DECI time per input event
Entries 36991
Mean 1.84
RMS 1.098
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

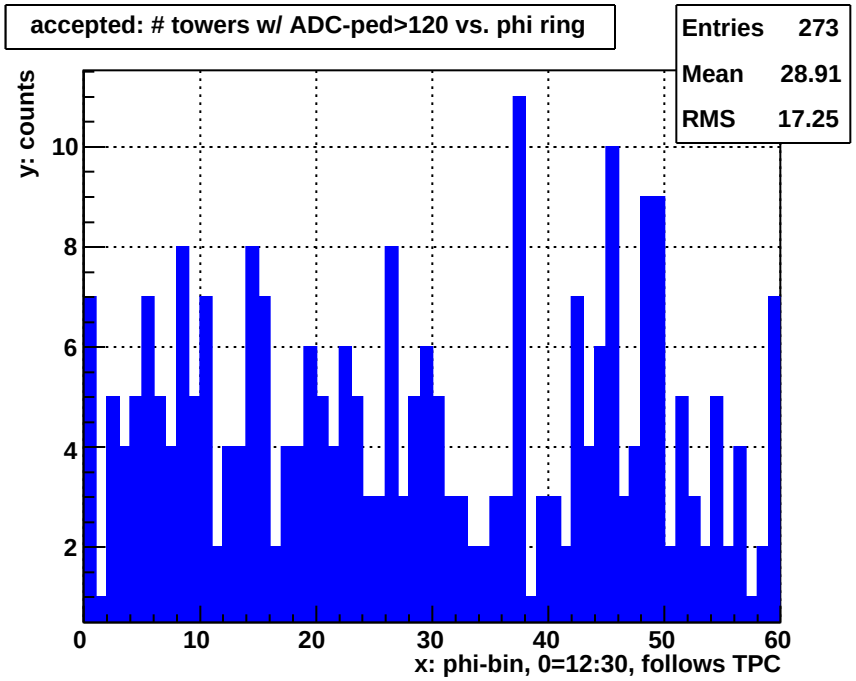

rate of ACCEPT
Entries 36991
Mean 110.5
RMS 54.89
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 36991
Mean 0.5557
RMS 0.2803
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 2062
Mean 5.462
RMS 2.958
χ² / ndf 25.81 / 5
Constant 9.148 ± 0.236
Slope -0.6122 ± 0.0353
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 1711
Mean 6.48
RMS 5.041
χ² / ndf 51.32 / 5
Constant 7.889 ± 0.282
Slope -0.4691 ± 0.0403
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 273
Mean 7.09
RMS 3.154
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 273
Mean 28.91
RMS 17.25
y: counts
x: phi-bin, 0=12:30, follows TPC