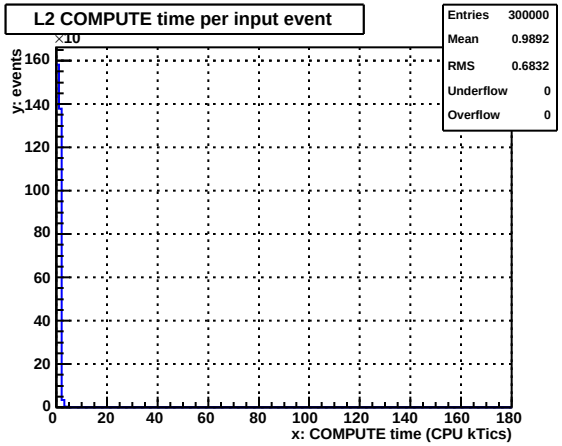
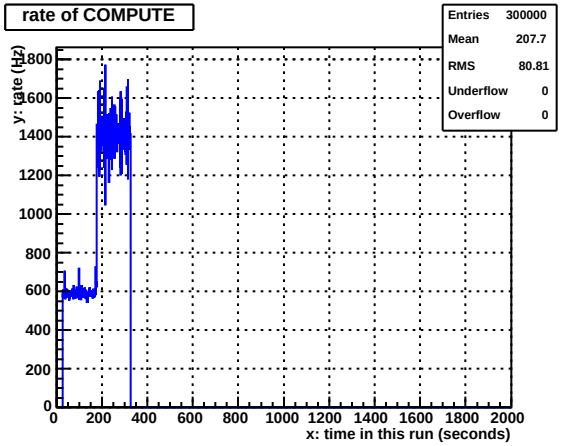
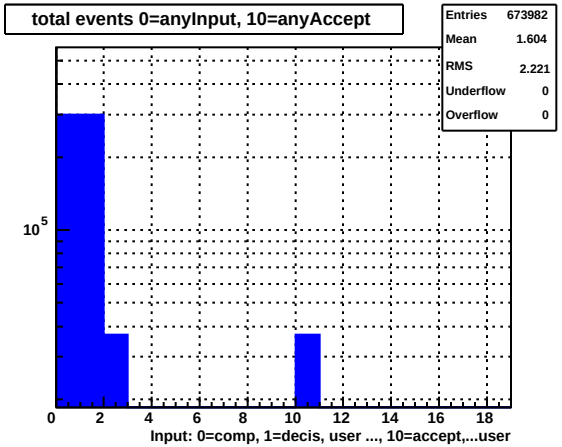
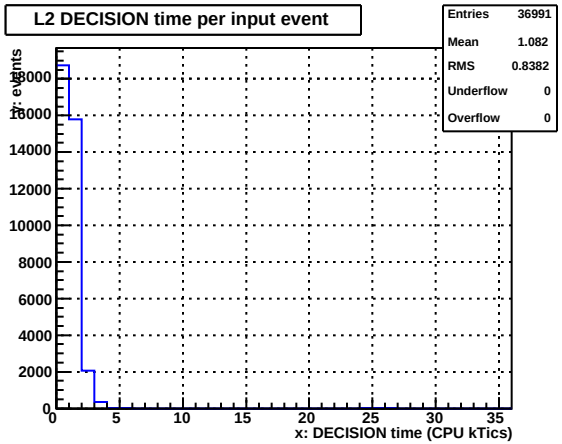
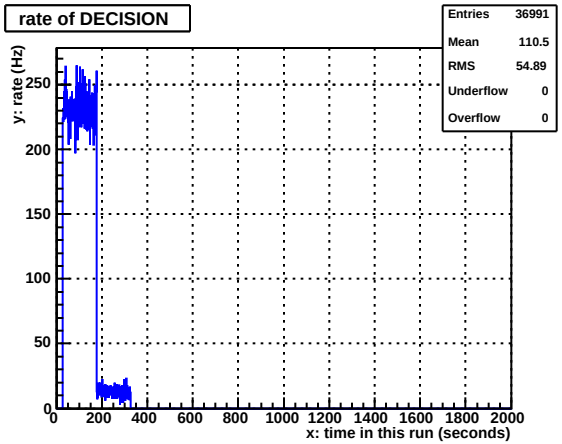
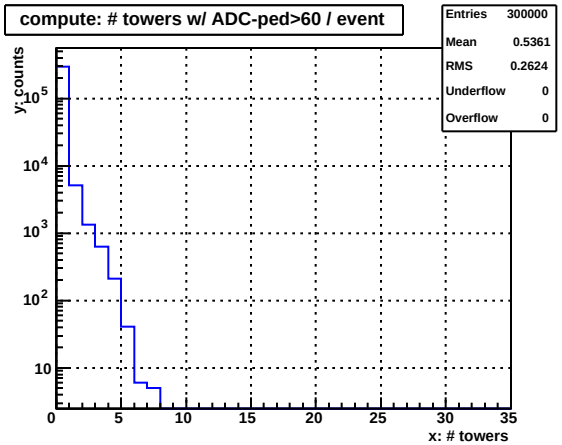

L2 COMPUTE time per input event
Entries 300000
Mean 0.9892
RMS 0.6832
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

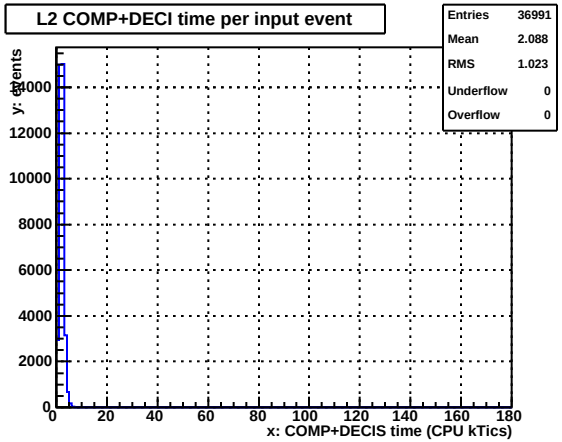

rate of COMPUTE
Entries 300000
Mean 207.7
RMS 80.81
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

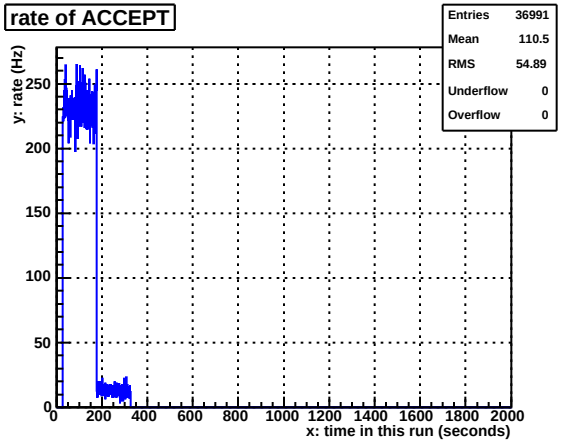

total events 0=anyInput, 10=anyAccept
Entries 673982
Mean 1.604
RMS 2.221
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

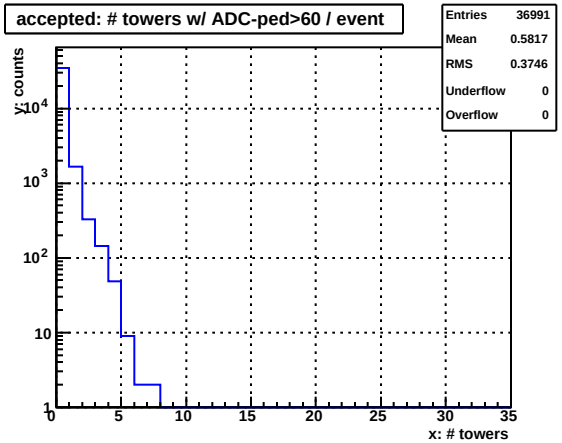

L2 DECISION time per input event
Entries 36991
Mean 1.082
RMS 0.8382
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

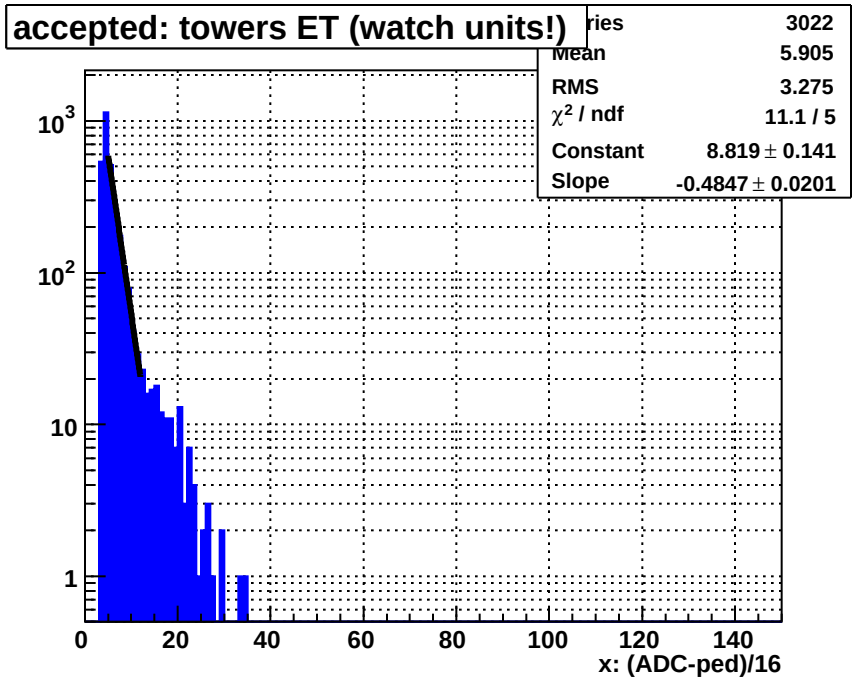

rate of DECISION
Entries 36991
Mean 110.5
RMS 54.89
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

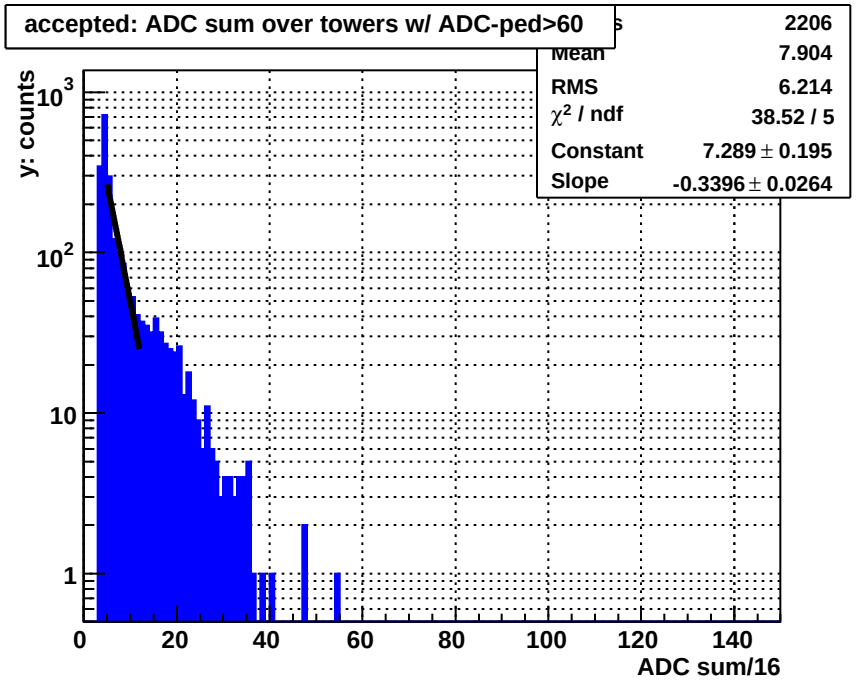

compute: # towers w/ ADC-ped>60 / event
Entries 300000
Mean 0.5361
RMS 0.2624
Underflow 0
Overflow 0
y: counts
x: # towers

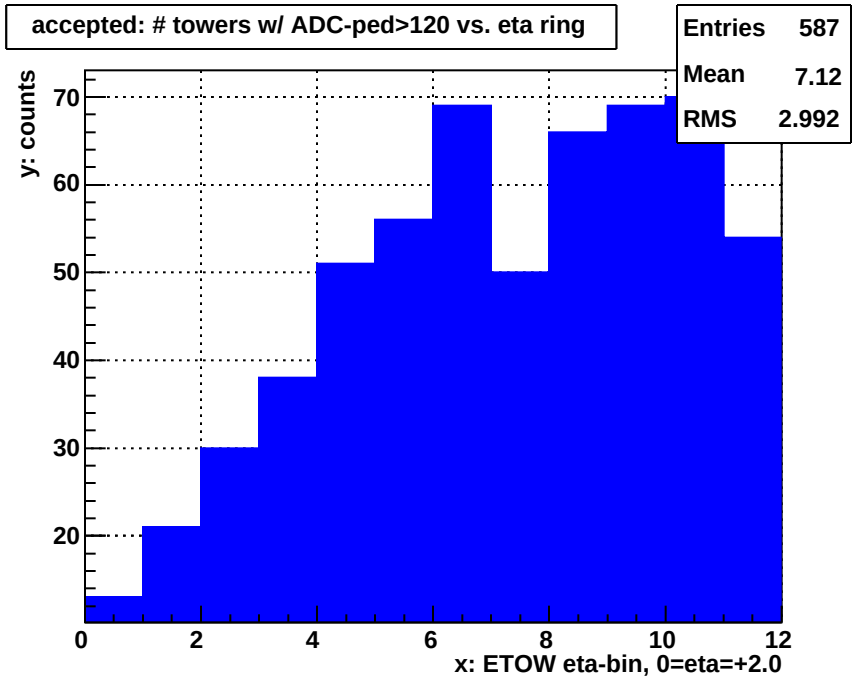

L2 COMP+DECI time per input event
Entries 36991
Mean 2.088
RMS 1.023
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

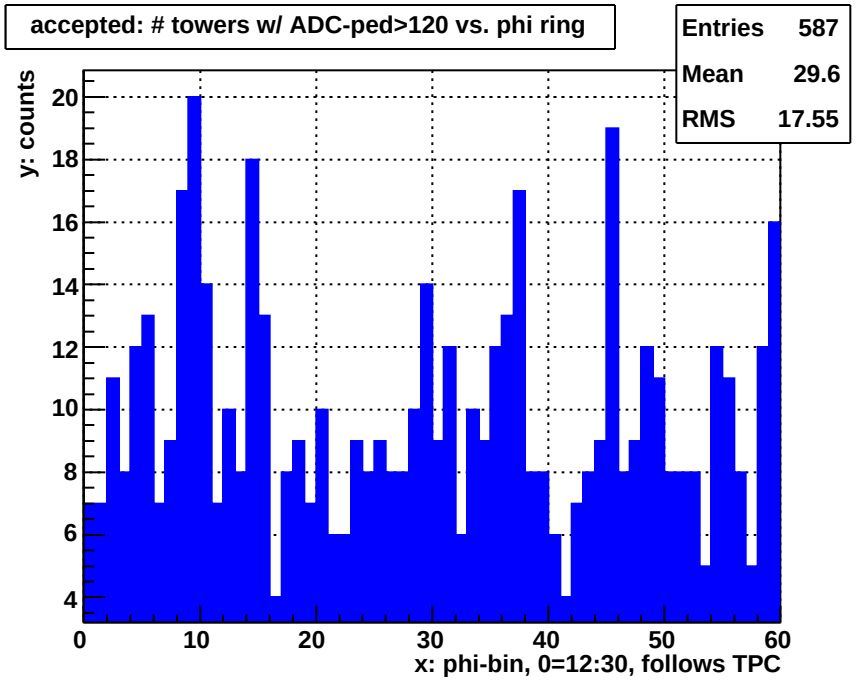

rate of ACCEPT
Entries 36991
Mean 110.5
RMS 54.89
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 36991
Mean 0.5817
RMS 0.3746
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 3022
Mean 5.905
RMS 3.275
χ² / ndf 11.1 / 5
Constant 8.819 ± 0.141
Slope -0.4847 ± 0.0201
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 2206
Mean 7.904
RMS 6.214
χ² / ndf 38.52 / 5
Constant 7.289 ± 0.195
Slope -0.3396 ± 0.0264
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 587
Mean 7.12
RMS 2.992
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 587
Mean 29.6
RMS 17.55
y: counts
x: phi-bin, 0=12:30, follows TPC