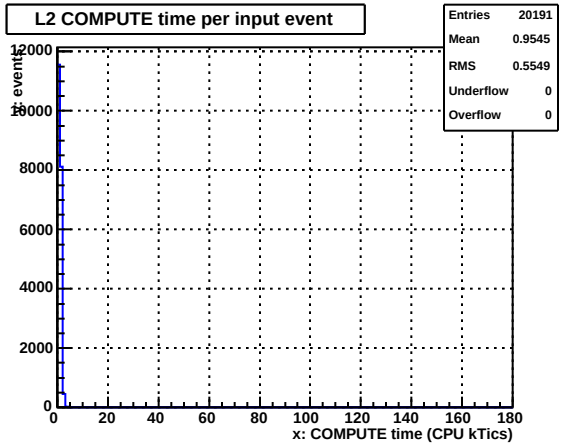
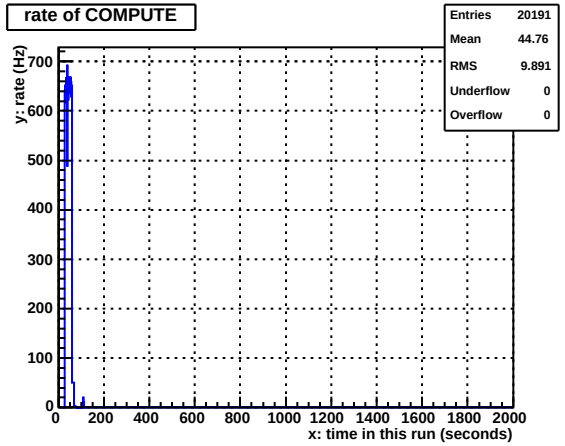
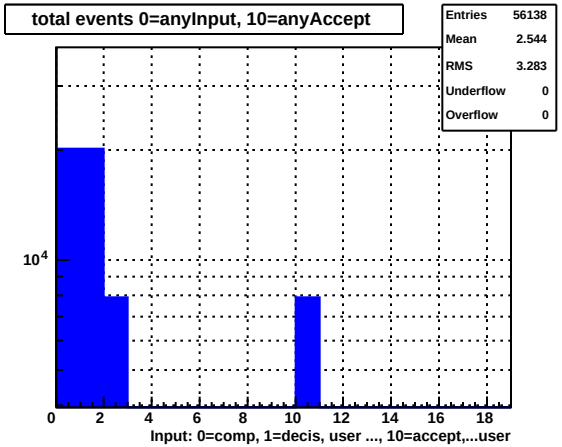
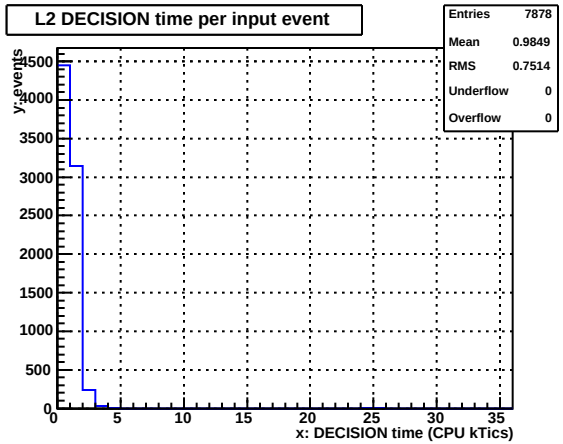
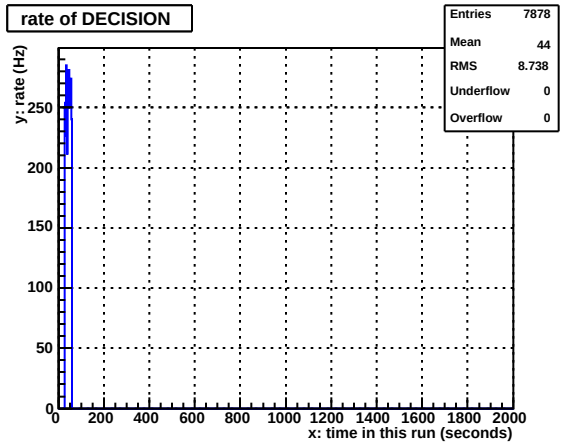
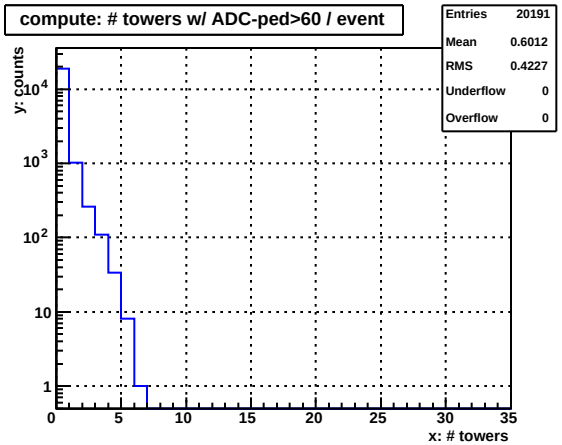

L2 COMPUTE time per input event
Entries 20191
Mean 0.9545
RMS 0.5549
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

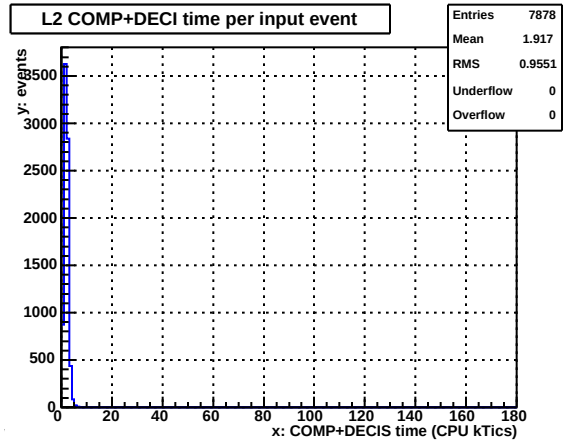

rate of COMPUTE
Entries 20191
Mean 44.76
RMS 9.891
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

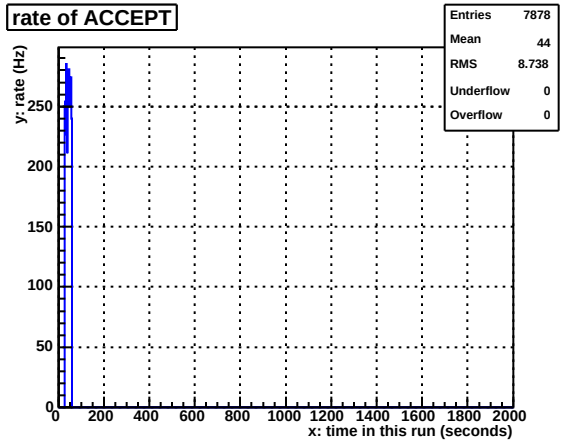

total events 0=anyInput, 10=anyAccept
Entries 56138
Mean 2.544
RMS 3.283
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

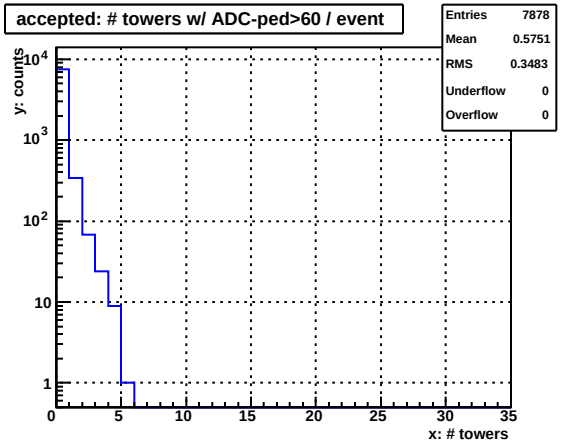

L2 DECISION time per input event
Entries 7878
Mean 0.9849
RMS 0.7514
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

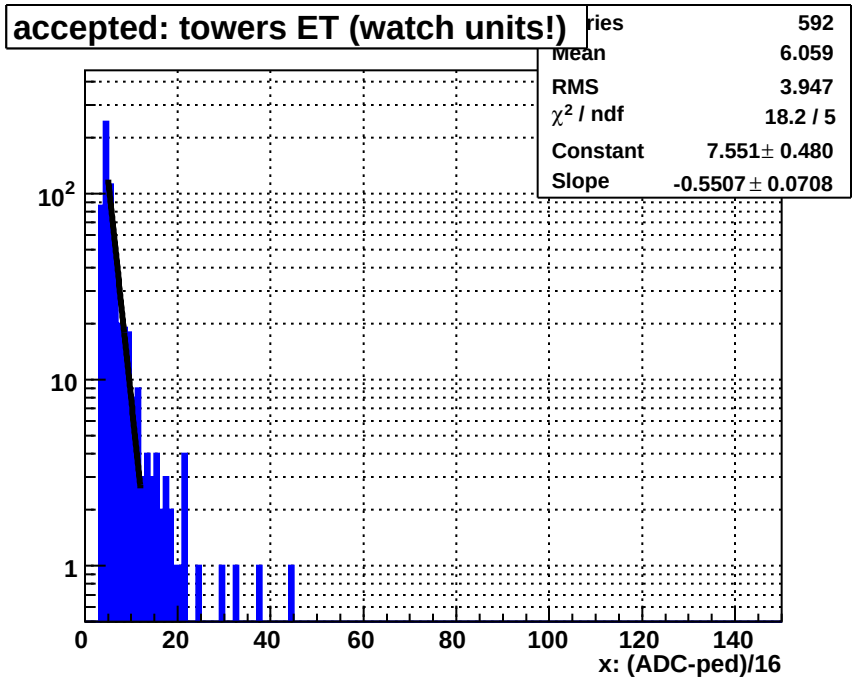

rate of DECISION
Entries 7878
Mean 44
RMS 8.738
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

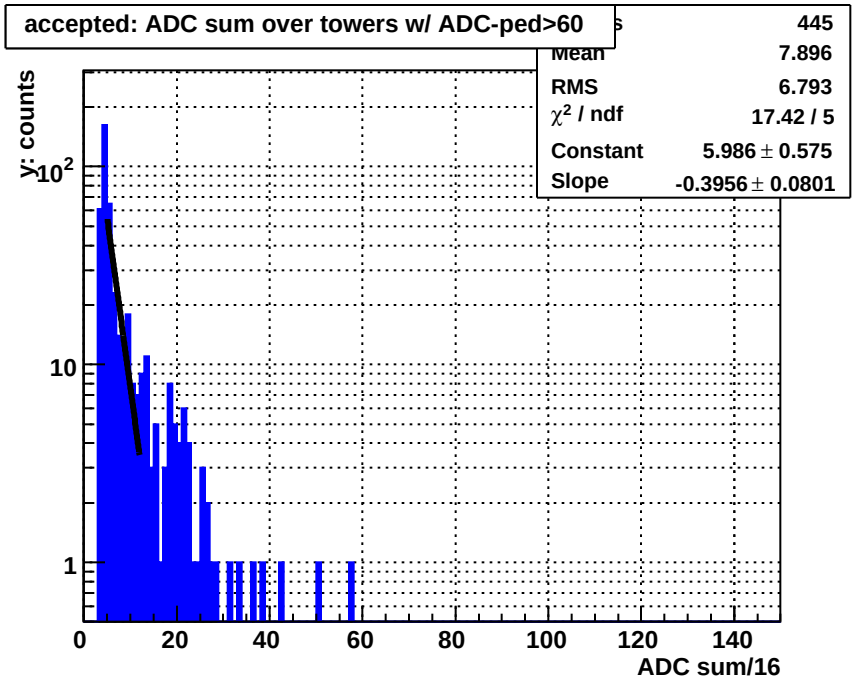

compute: # towers w/ ADC-ped>60 / event
Entries 20191
Mean 0.6012
RMS 0.4227
Underflow 0
Overflow 0
y: counts
x: # towers

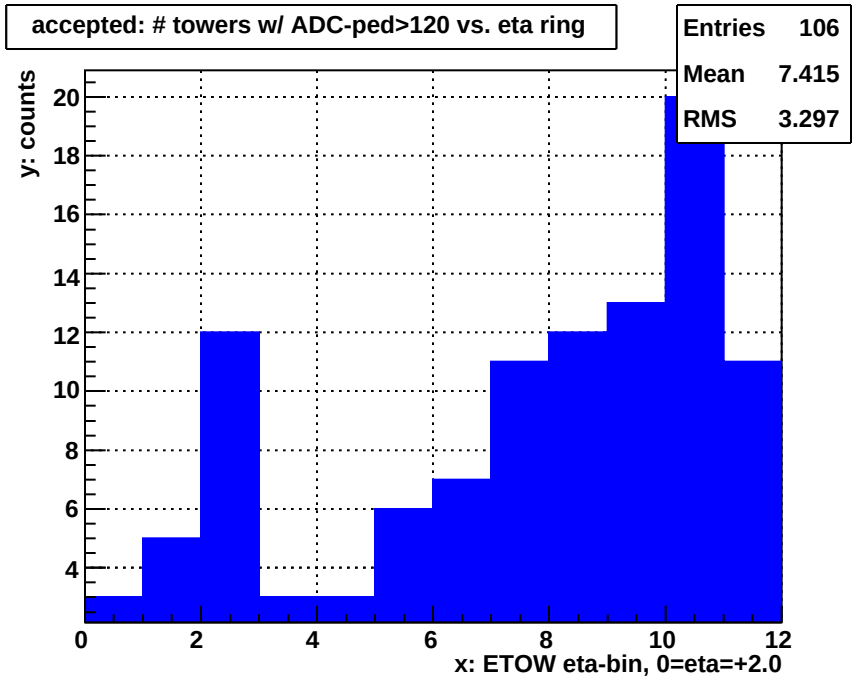

L2 COMP+DECI time per input event
Entries 7878
Mean 1.917
RMS 0.9551
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

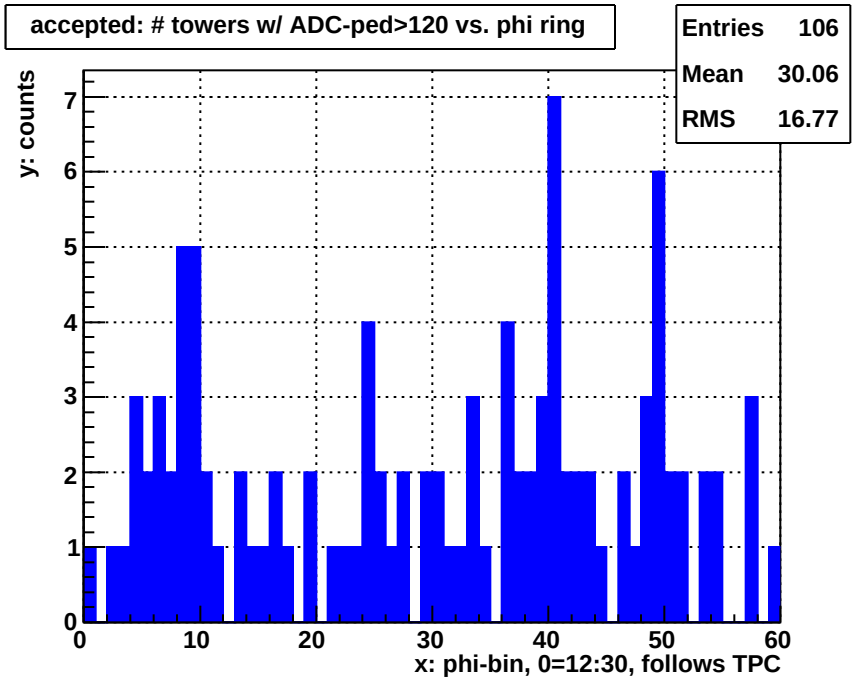

rate of ACCEPT
Entries 7878
Mean 44
RMS 8.738
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 7878
Mean 0.5751
RMS 0.3483
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 592
Mean 6.059
RMS 3.947
χ² / ndf 18.2 / 5
Constant 7.551 ± 0.480
Slope -0.5507 ± 0.0708
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 445
Mean 7.896
RMS 6.793
χ² / ndf 17.42 / 5
Constant 5.986 ± 0.575
Slope -0.3956 ± 0.0801
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 106
Mean 7.415
RMS 3.297
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 106
Mean 30.06
RMS 16.77
y: counts
x: phi-bin, 0=12:30, follows TPC