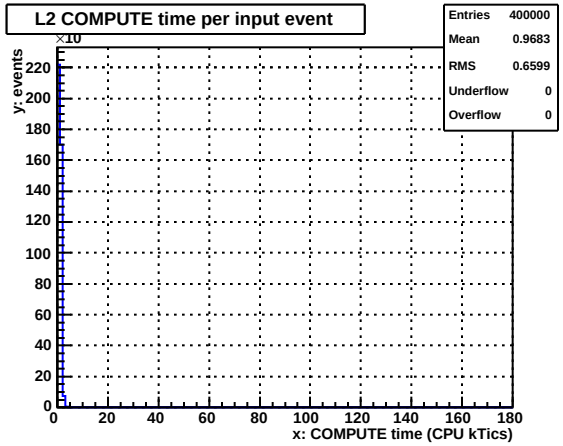
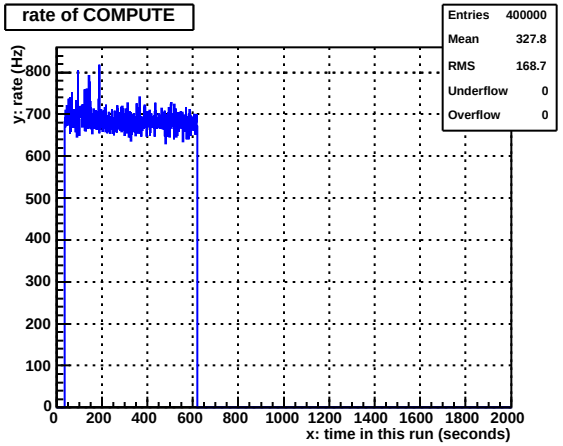
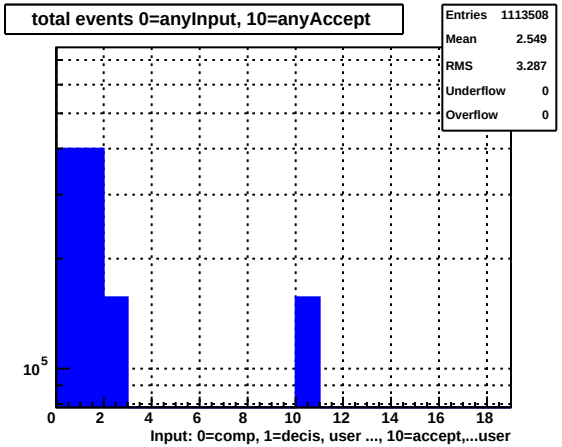
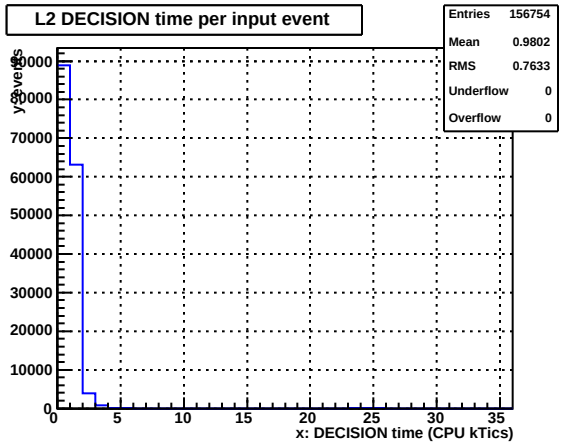
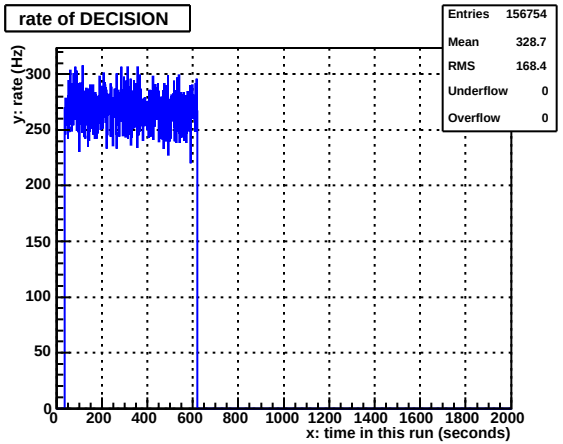
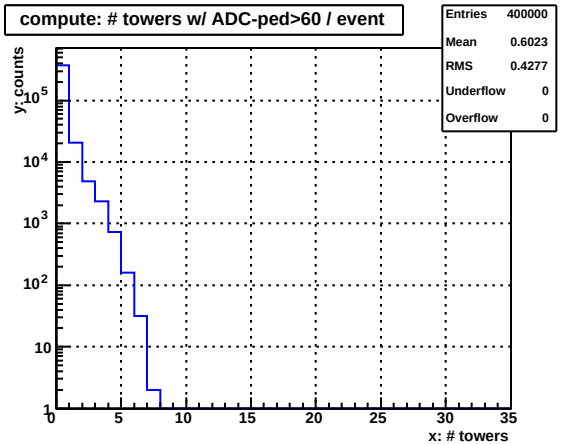

L2 COMPUTE time per input event
Entries 400000
Mean 0.9683
RMS 0.6599
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

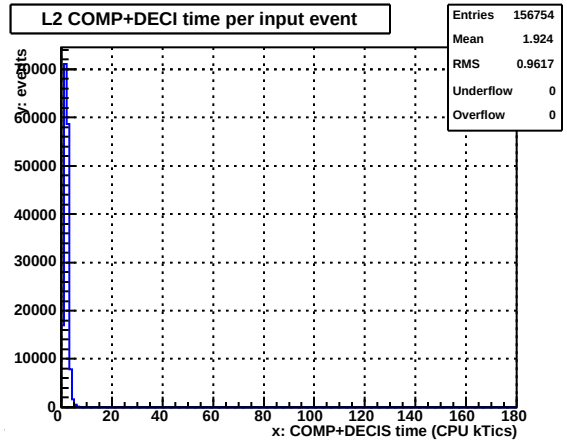

rate of COMPUTE
Entries 400000
Mean 327.8
RMS 168.7
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

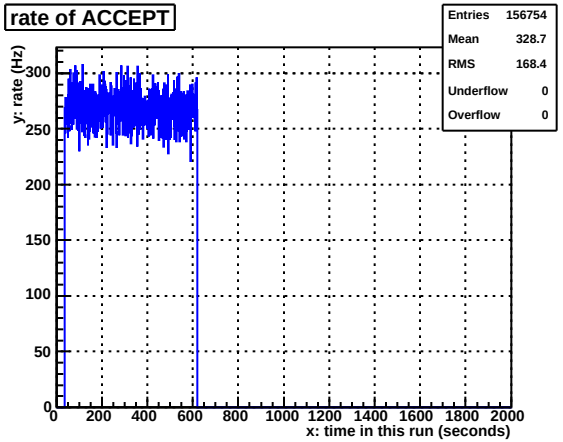

total events 0=anyInput, 10=anyAccept
Entries 1113508
Mean 2.549
RMS 3.287
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

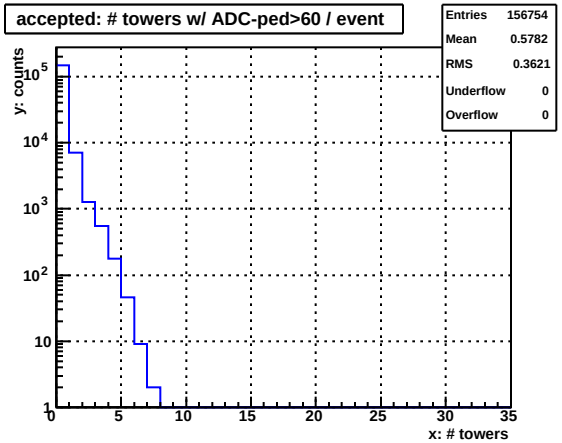

L2 DECISION time per input event
Entries 156754
Mean 0.9802
RMS 0.7633
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

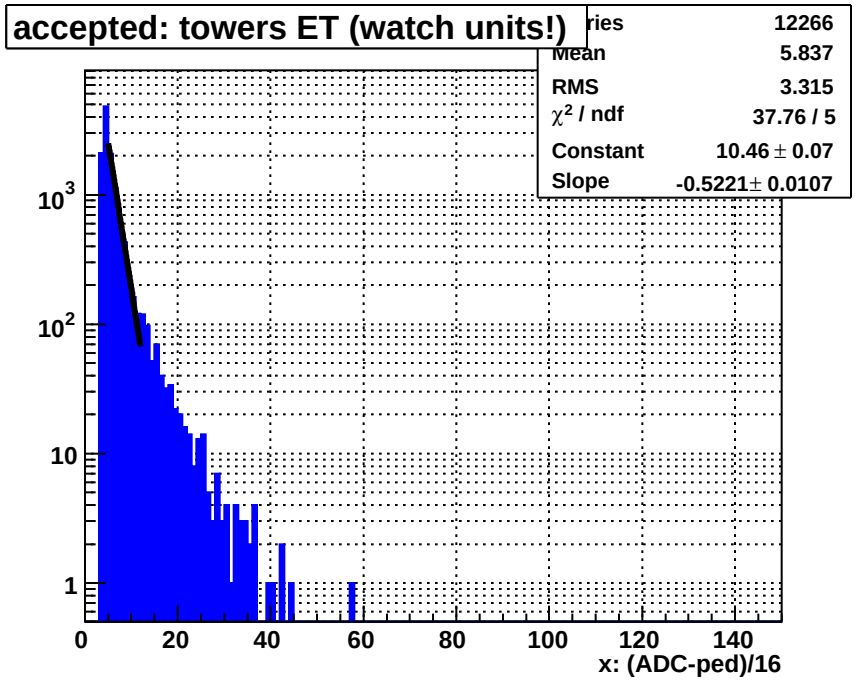

rate of DECISION
Entries 156754
Mean 328.7
RMS 168.4
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

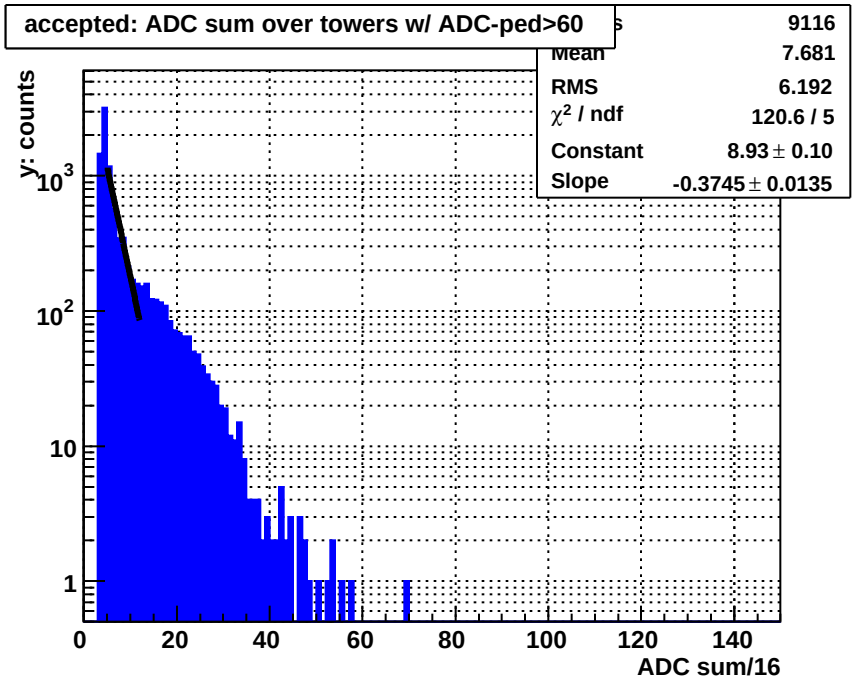

compute: # towers w/ ADC-ped>60 / event
Entries 400000
Mean 0.6023
RMS 0.4277
Underflow 0
Overflow 0
y: counts
x: # towers

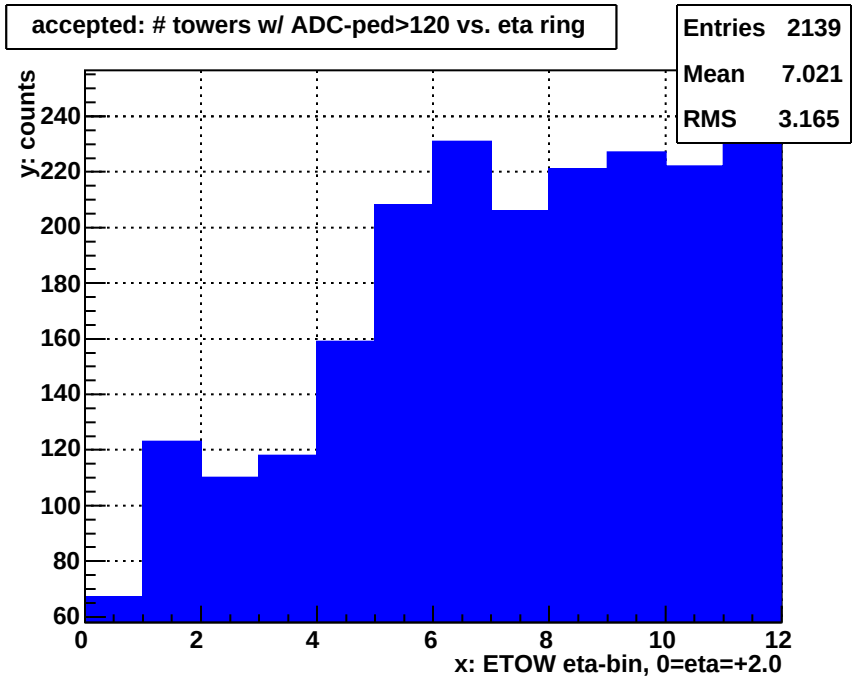

L2 COMP+DECI time per input event
Entries 156754
Mean 1.924
RMS 0.9617
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

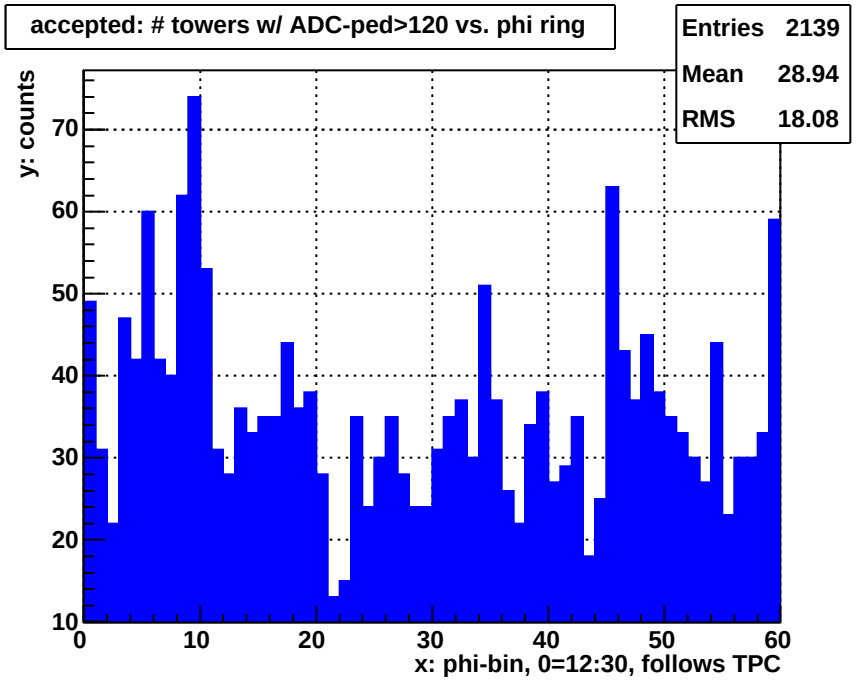

rate of ACCEPT
Entries 156754
Mean 328.7
RMS 168.4
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 156754
Mean 0.5782
RMS 0.3621
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 12266
Mean 5.837
RMS 3.315
χ² / ndf 37.76 / 5
Constant 10.46 ± 0.07
Slope -0.5221 ± 0.0107
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 9116
Mean 7.681
RMS 6.192
χ² / ndf 120.6 / 5
Constant 8.93 ± 0.10
Slope -0.3745 ± 0.0135
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 2139
Mean 7.021
RMS 3.165
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 2139
Mean 28.94
RMS 18.08
y: counts
x: phi-bin, 0=12:30, follows TPC