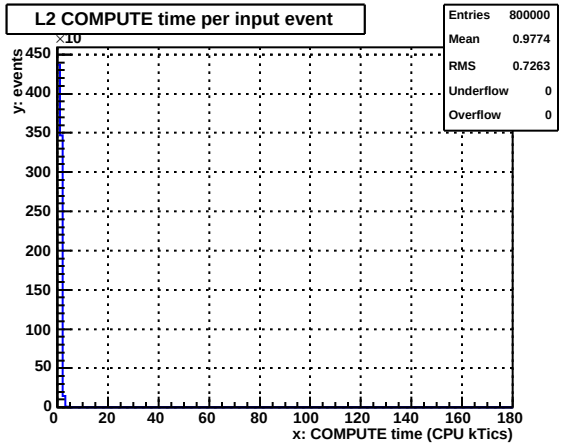
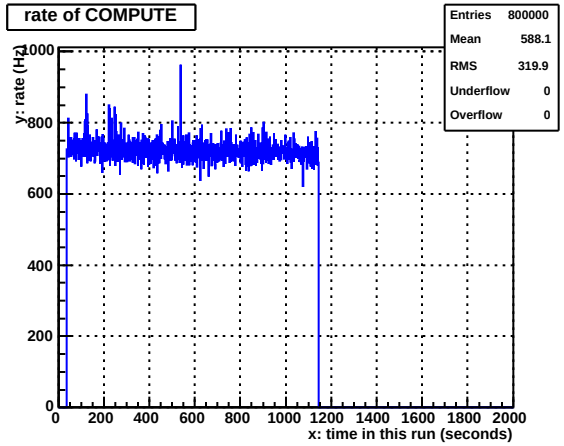
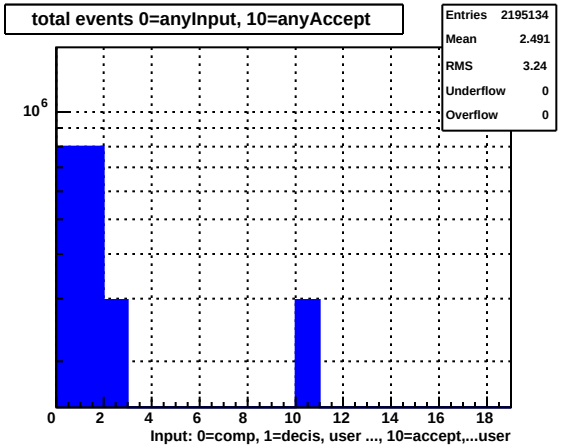
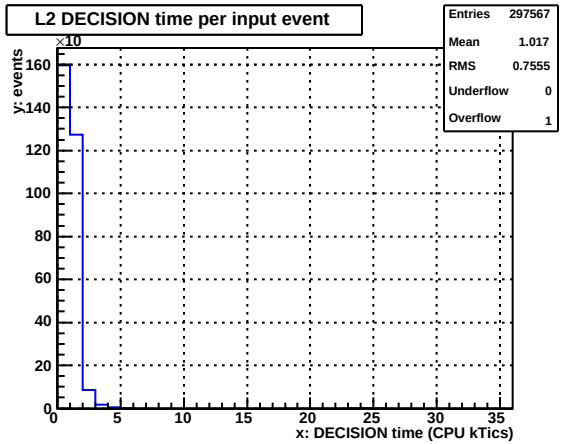
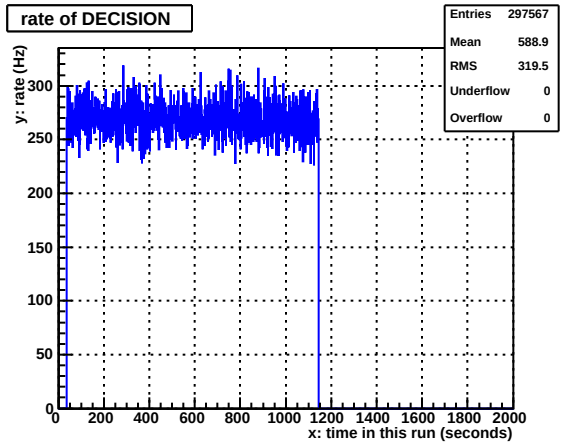
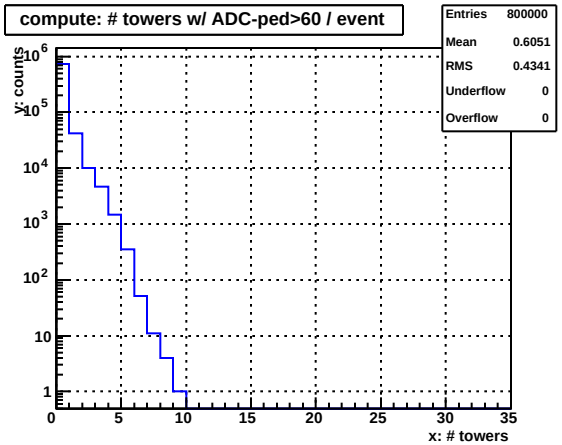

L2 COMPUTE time per input event
Entries 800000
Mean 0.9774
RMS 0.7263
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

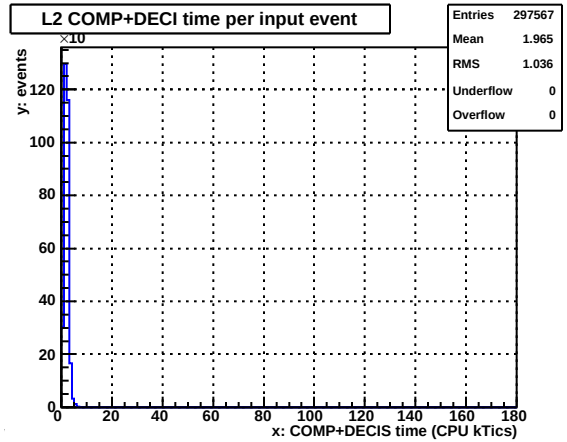

rate of COMPUTE
Entries 800000
Mean 588.1
RMS 319.9
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

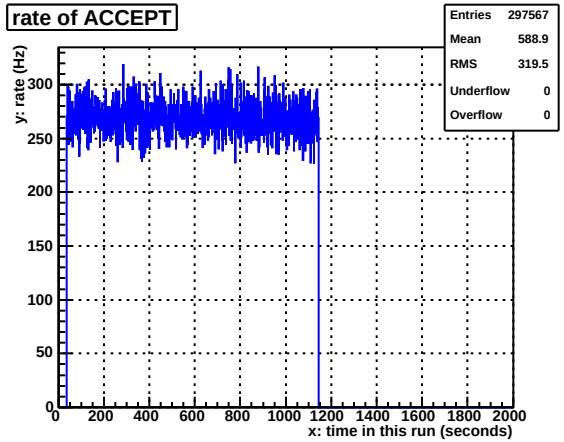

total events 0=anyInput, 10=anyAccept
Entries 2195134
Mean 2.491
RMS 3.24
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

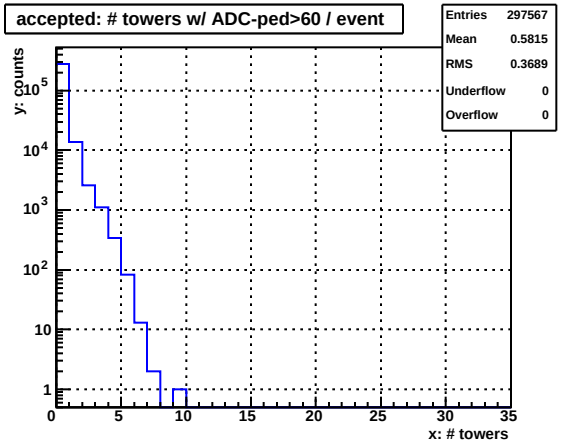

L2 DECISION time per input event
Entries 297567
Mean 1.017
RMS 0.7555
Underflow 0
Overflow 1
y: events
x: DECISION time (CPU kTics)

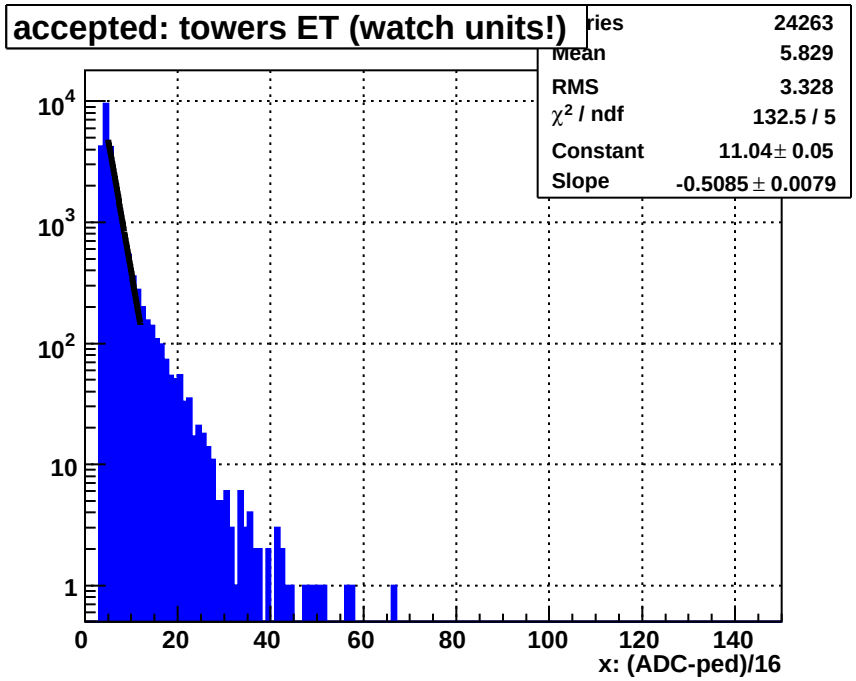

rate of DECISION
Entries 297567
Mean 588.9
RMS 319.5
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

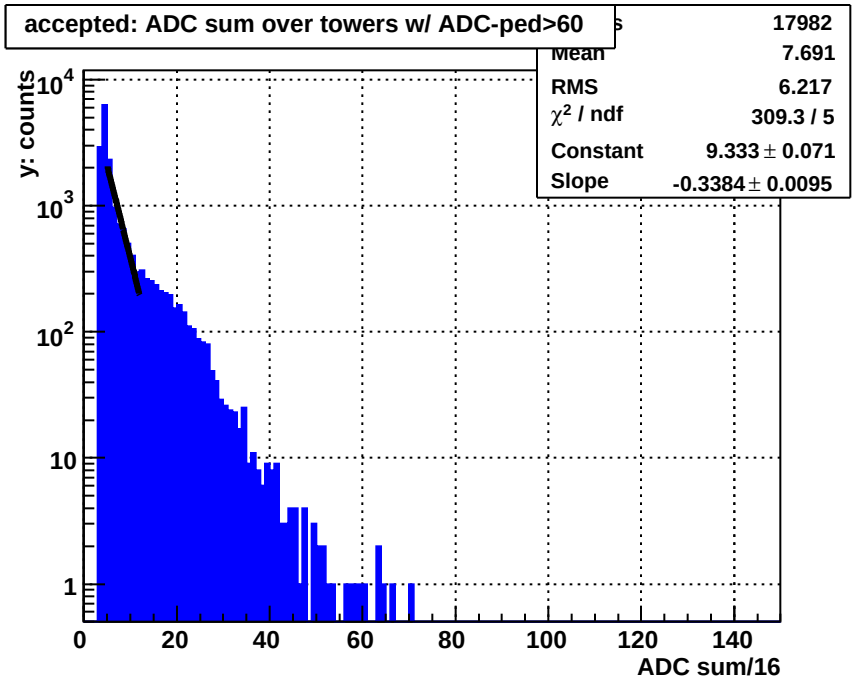

compute: # towers w/ ADC-ped>60 / event
Entries 800000
Mean 0.6051
RMS 0.4341
Underflow 0
Overflow 0
y: counts
x: # towers

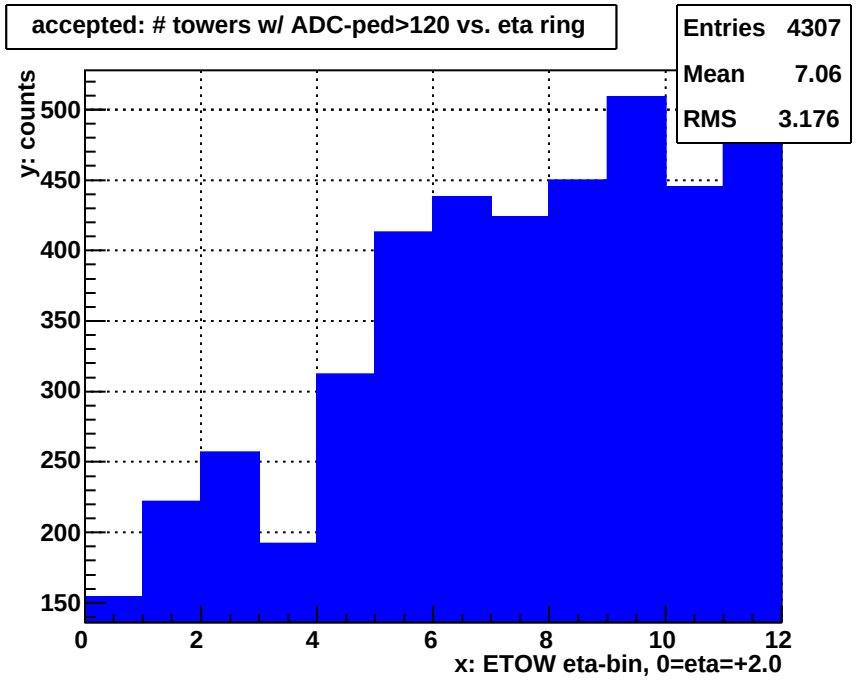

L2 COMP+DECI time per input event
Entries 297567
Mean 1.965
RMS 1.036
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

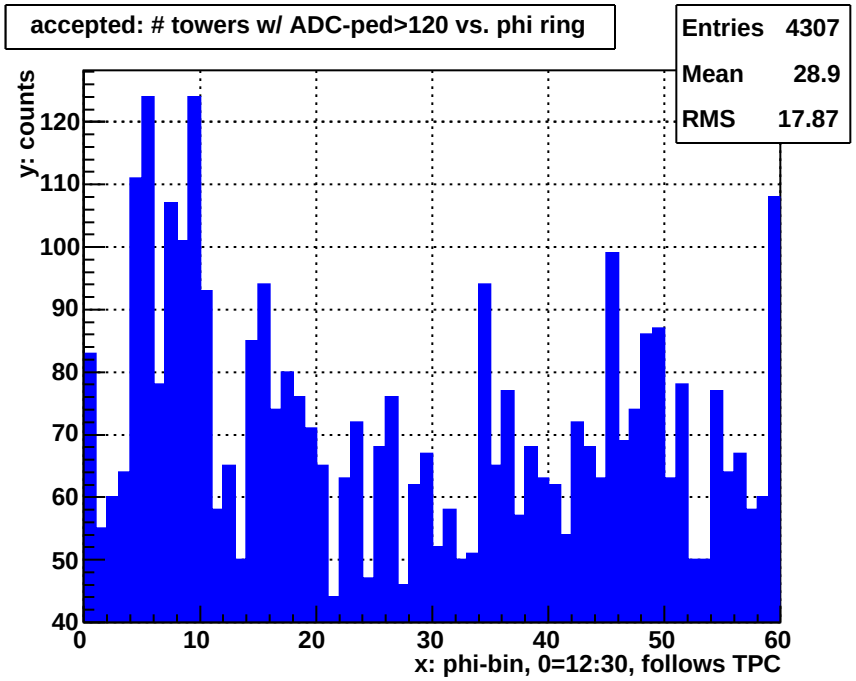

rate of ACCEPT
Entries 297567
Mean 588.9
RMS 319.5
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 297567
Mean 0.5815
RMS 0.3689
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 24263
Mean 5.829
RMS 3.328
χ² / ndf 132.5 / 5
Constant 11.04 ± 0.05
Slope -0.5085 ± 0.0079
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 17982
Mean 7.691
RMS 6.217
χ² / ndf 309.3 / 5
Constant 9.333 ± 0.071
Slope -0.3384 ± 0.0095
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 4307
Mean 7.06
RMS 3.176
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 4307
Mean 28.9
RMS 17.87
y: counts
x: phi-bin, 0=12:30, follows TPC