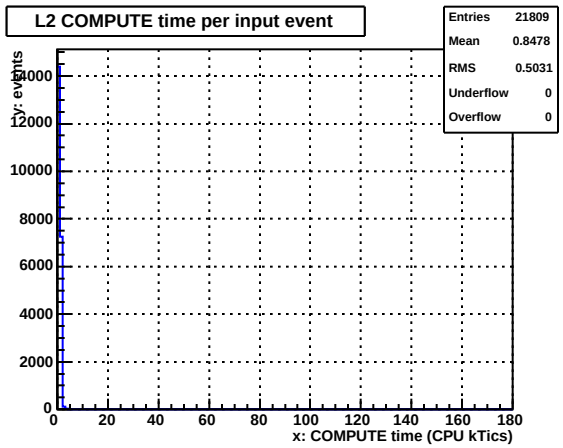
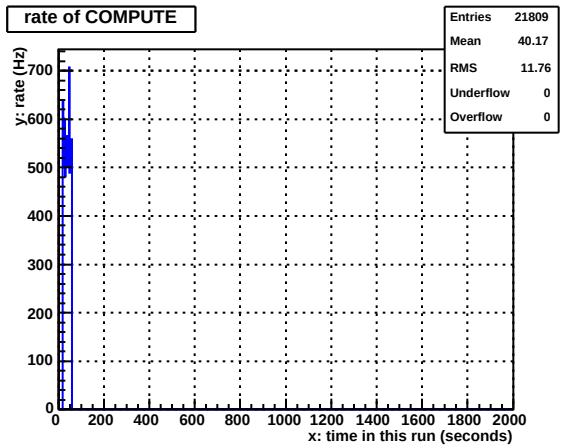
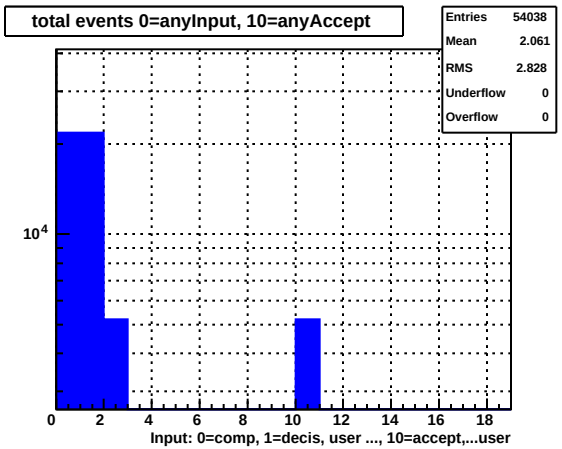
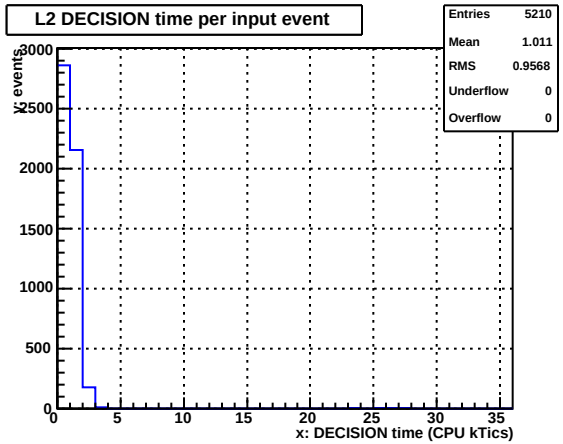
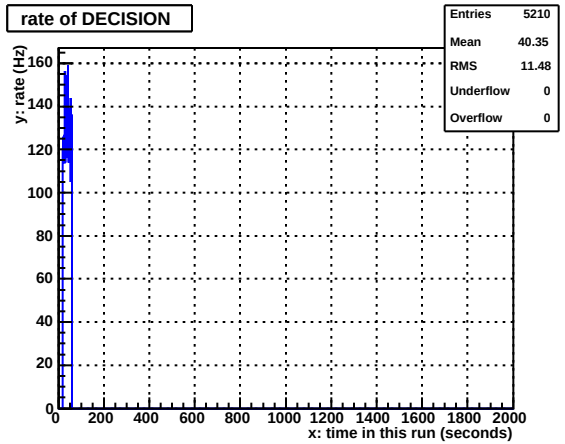
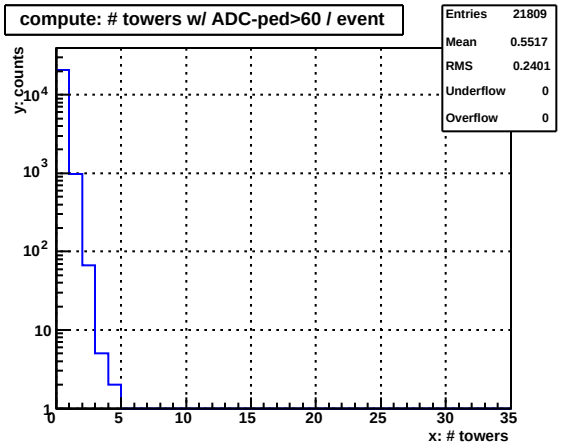

L2 COMPUTE time per input event
Entries 21809
Mean 0.8478
RMS 0.5031
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

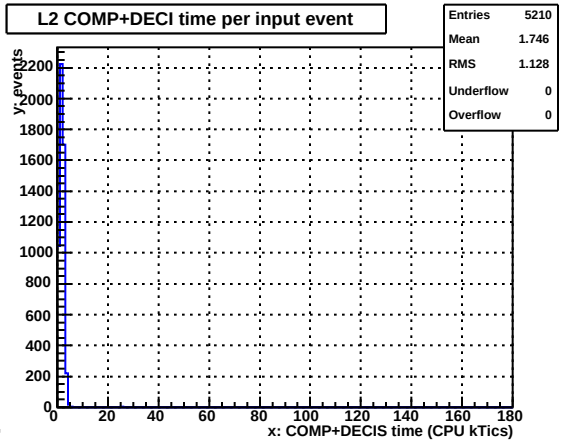

rate of COMPUTE
Entries 21809
Mean 40.17
RMS 11.76
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

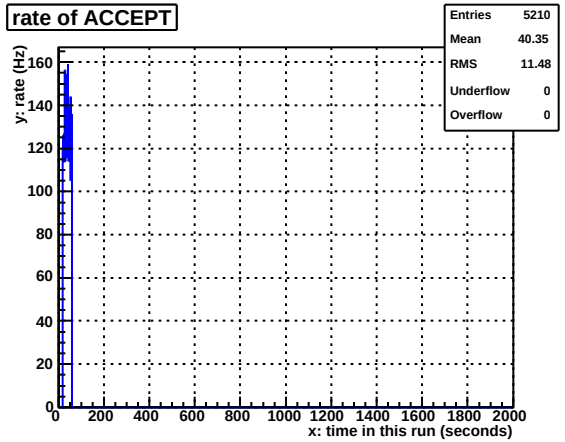

total events 0=anyInput, 10=anyAccept
Entries 54038
Mean 2.061
RMS 2.828
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

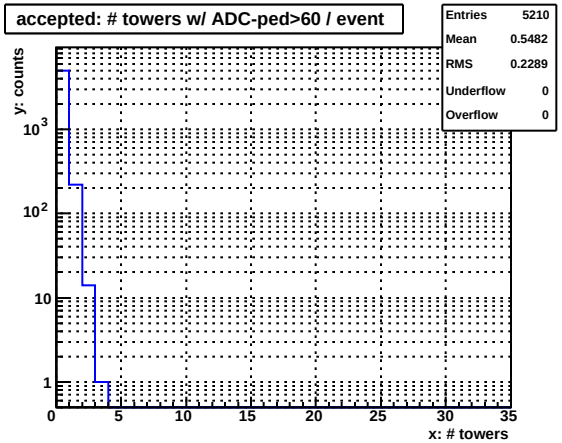

L2 DECISION time per input event
Entries 5210
Mean 1.011
RMS 0.9568
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

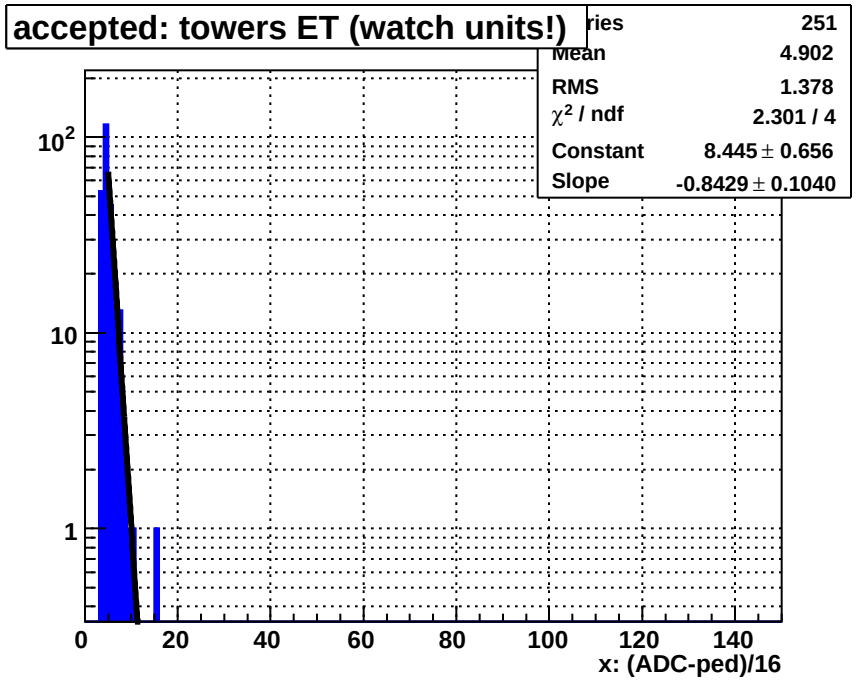

rate of DECISION
Entries 5210
Mean 40.35
RMS 11.48
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

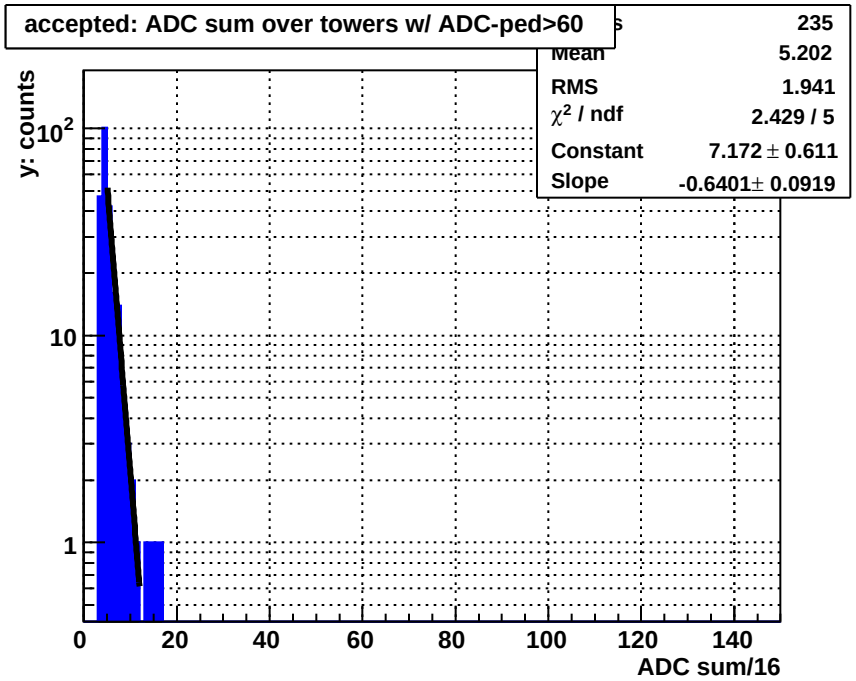

compute: # towers w/ ADC-ped>60 / event
Entries 21809
Mean 0.5517
RMS 0.2401
Underflow 0
Overflow 0
y: counts
x: # towers

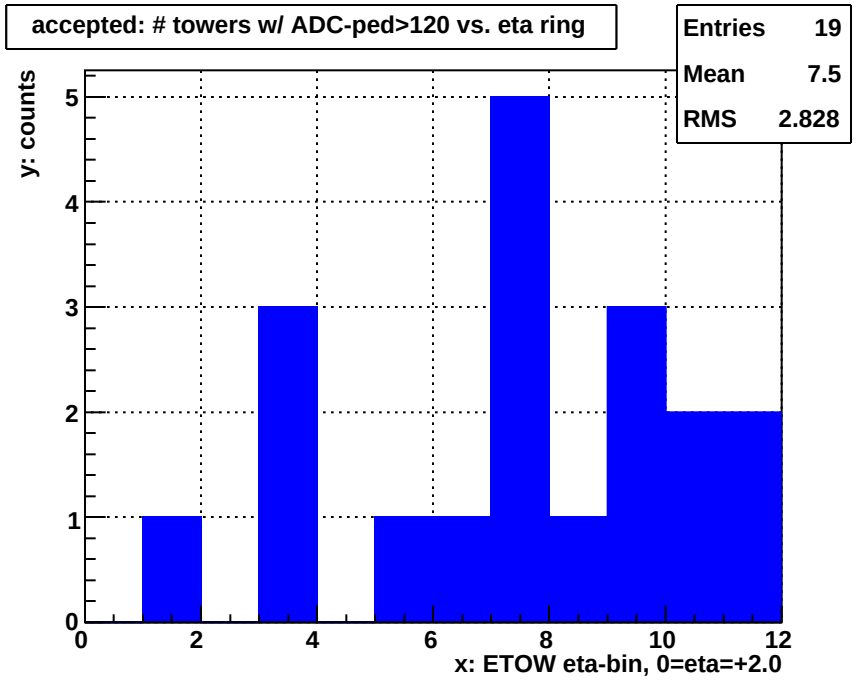

L2 COMP+DECI time per input event
Entries 5210
Mean 1.746
RMS 1.128
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

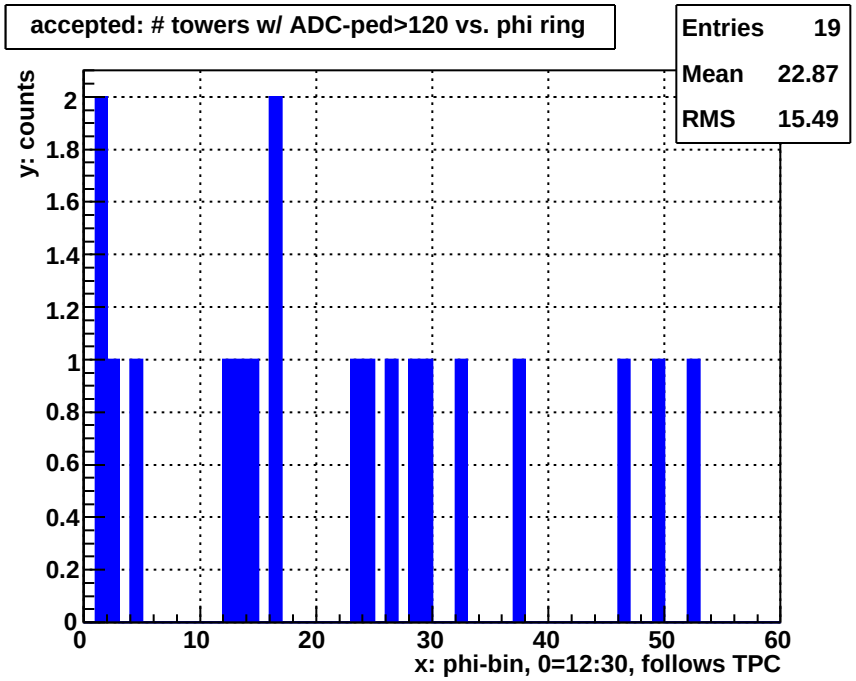

rate of ACCEPT
Entries 5210
Mean 40.35
RMS 11.48
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 5210
Mean 0.5482
RMS 0.2289
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 251
Mean 4.902
RMS 1.378
χ² / ndf 2.301 / 4
Constant 8.445 ± 0.656
Slope -0.8429 ± 0.1040
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 235
Mean 5.202
RMS 1.941
χ² / ndf 2.429 / 5
Constant 7.172 ± 0.611
Slope -0.6401± 0.0919
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 19
Mean 7.5
RMS 2.828
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 19
Mean 22.87
RMS 15.49
y: counts
x: phi-bin, 0=12:30, follows TPC