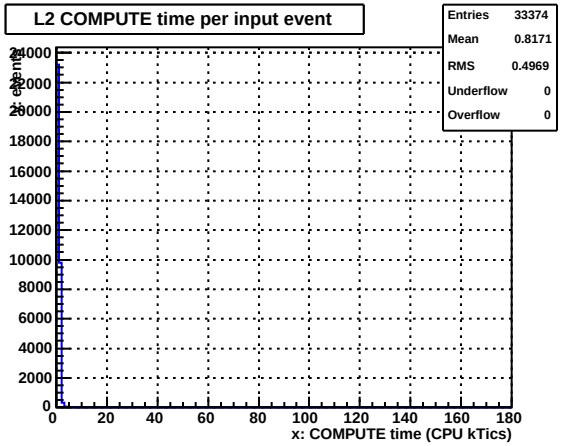
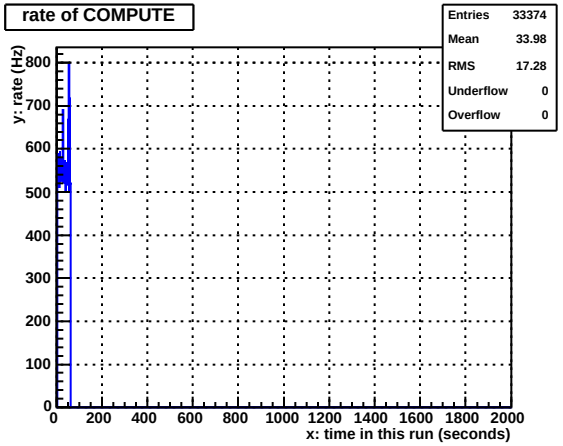
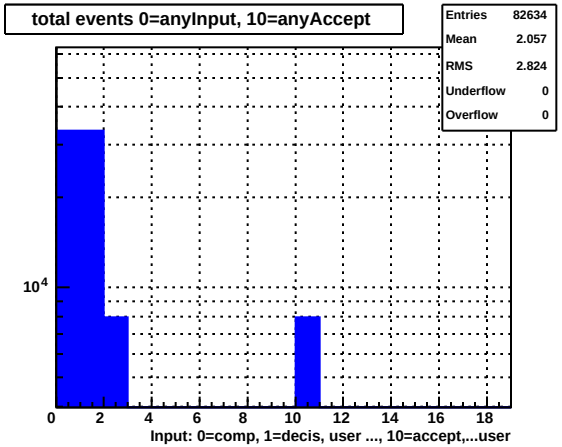
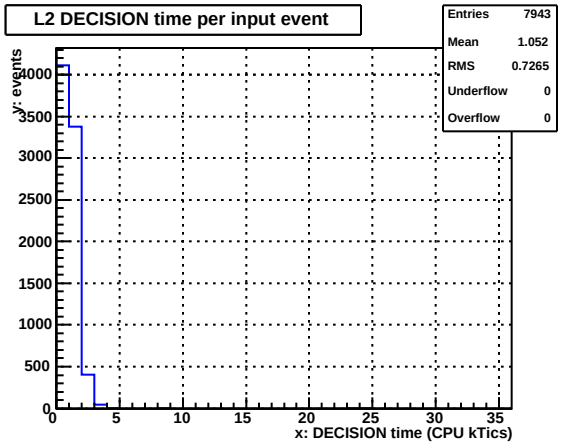
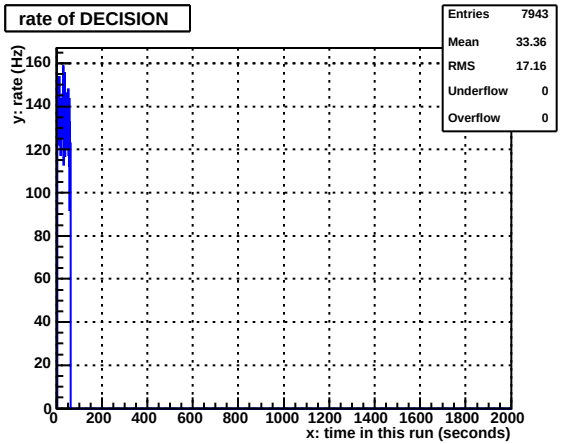
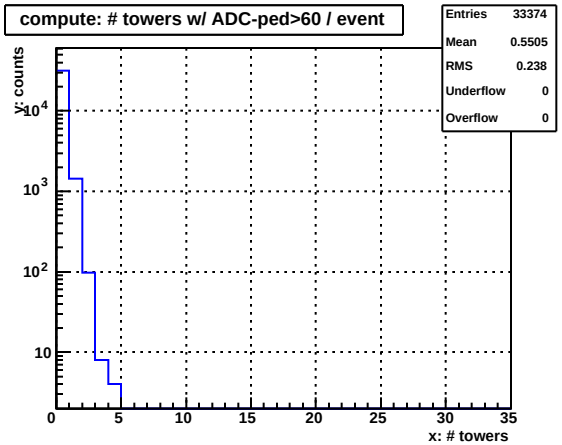

L2 COMPUTE time per input event
Entries 33374
Mean 0.8171
RMS 0.4969
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

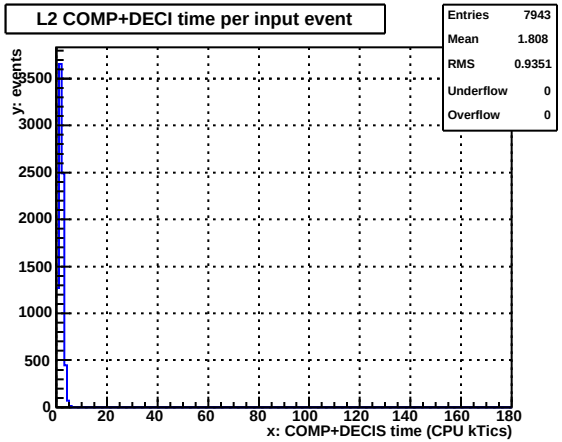

rate of COMPUTE
Entries 33374
Mean 33.98
RMS 17.28
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

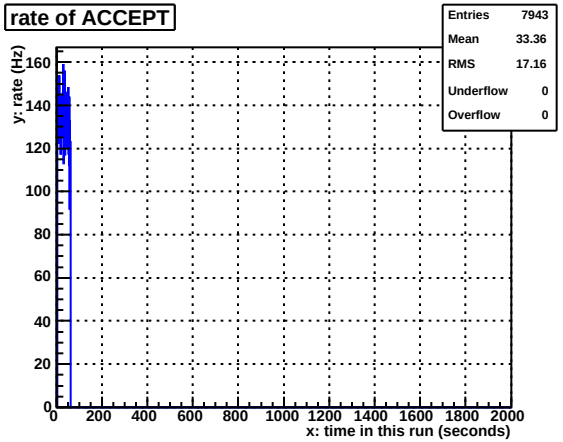

total events 0=anyInput, 10=anyAccept
Entries 82634
Mean 2.057
RMS 2.824
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

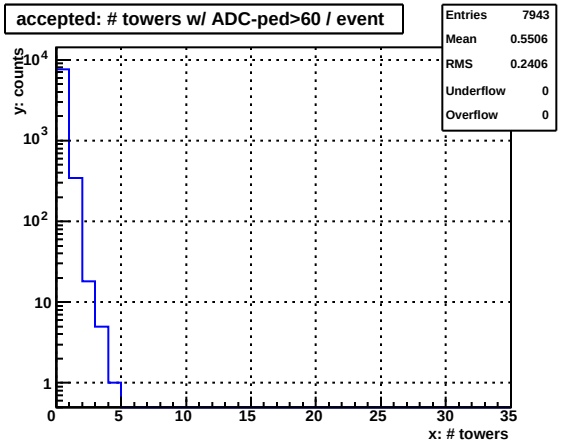

L2 DECISION time per input event
Entries 7943
Mean 1.052
RMS 0.7265
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

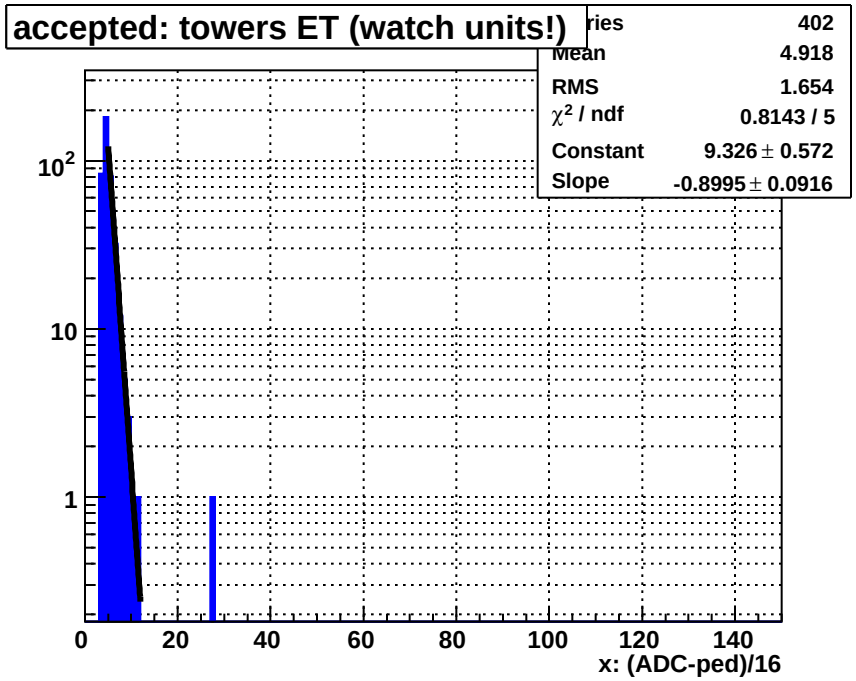

rate of DECISION
Entries 7943
Mean 33.36
RMS 17.16
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

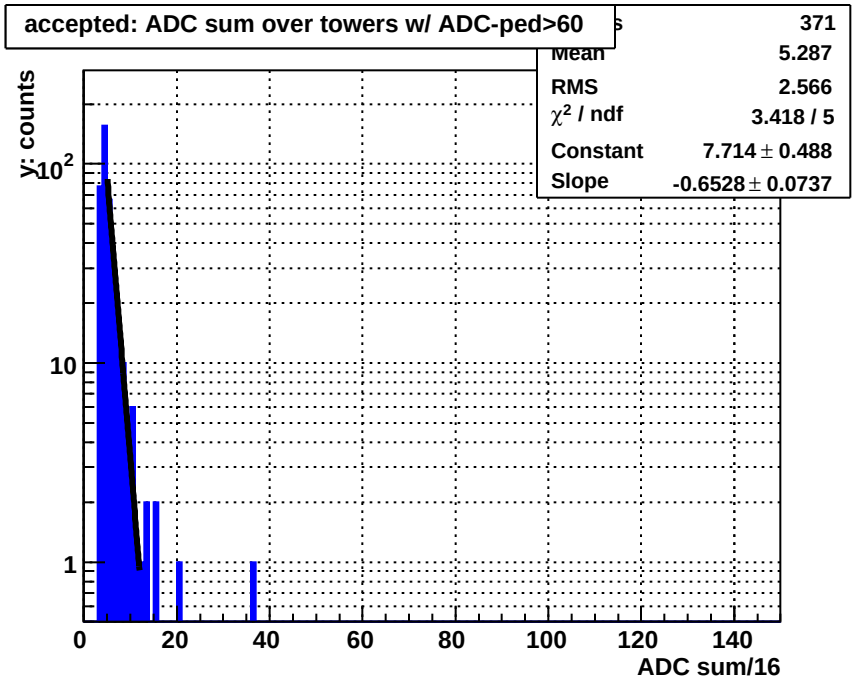

compute: # towers w/ ADC-ped>60 / event
Entries 33374
Mean 0.5505
RMS 0.238
Underflow 0
Overflow 0
y: counts
x: # towers

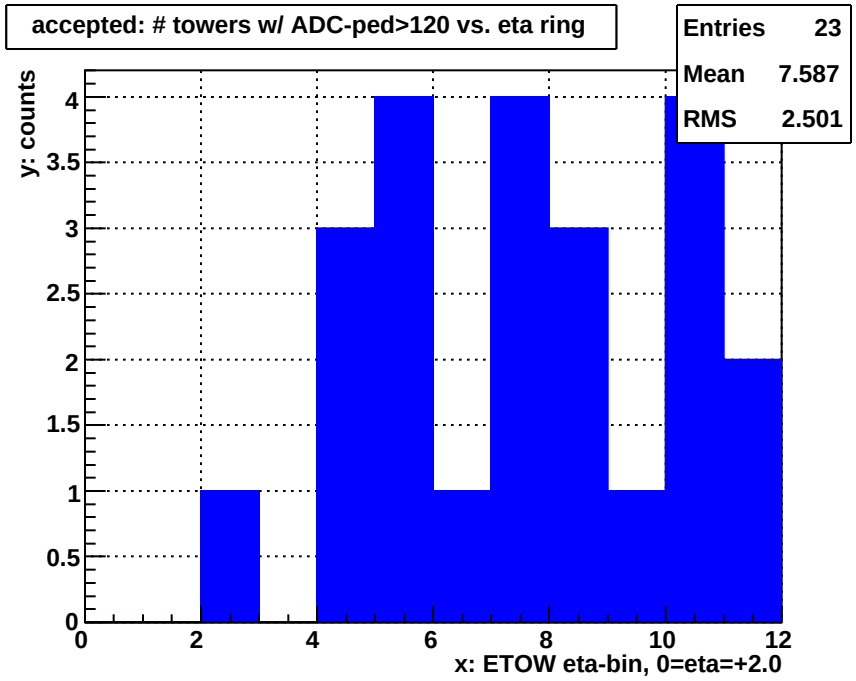

L2 COMP+DECI time per input event
Entries 7943
Mean 1.808
RMS 0.9351
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

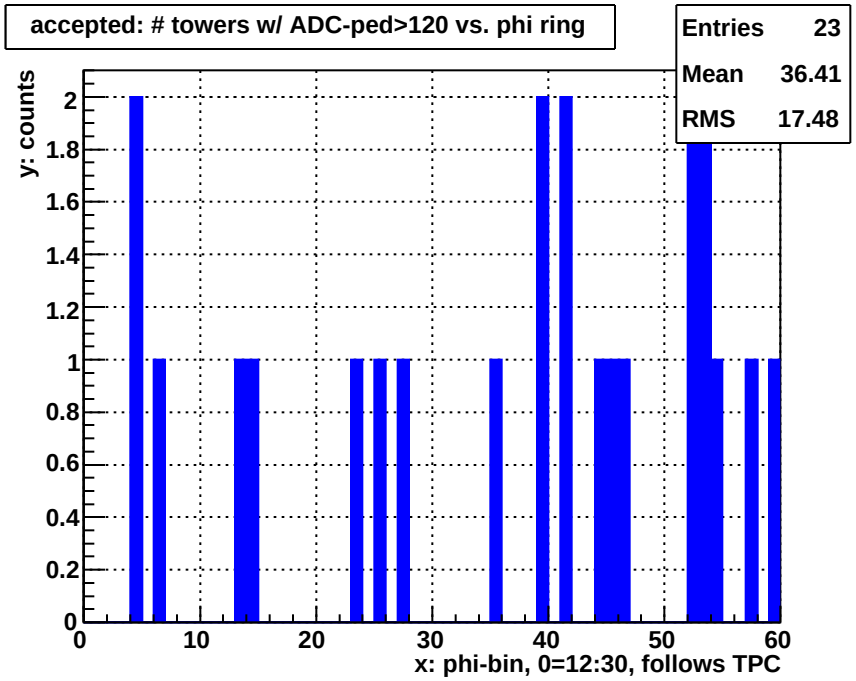

rate of ACCEPT
Entries 7943
Mean 33.36
RMS 17.16
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 7943
Mean 0.5506
RMS 0.2406
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 402
Mean 4.918
RMS 1.654
χ² / ndf 0.8143 / 5
Constant 9.326 ± 0.572
Slope -0.8995 ± 0.0916
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 371
Mean 5.287
RMS 2.566
χ² / ndf 3.418 / 5
Constant 7.714 ± 0.488
Slope -0.6528 ± 0.0737
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 23
Mean 7.587
RMS 2.501
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 23
Mean 36.41
RMS 17.48
y: counts
x: phi-bin, 0=12:30, follows TPC