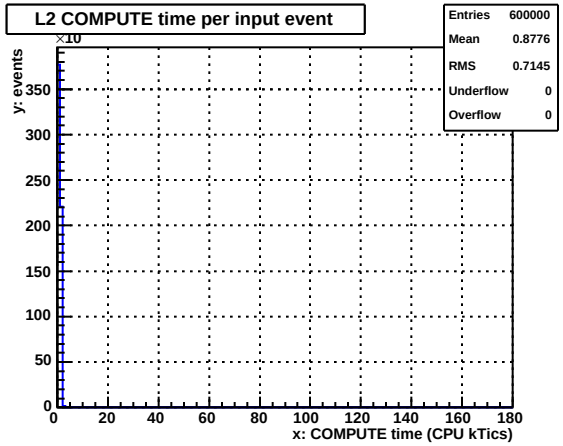
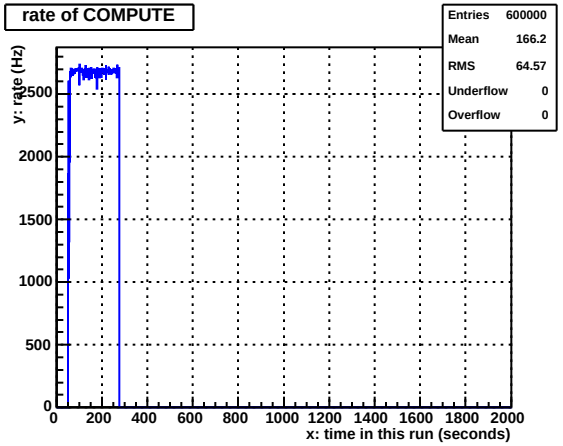
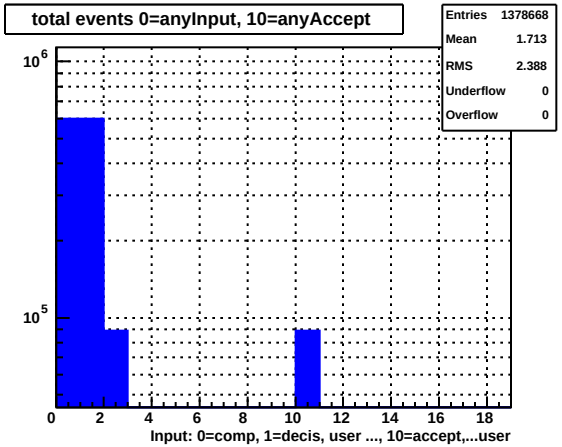
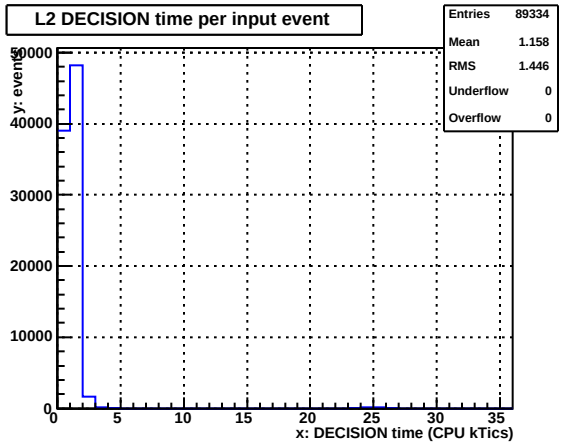
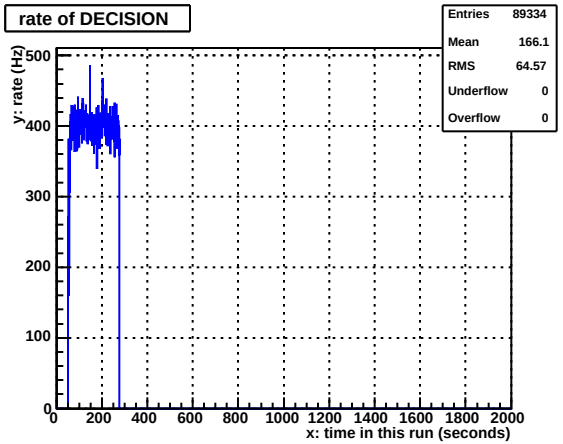
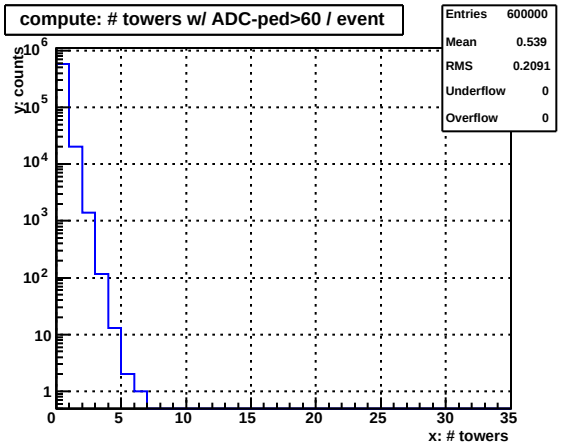

L2 COMPUTE time per input event
Entries 600000
Mean 0.8776
RMS 0.7145
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

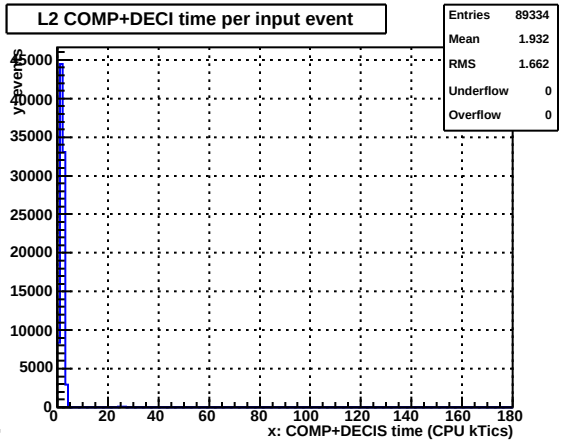

rate of COMPUTE
Entries 600000
Mean 166.2
RMS 64.57
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

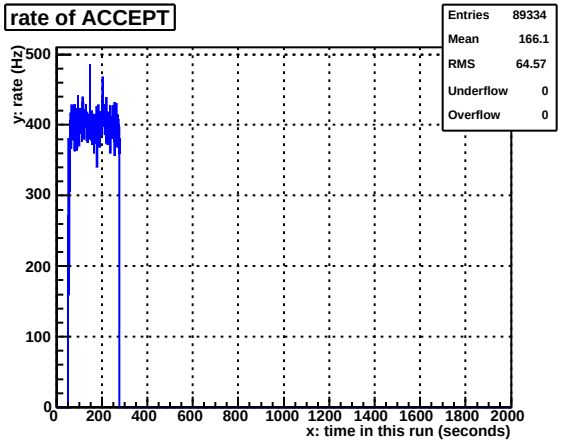

total events 0=anyInput, 10=anyAccept
Entries 1378668
Mean 1.713
RMS 2.388
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

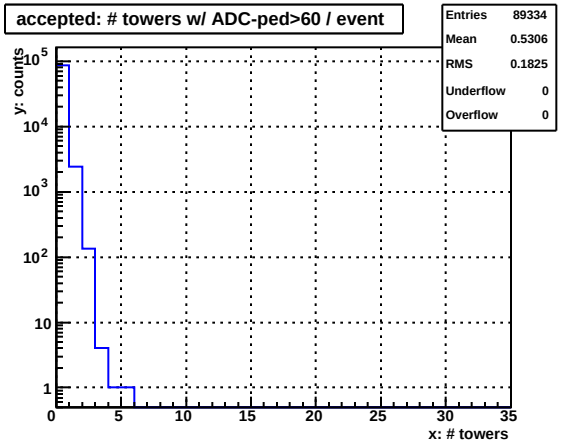

L2 DECISION time per input event
Entries 89334
Mean 1.158
RMS 1.446
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

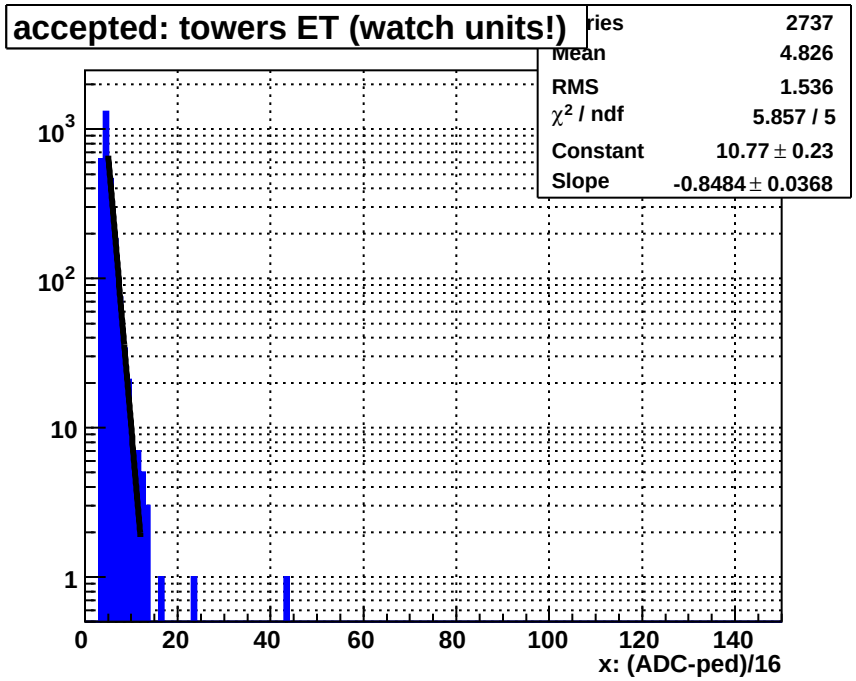

rate of DECISION
Entries 89334
Mean 166.1
RMS 64.57
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

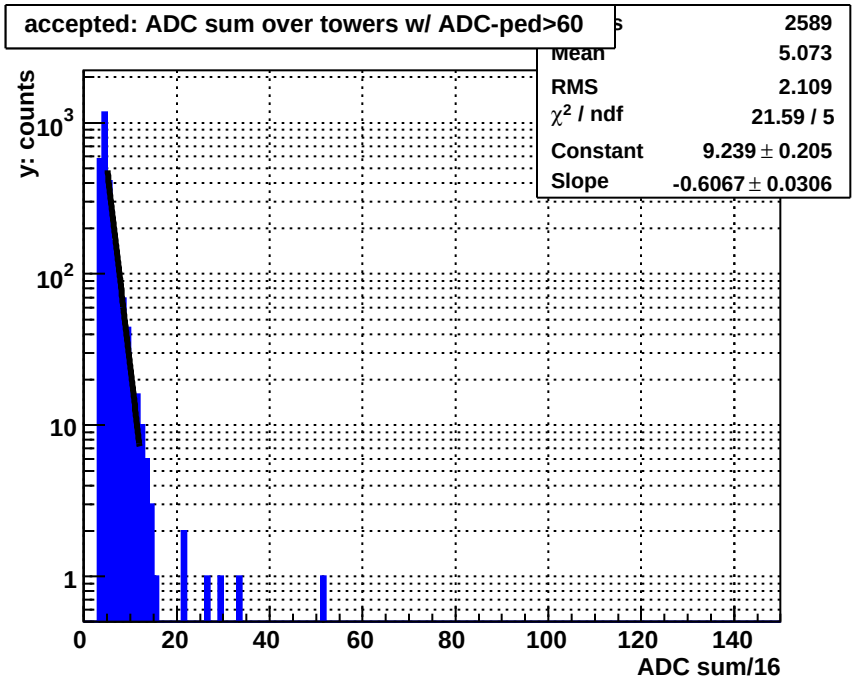

compute: # towers w/ ADC-ped>60 / event
Entries 600000
Mean 0.539
RMS 0.2091
Underflow 0
Overflow 0
y: counts
x: # towers

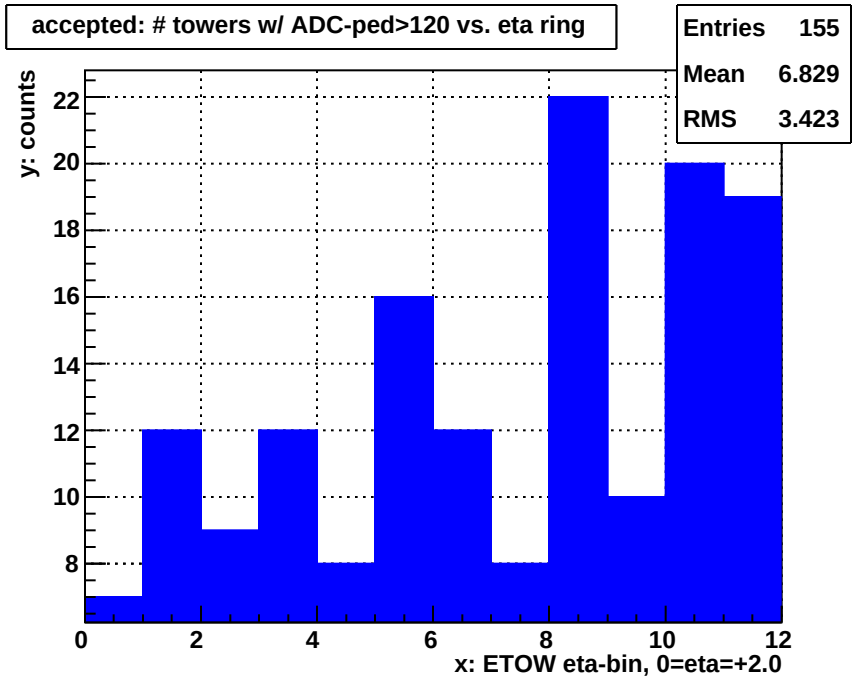

L2 COMP+DECI time per input event
Entries 89334
Mean 1.932
RMS 1.662
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

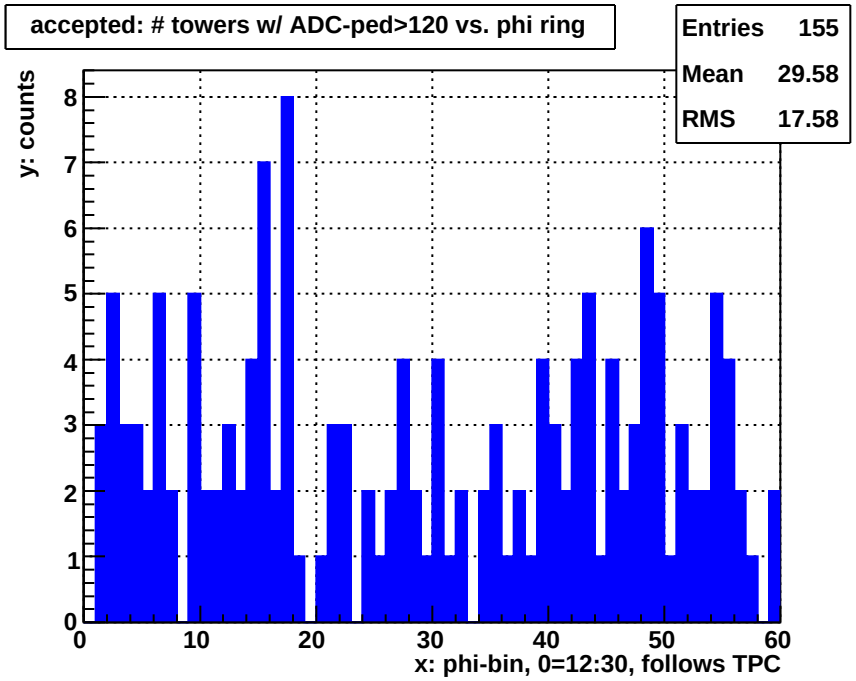

rate of ACCEPT
Entries 89334
Mean 166.1
RMS 64.57
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 89334
Mean 0.5306
RMS 0.1825
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 2737
Mean 4.826
RMS 1.536
χ² / ndf 5.857 / 5
Constant 10.77 ± 0.23
Slope -0.8484 ± 0.0368
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 2589
Mean 5.073
RMS 2.109
χ² / ndf 21.59 / 5
Constant 9.239 ± 0.205
Slope -0.6067 ± 0.0306
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 155
Mean 6.829
RMS 3.423
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 155
Mean 29.58
RMS 17.58
y: counts
x: phi-bin, 0=12:30, follows TPC