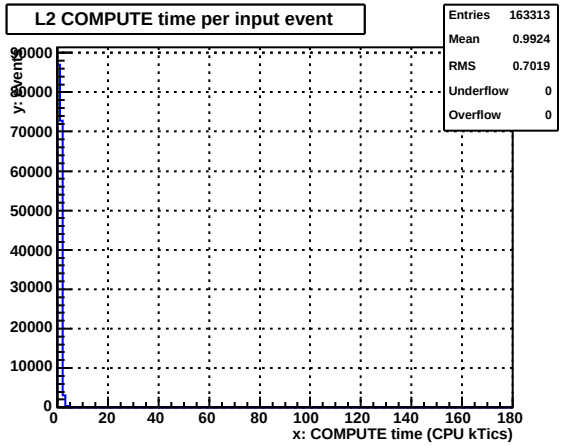
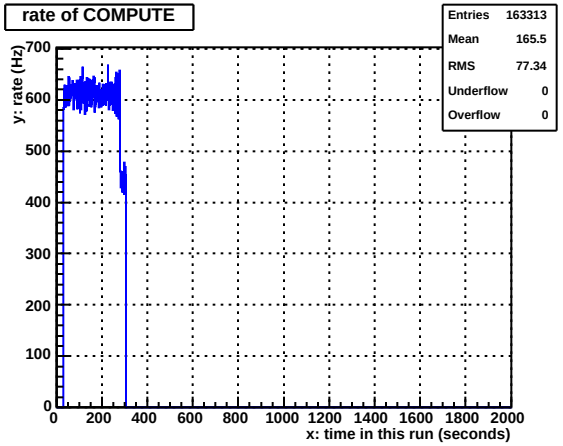
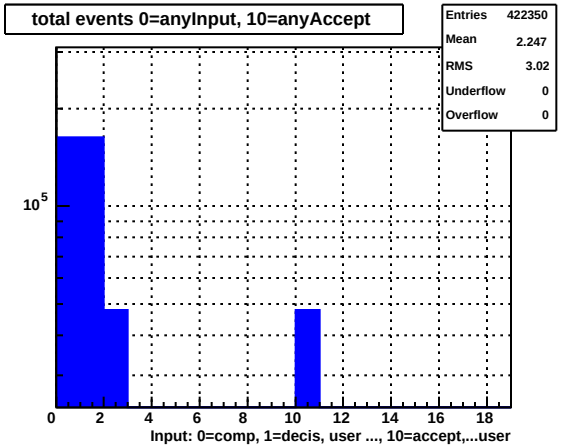
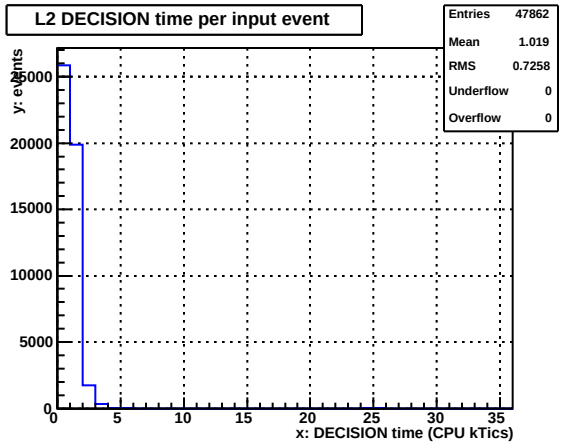
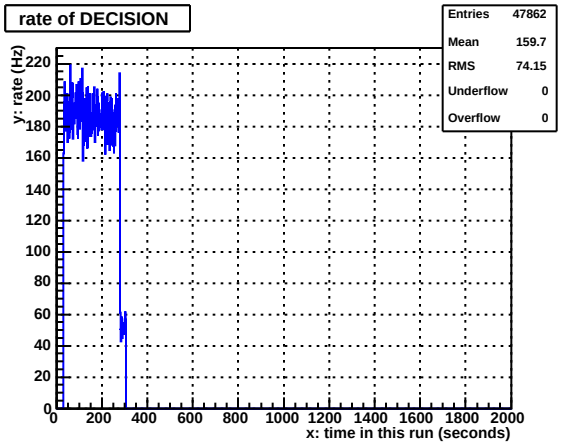
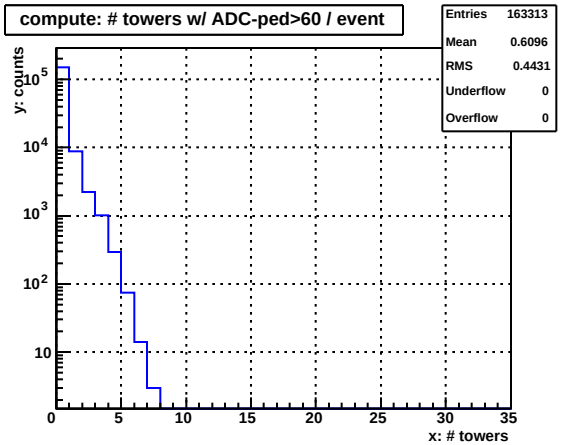

L2 COMPUTE time per input event
Entries 163313
Mean 0.9924
RMS 0.7019
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

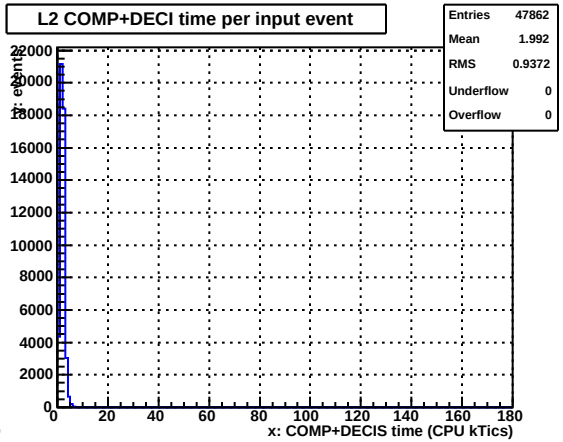

rate of COMPUTE
Entries 163313
Mean 165.5
RMS 77.34
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

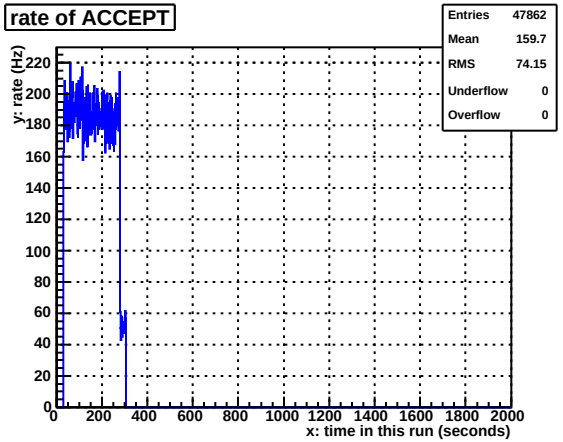

total events 0=anyInput, 10=anyAccept
Entries 422350
Mean 2.247
RMS 3.02
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

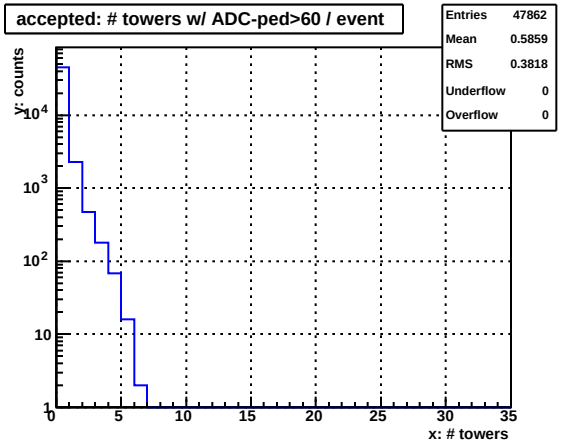

L2 DECISION time per input event
Entries 47862
Mean 1.019
RMS 0.7258
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

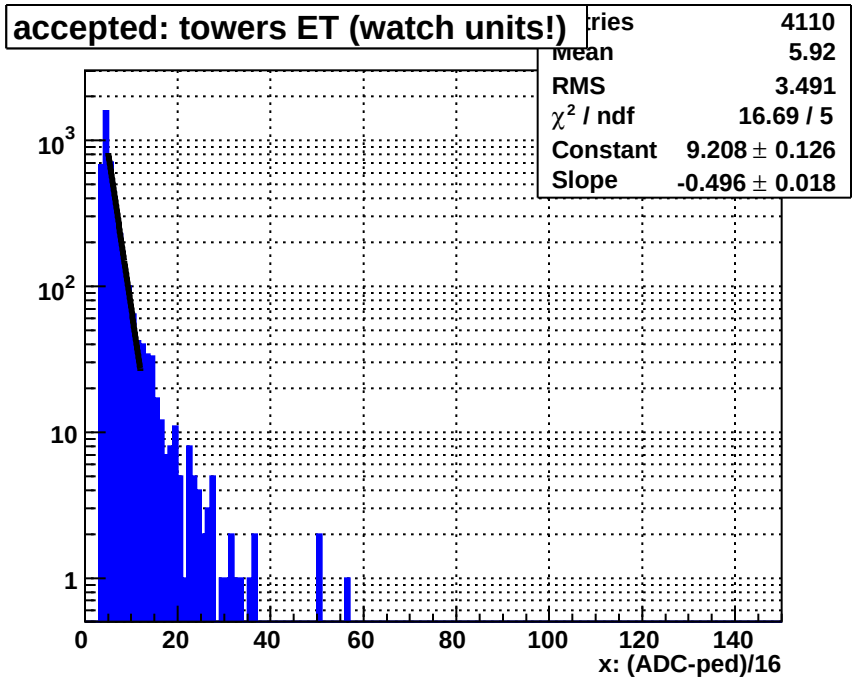

rate of DECISION
Entries 47862
Mean 159.7
RMS 74.15
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

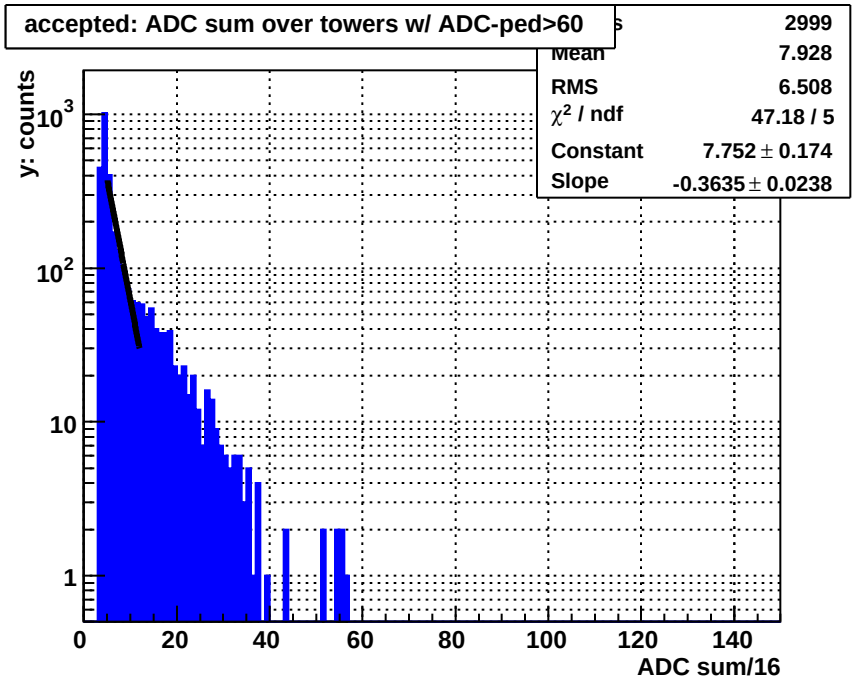

compute: # towers w/ ADC-ped>60 / event
Entries 163313
Mean 0.6096
RMS 0.4431
Underflow 0
Overflow 0
y: counts
x: # towers

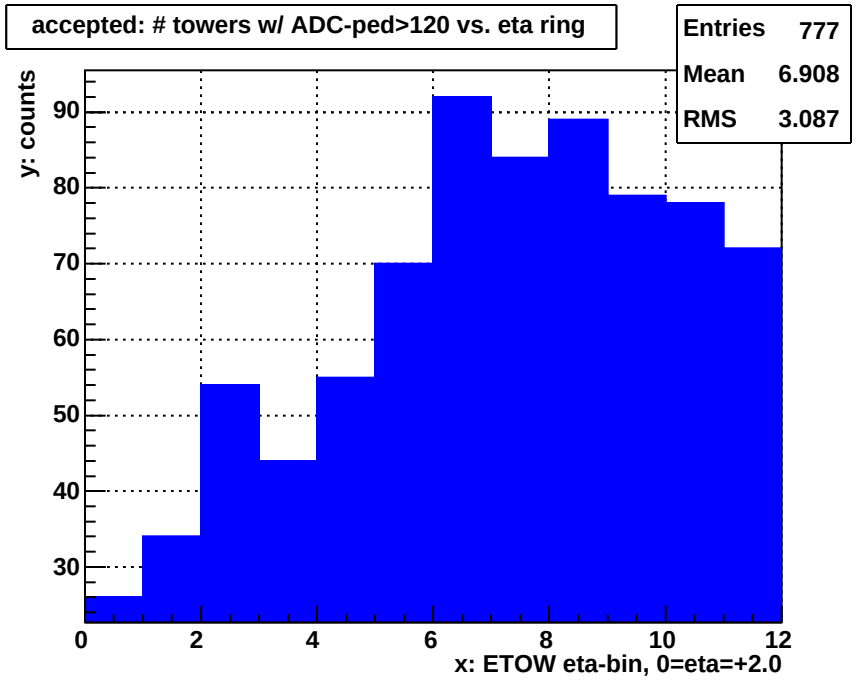

L2 COMP+DECI time per input event
Entries 47862
Mean 1.992
RMS 0.9372
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

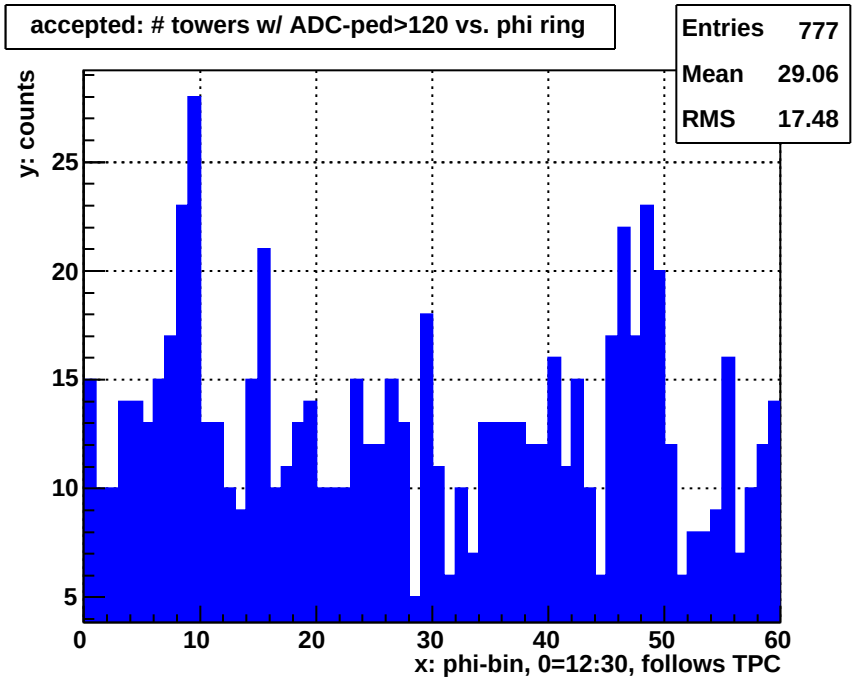

rate of ACCEPT
Entries 47862
Mean 159.7
RMS 74.15
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 47862
Mean 0.5859
RMS 0.3818
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
Entries 4110
Mean 5.92
RMS 3.491
χ² / ndf 16.69 / 5
Constant 9.208 ± 0.126
Slope -0.496 ± 0.018
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
Entries 2999
Mean 7.928
RMS 6.508
χ² / ndf 47.18 / 5
Constant 7.752 ± 0.174
Slope -0.3635 ± 0.0238
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 777
Mean 6.908
RMS 3.087
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 777
Mean 29.06
RMS 17.48
y: counts
x: phi-bin, 0=12:30, follows TPC