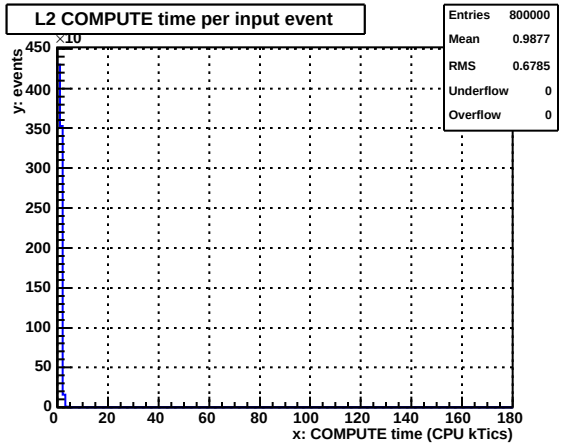
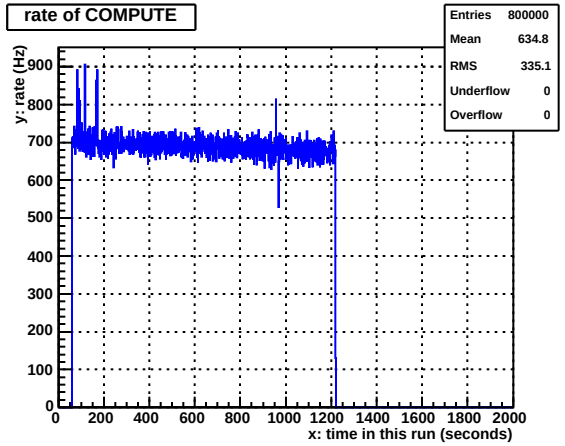
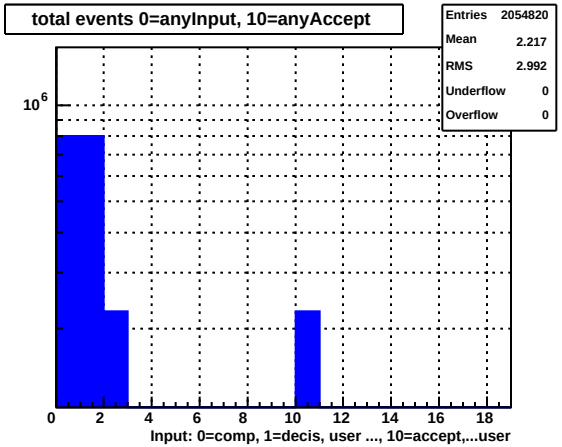
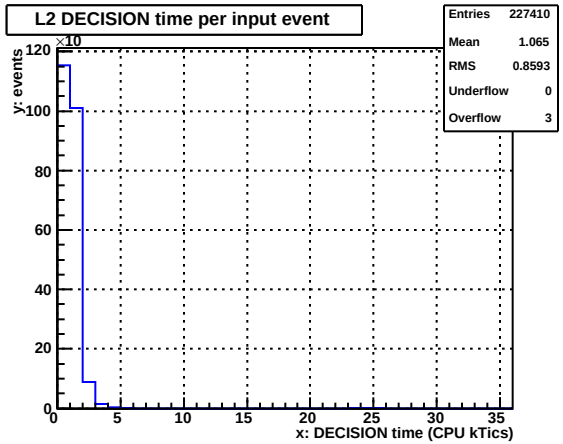
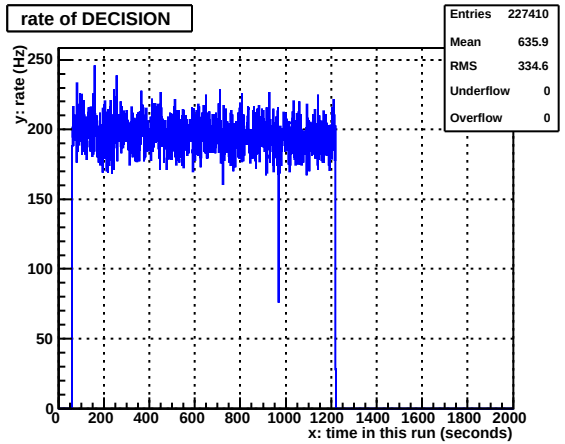
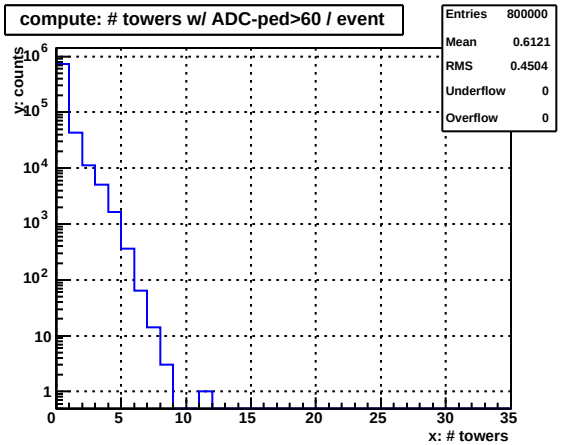

L2 COMPUTE time per input event
Entries 800000
Mean 0.9877
RMS 0.6785
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

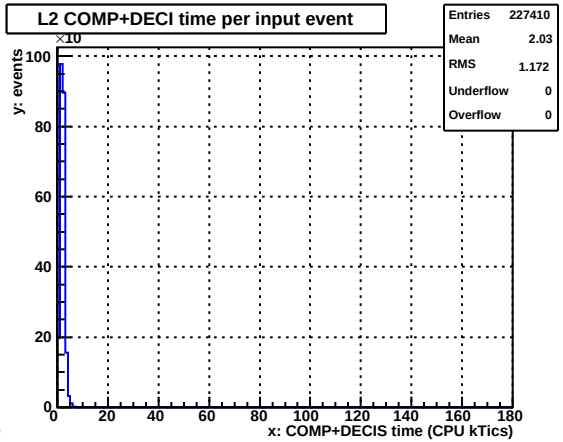

rate of COMPUTE
Entries 800000
Mean 634.8
RMS 335.1
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

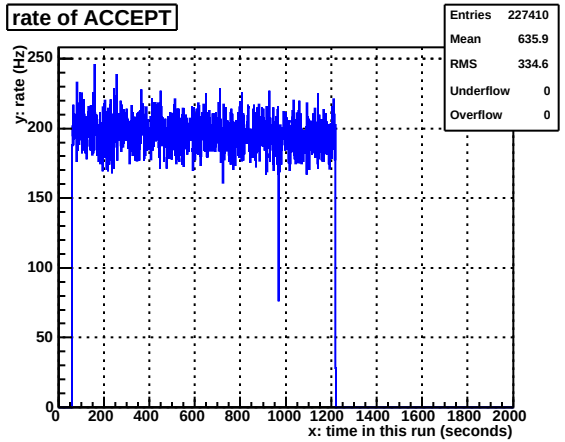

total events 0=anyInput, 10=anyAccept
Entries 2054820
Mean 2.217
RMS 2.992
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

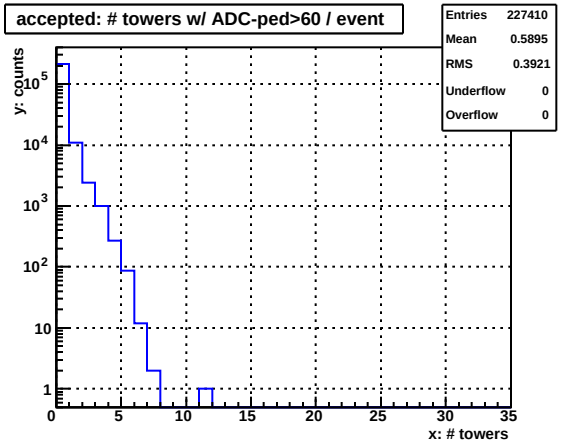

L2 DECISION time per input event
Entries 227410
Mean 1.065
RMS 0.8593
Underflow 0
Overflow 3
y: events
x: DECISION time (CPU kTics)

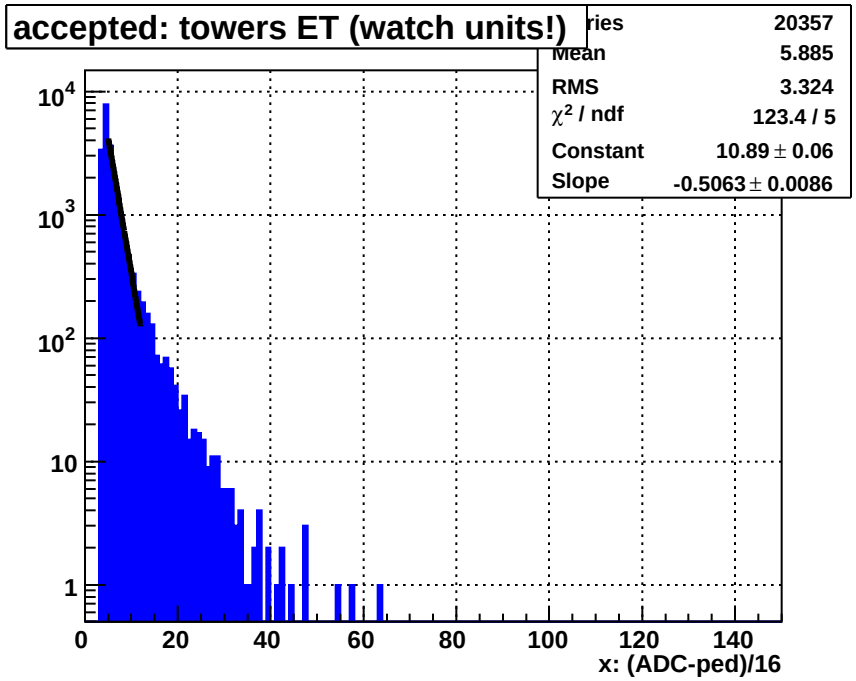

rate of DECISION
Entries 227410
Mean 635.9
RMS 334.6
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

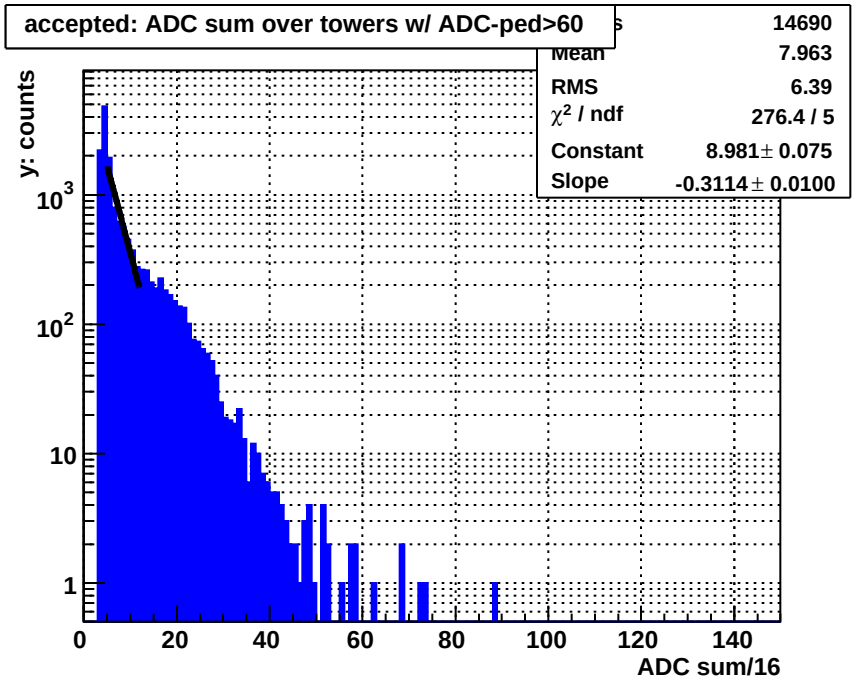

compute: # towers w/ ADC-ped>60 / event
Entries 800000
Mean 0.6121
RMS 0.4504
Underflow 0
Overflow 0
y: counts
x: # towers

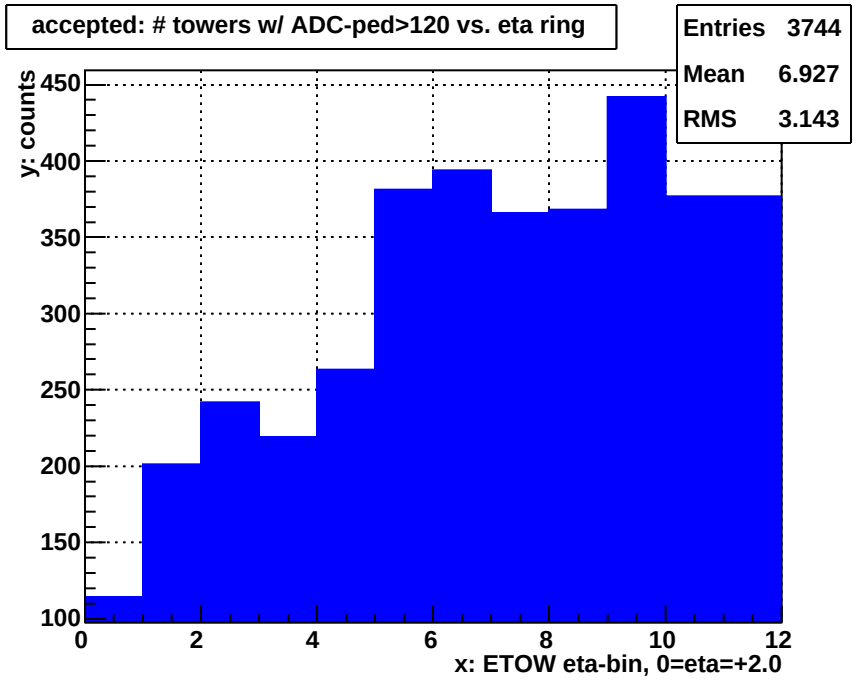

L2 COMP+DECI time per input event
Entries 227410
Mean 2.03
RMS 1.172
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

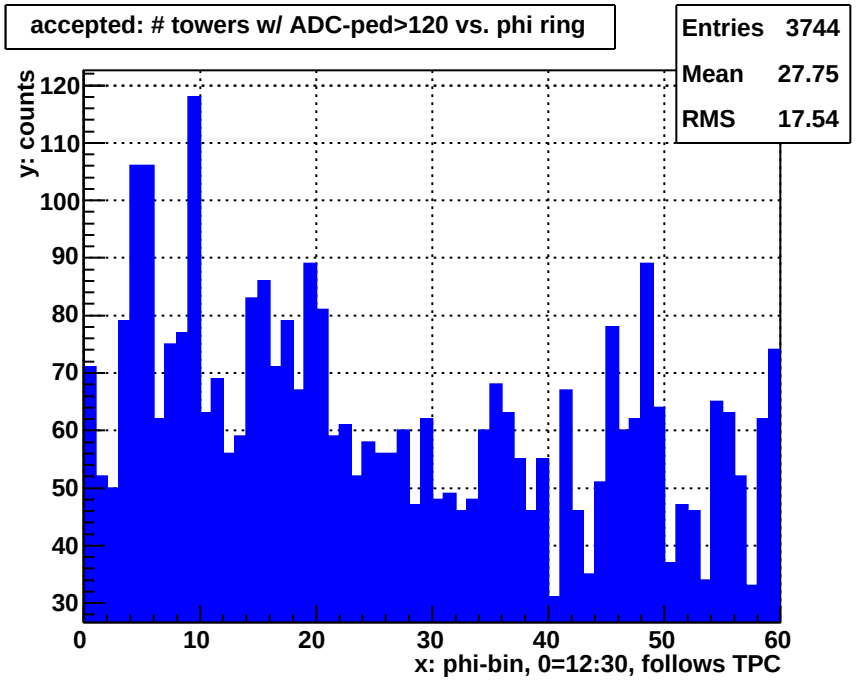

rate of ACCEPT
Entries 227410
Mean 635.9
RMS 334.6
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 227410
Mean 0.5895
RMS 0.3921
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 20357
Mean 5.885
RMS 3.324
χ² / ndf 123.4 / 5
Constant 10.89 ± 0.06
Slope -0.5063 ± 0.0086
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 14690
Mean 7.963
RMS 6.39
χ² / ndf 276.4 / 5
Constant 8.981 ± 0.075
Slope -0.3114 ± 0.0100
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 3744
Mean 6.927
RMS 3.143
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 3744
Mean 27.75
RMS 17.54
y: counts
x: phi-bin, 0=12:30, follows TPC