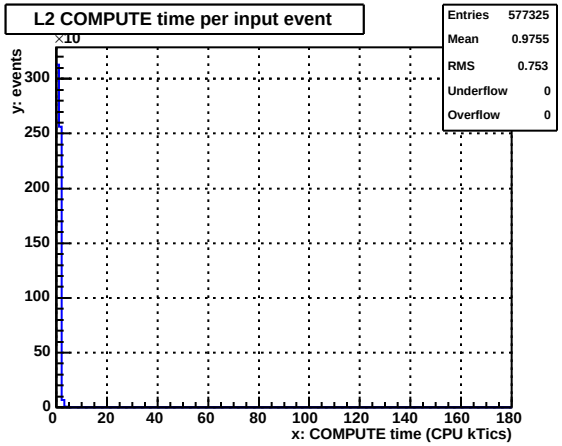
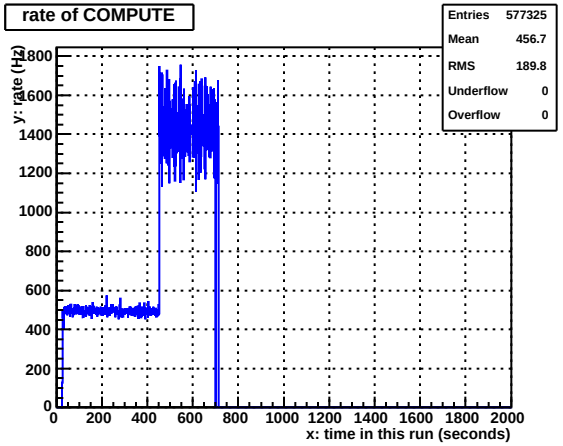
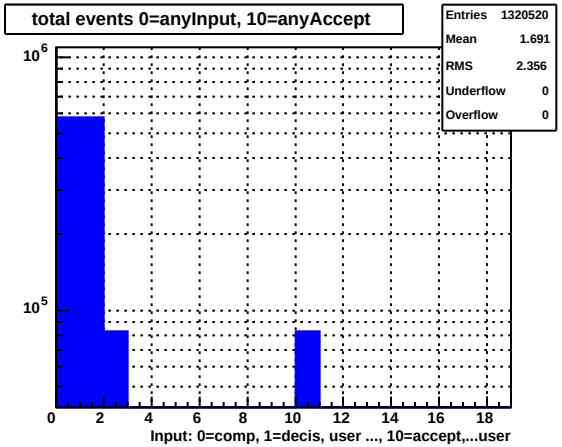
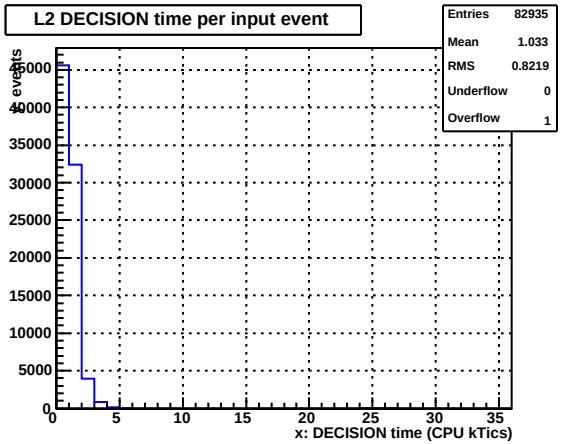
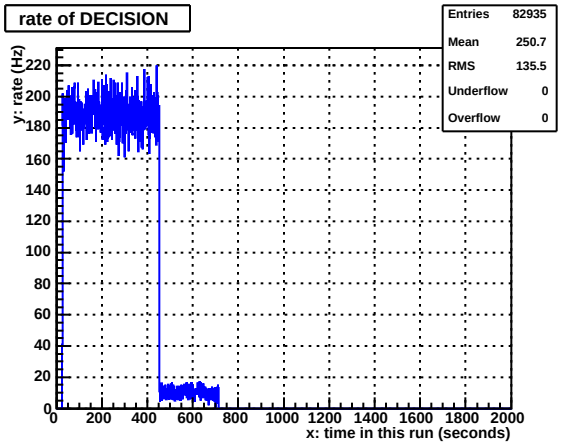
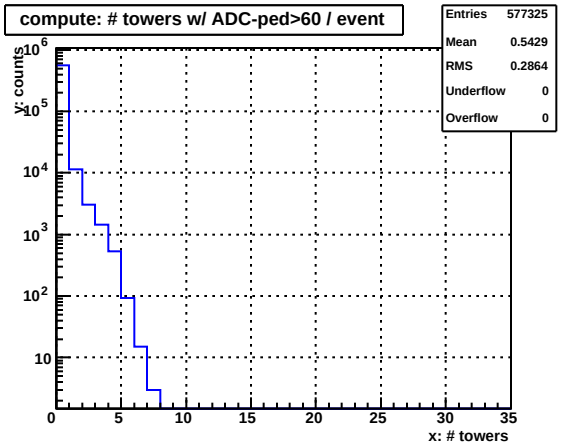

L2 COMPUTE time per input event
Entries 577325
Mean 0.9755
RMS 0.753
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

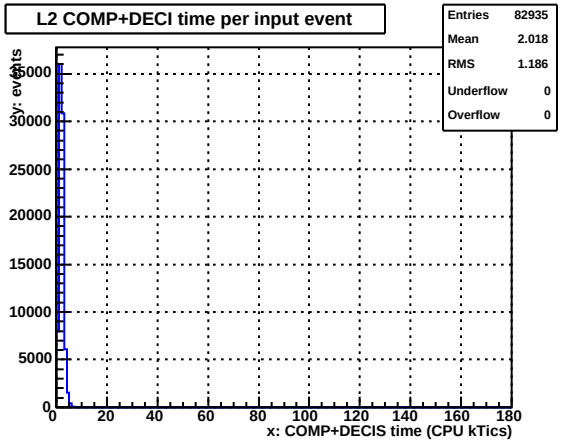

rate of COMPUTE
Entries 577325
Mean 456.7
RMS 189.8
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

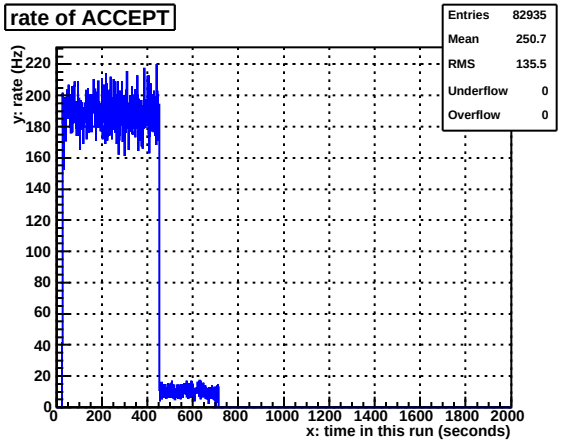

total events 0=anyInput, 10=anyAccept
Entries 1320520
Mean 1.691
RMS 2.356
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

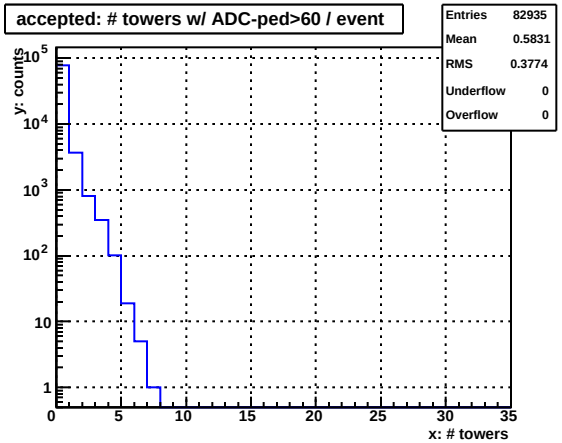

L2 DECISION time per input event
Entries 82935
Mean 1.033
RMS 0.8219
Underflow 0
Overflow 1
y: events
x: DECISION time (CPU kTics)

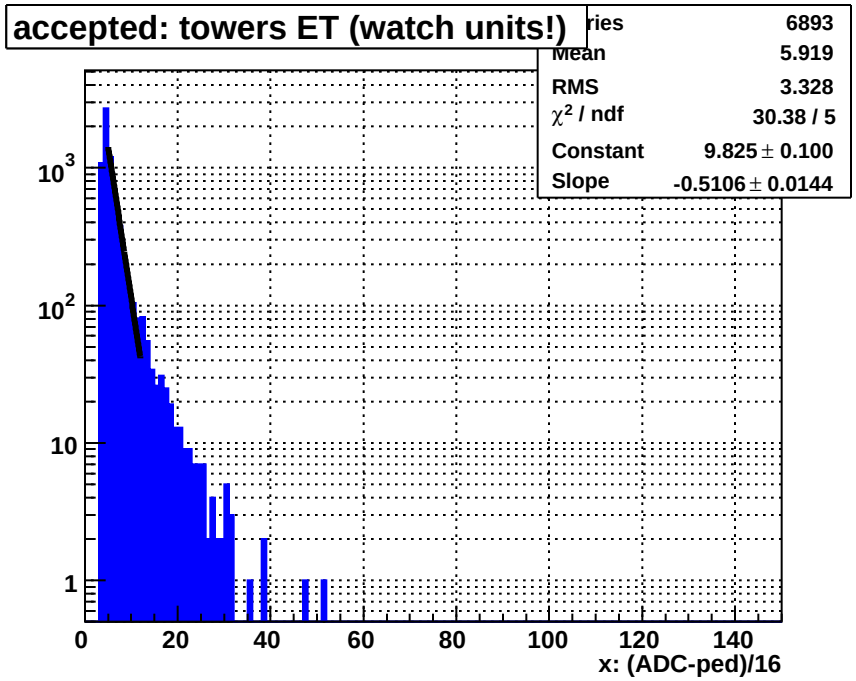

rate of DECISION
Entries 82935
Mean 250.7
RMS 135.5
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

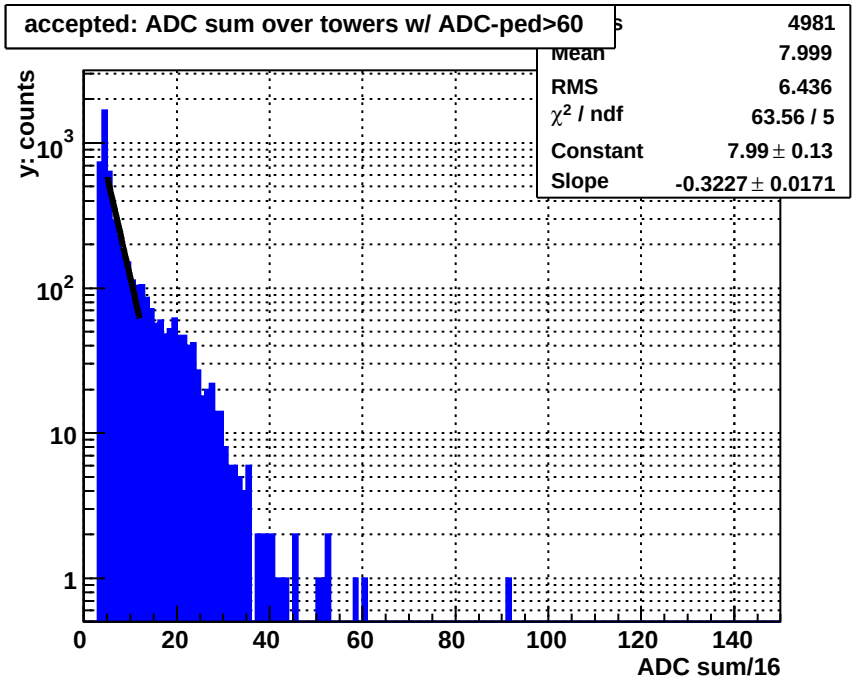

compute: # towers w/ ADC-ped>60 / event
Entries 577325
Mean 0.5429
RMS 0.2864
Underflow 0
Overflow 0
y: counts
x: # towers

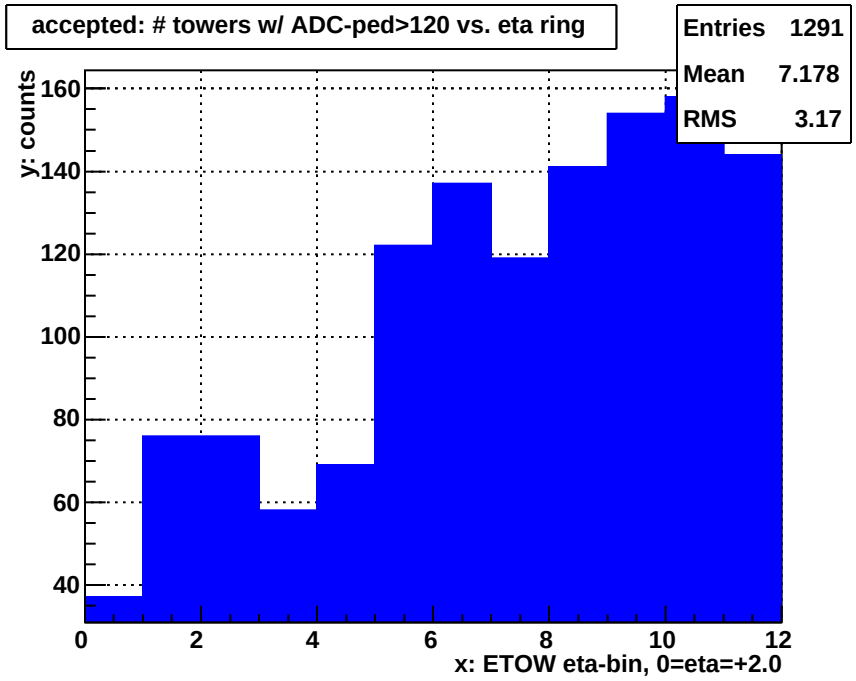

L2 COMP+DECI time per input event
Entries 82935
Mean 2.018
RMS 1.186
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

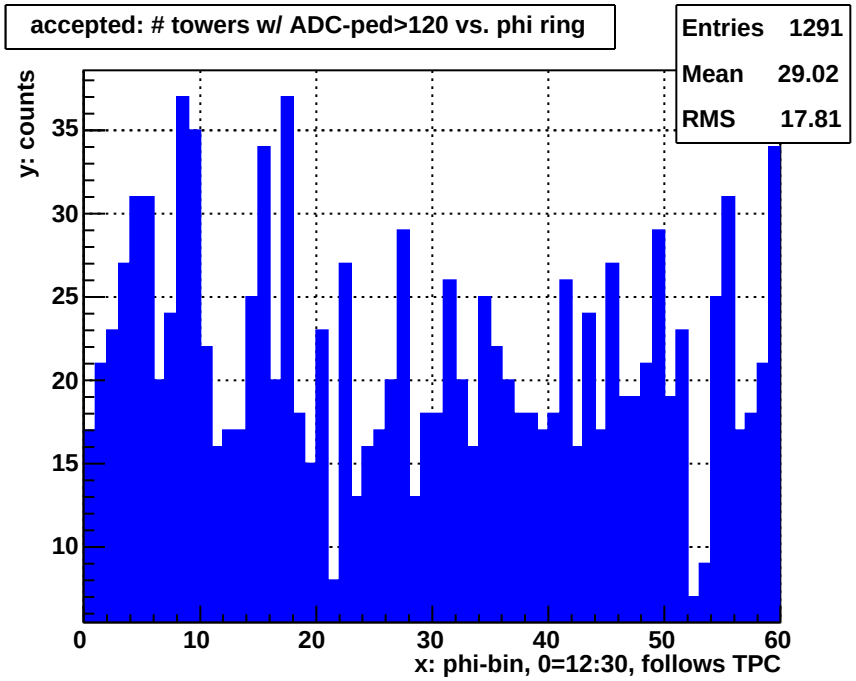

rate of ACCEPT
Entries 82935
Mean 250.7
RMS 135.5
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 82935
Mean 0.5831
RMS 0.3774
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 6893
Mean 5.919
RMS 3.328
χ² / ndf 30.38 / 5
Constant 9.825 ± 0.100
Slope -0.5106 ± 0.0144
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 4981
Mean 7.999
RMS 6.436
χ² / ndf 63.56 / 5
Constant 7.99 ± 0.13
Slope -0.3227 ± 0.0171
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 1291
Mean 7.178
RMS 3.17
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 1291
Mean 29.02
RMS 17.81
y: counts
x: phi-bin, 0=12:30, follows TPC