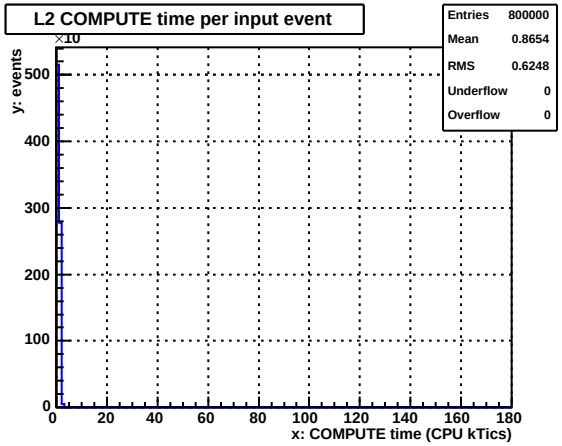
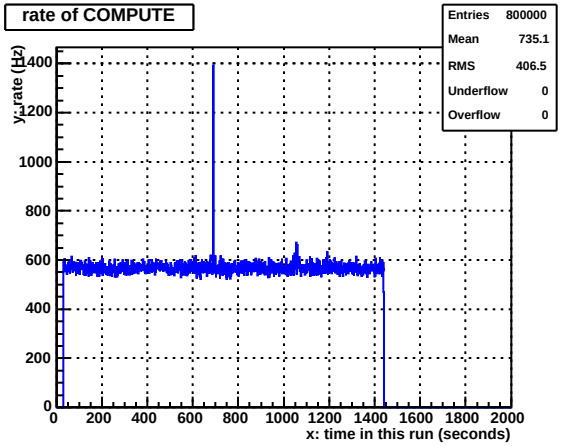
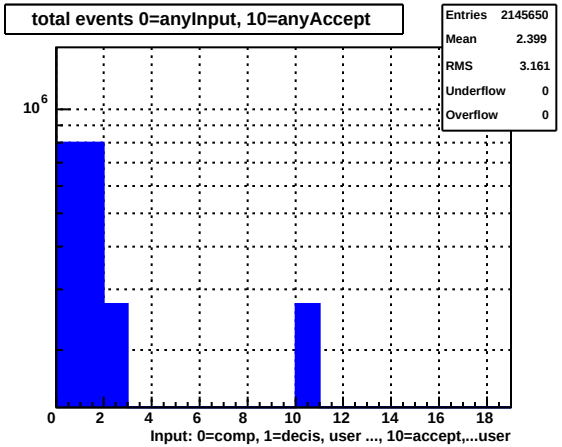
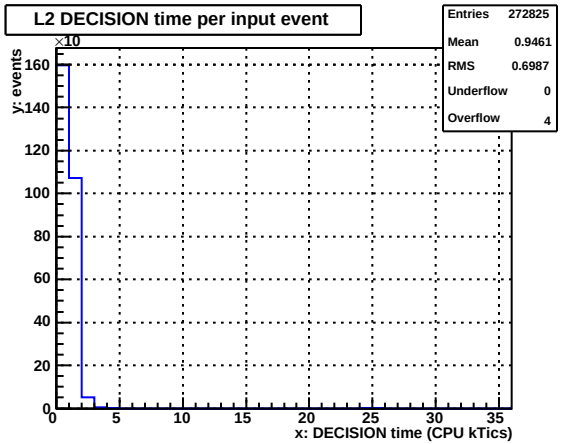
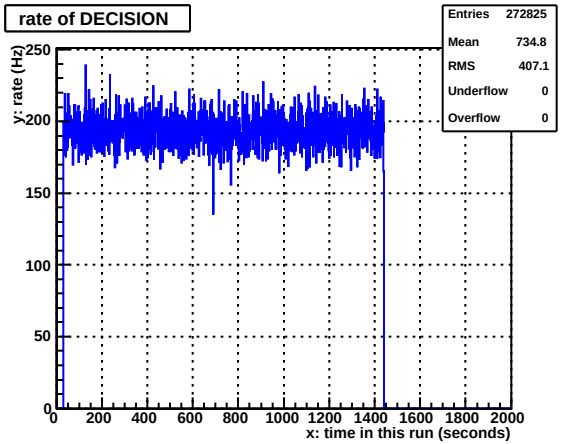
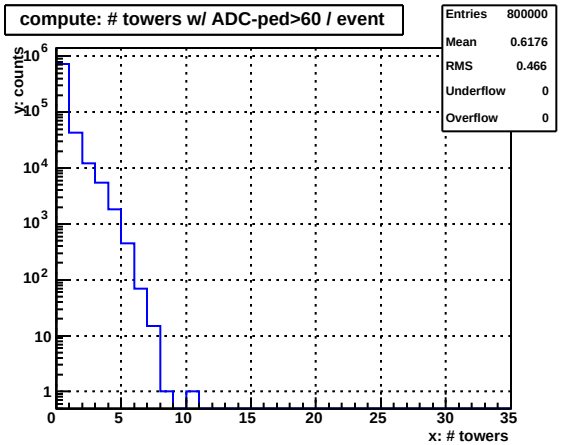

L2 COMPUTE time per input event
Entries 800000
Mean 0.8654
RMS 0.6248
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

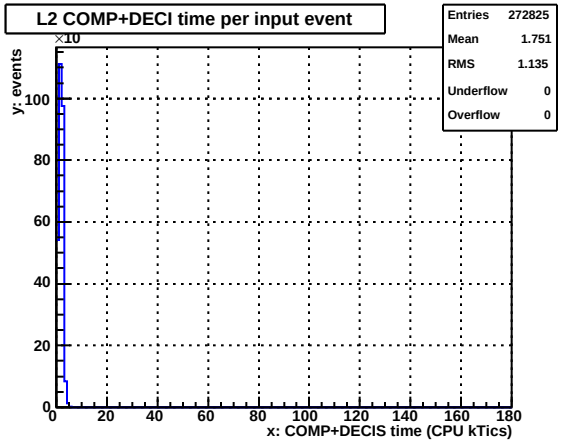

rate of COMPUTE
Entries 800000
Mean 735.1
RMS 406.5
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

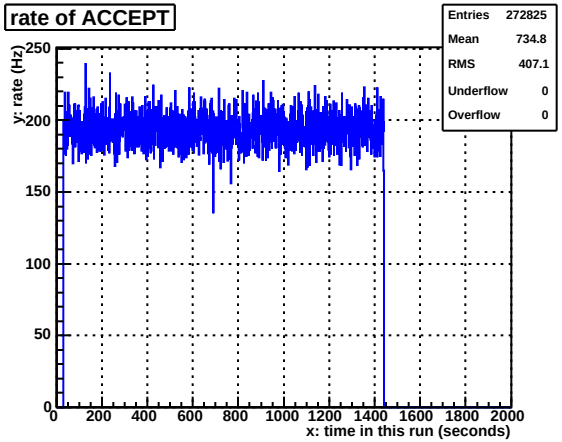

total events 0=anyInput, 10=anyAccept
Entries 2145650
Mean 2.399
RMS 3.161
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

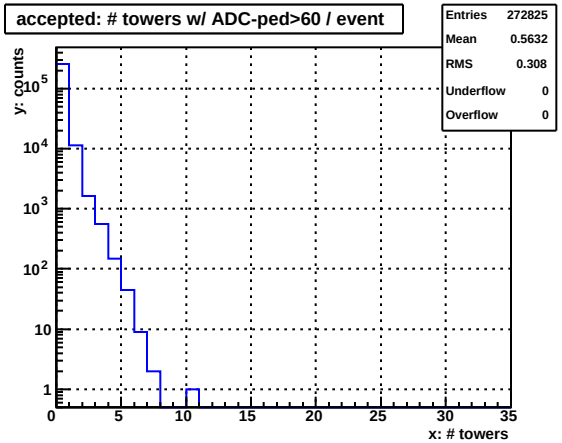

L2 DECISION time per input event
Entries 272825
Mean 0.9461
RMS 0.6987
Underflow 0
Overflow 4
y: events
x: DECISION time (CPU kTics)

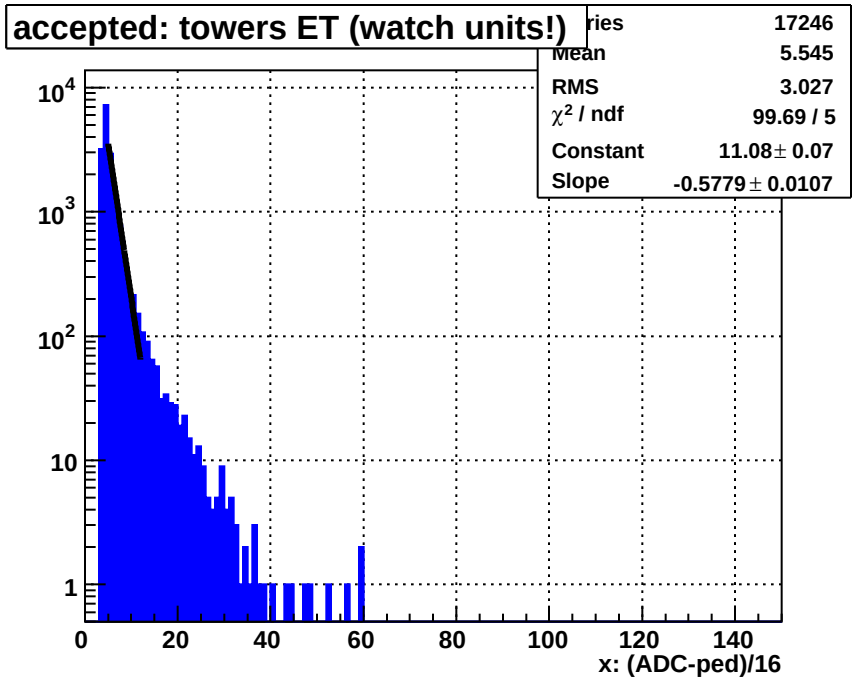

rate of DECISION
Entries 272825
Mean 734.8
RMS 407.1
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

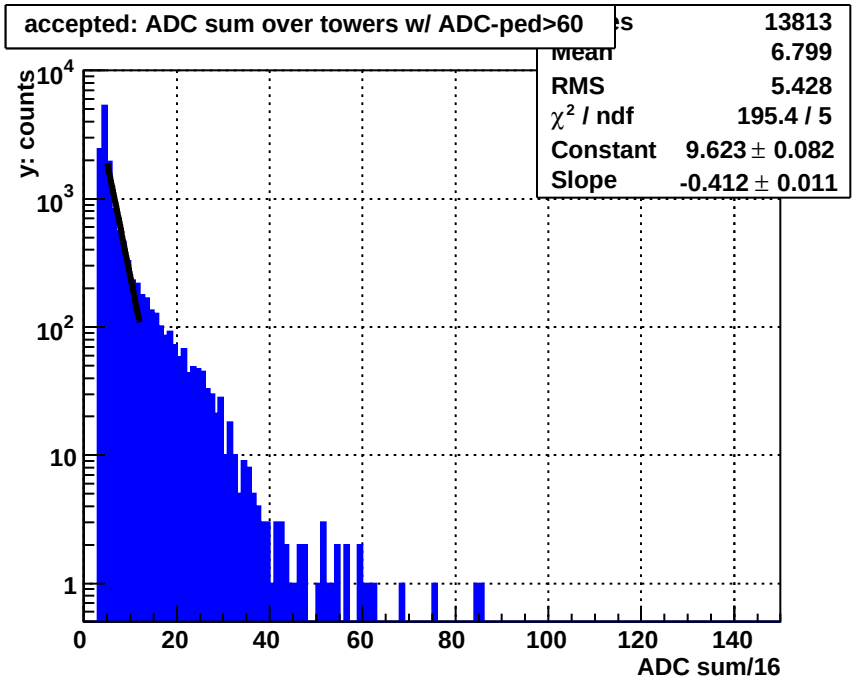

compute: # towers w/ ADC-ped>60 / event
Entries 800000
Mean 0.6176
RMS 0.466
Underflow 0
Overflow 0
y: counts
x: # towers

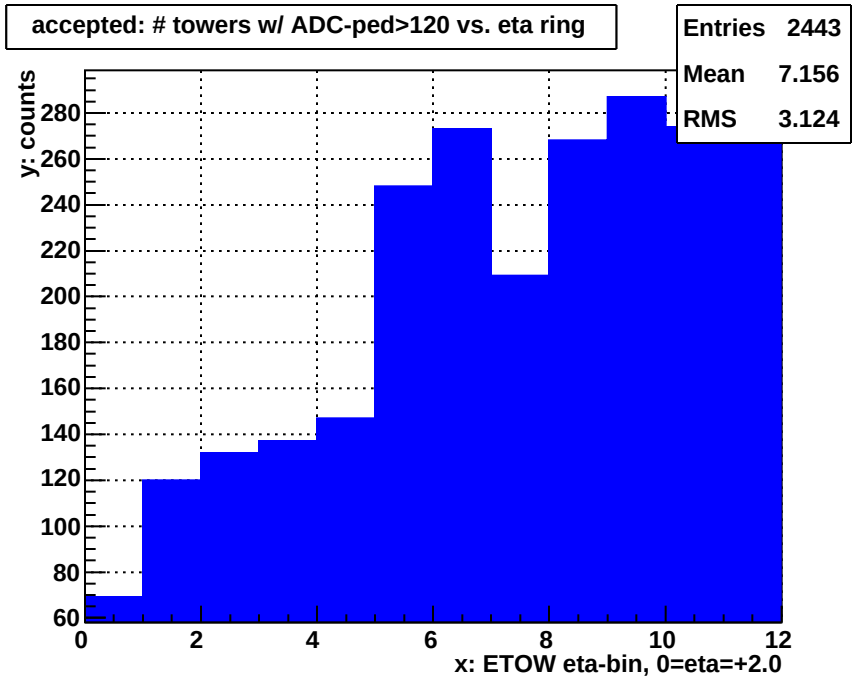

L2 COMP+DECI time per input event
Entries 272825
Mean 1.751
RMS 1.135
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

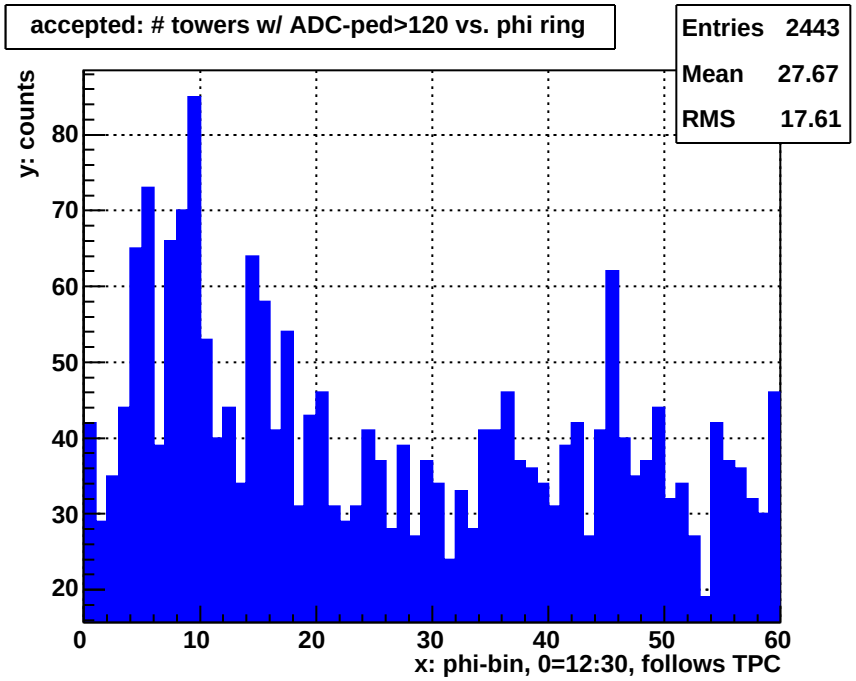

rate of ACCEPT
Entries 272825
Mean 734.8
RMS 407.1
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 272825
Mean 0.5632
RMS 0.308
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 17246
Mean 5.545
RMS 3.027
χ² / ndf 99.69 / 5
Constant 11.08 ± 0.07
Slope -0.5779 ± 0.0107
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
es 13813
Mean 6.799
RMS 5.428
χ² / ndf 195.4 / 5
Constant 9.623 ± 0.082
Slope -0.412 ± 0.011
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 2443
Mean 7.156
RMS 3.124
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 2443
Mean 27.67
RMS 17.61
y: counts
x: phi-bin, 0=12:30, follows TPC