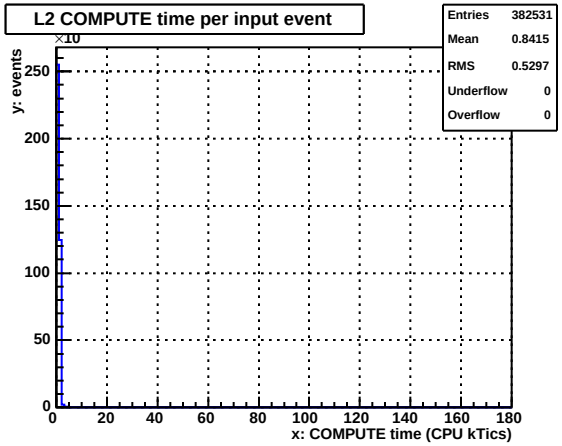
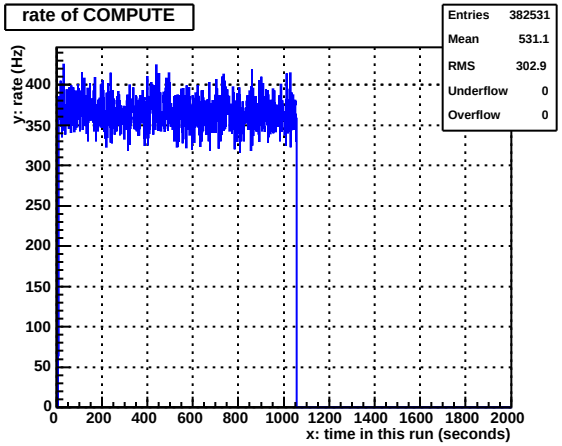
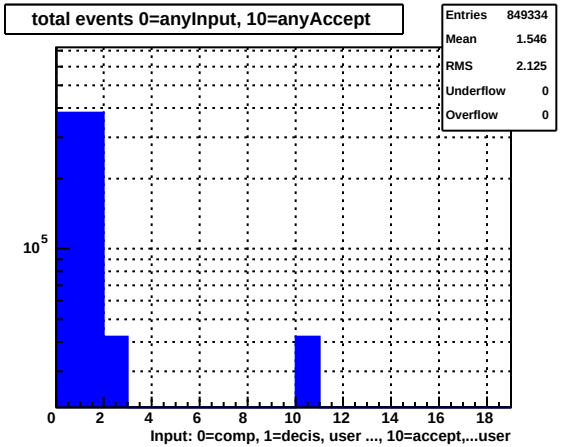
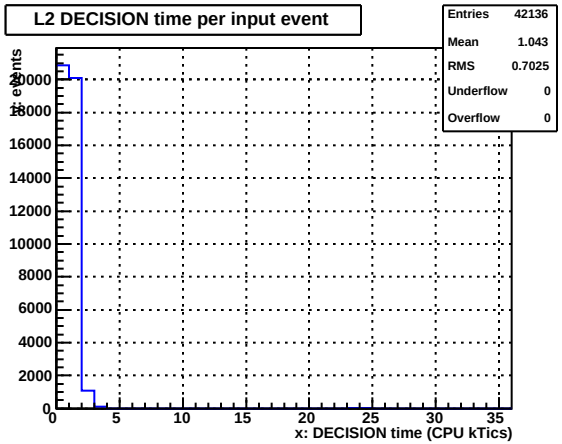
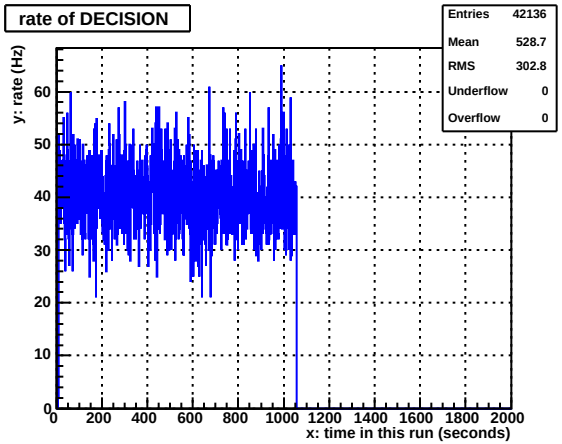
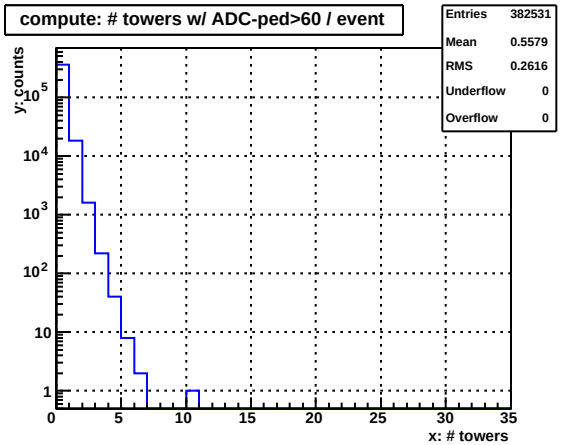

L2 COMPUTE time per input event
Entries 382531
Mean 0.8415
RMS 0.5297
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

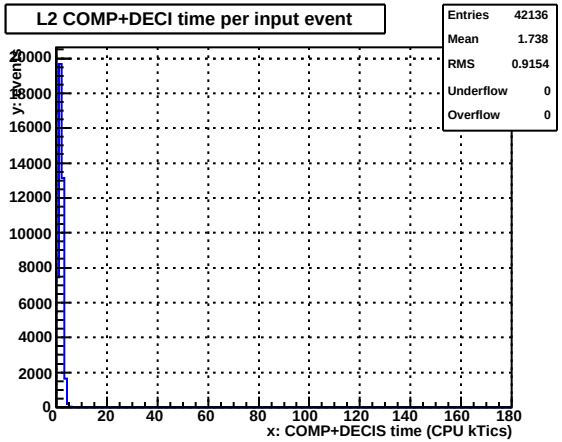

rate of COMPUTE
Entries 382531
Mean 531.1
RMS 302.9
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

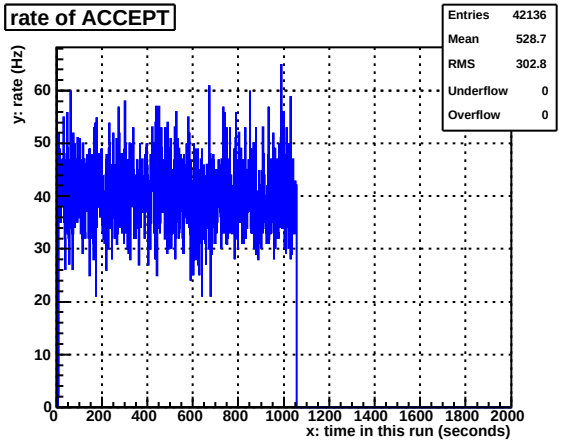

total events 0=anyInput, 10=anyAccept
Entries 849334
Mean 1.546
RMS 2.125
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

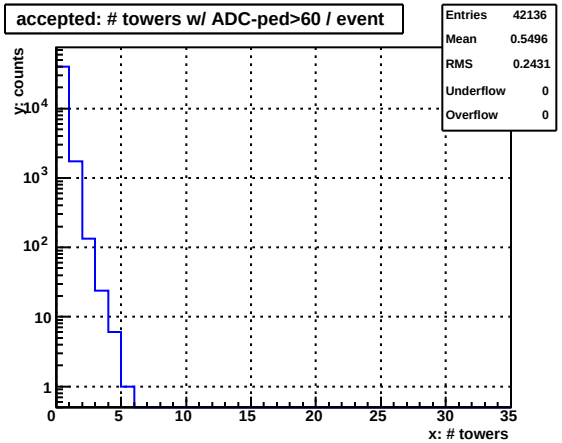

L2 DECISION time per input event
Entries 42136
Mean 1.043
RMS 0.7025
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

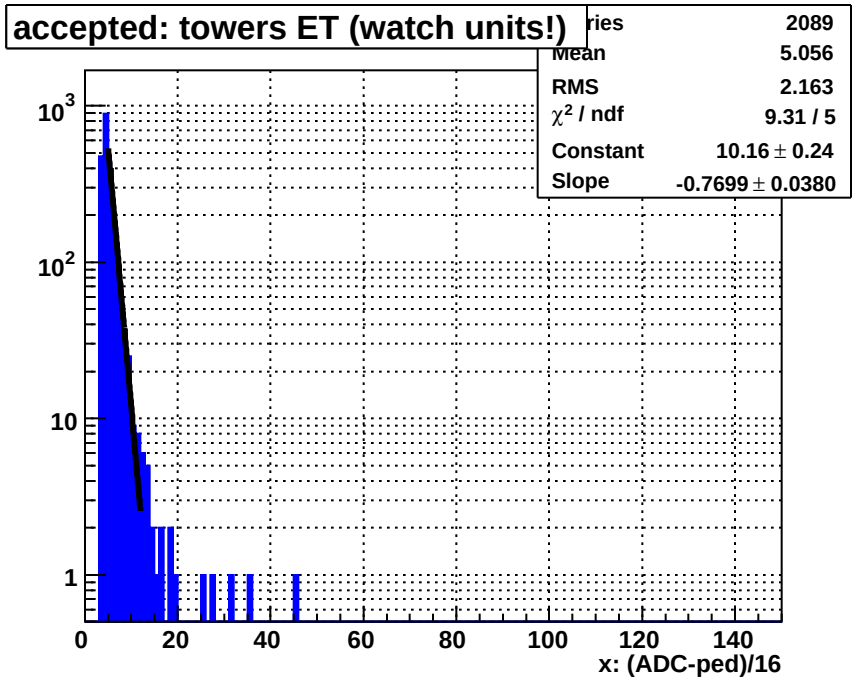

rate of DECISION
Entries 42136
Mean 528.7
RMS 302.8
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

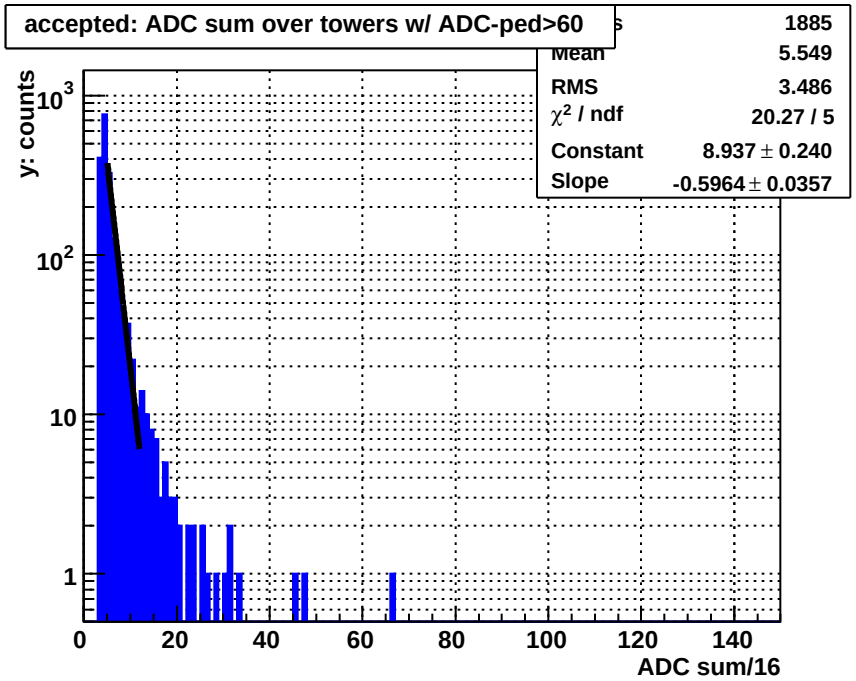

compute: # towers w/ ADC-ped>60 / event
Entries 382531
Mean 0.5579
RMS 0.2616
Underflow 0
Overflow 0
y: counts
x: # towers

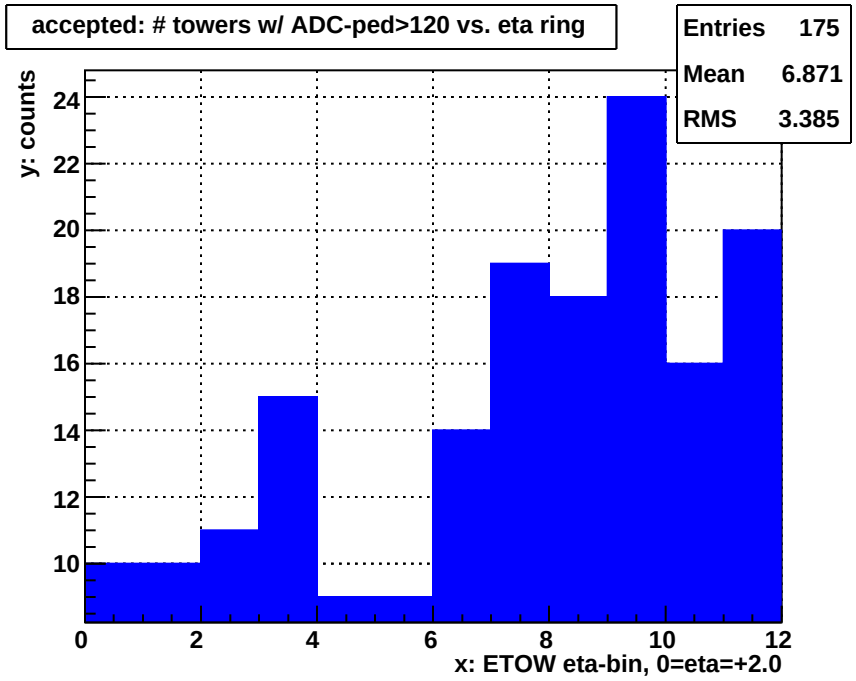

L2 COMP+DECI time per input event
Entries 42136
Mean 1.738
RMS 0.9154
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

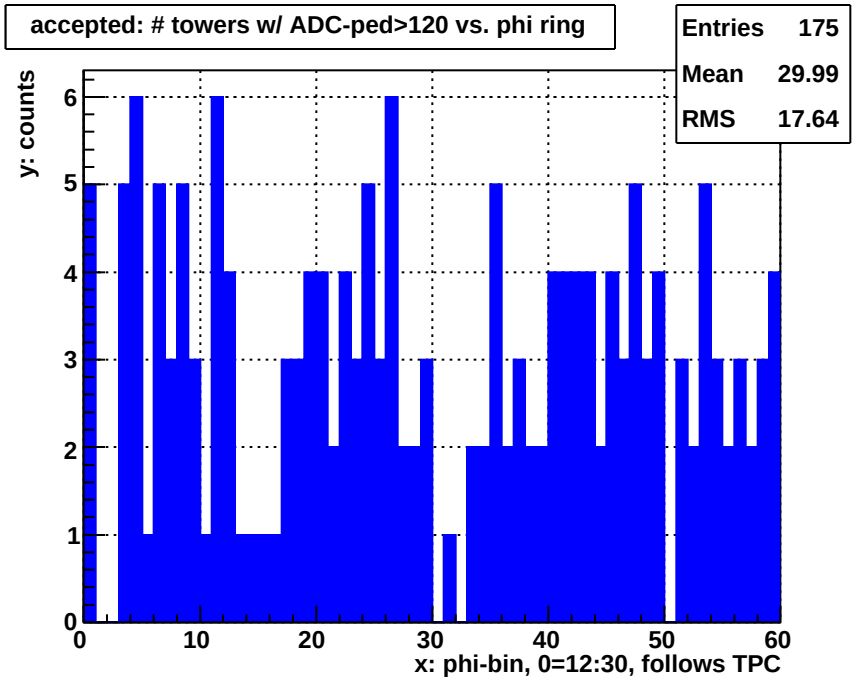

rate of ACCEPT
Entries 42136
Mean 528.7
RMS 302.8
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 42136
Mean 0.5496
RMS 0.2431
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
Entries 2089
Mean 5.056
RMS 2.163
χ² / ndf 9.31 / 5
Constant 10.16 ± 0.24
Slope -0.7699 ± 0.0380
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
Entries 1885
Mean 5.549
RMS 3.486
χ² / ndf 20.27 / 5
Constant 8.937 ± 0.240
Slope -0.5964 ± 0.0357
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 175
Mean 6.871
RMS 3.385
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 175
Mean 29.99
RMS 17.64
y: counts
x: phi-bin, 0=12:30, follows TPC