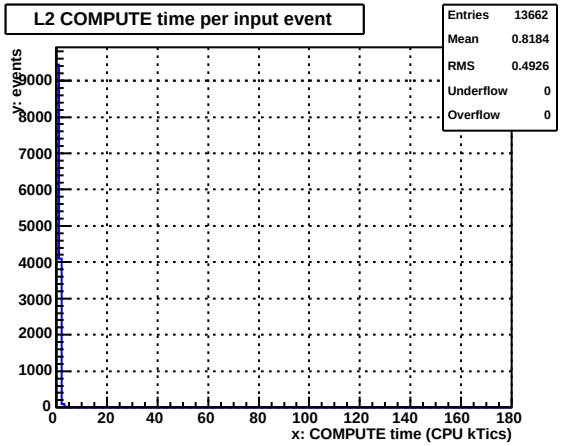
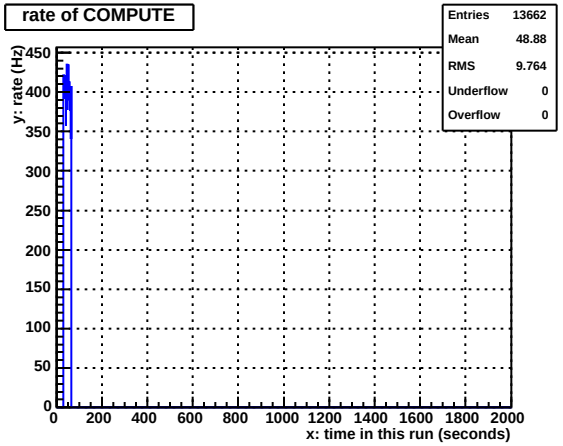
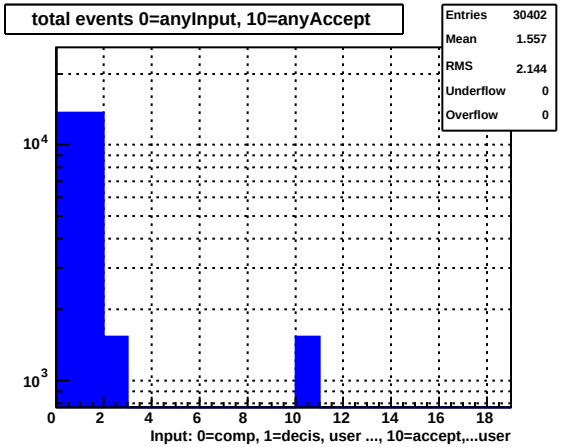
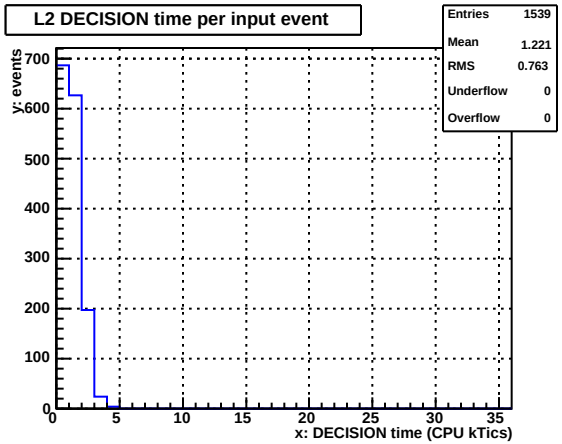
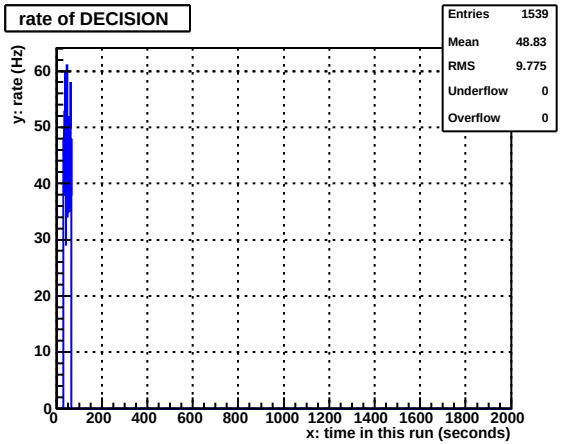
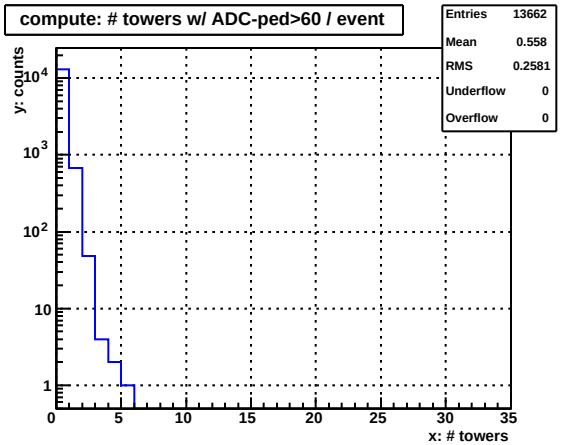

L2 COMPUTE time per input event
Entries 13662
Mean 0.8184
RMS 0.4926
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

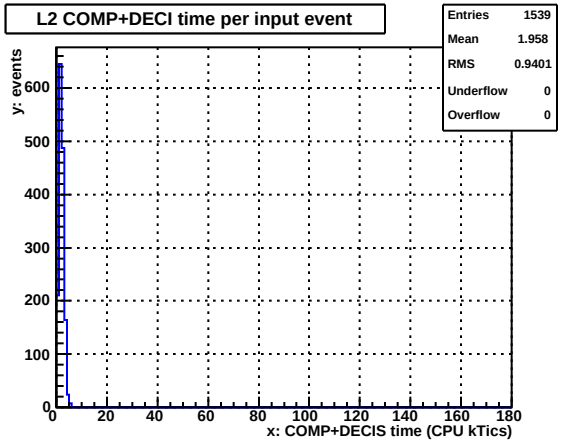

rate of COMPUTE
Entries 13662
Mean 48.88
RMS 9.764
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

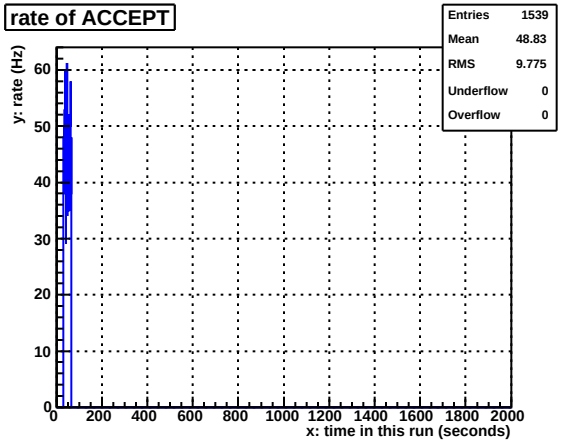

total events 0=anyInput, 10=anyAccept
Entries 30402
Mean 1.557
RMS 2.144
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

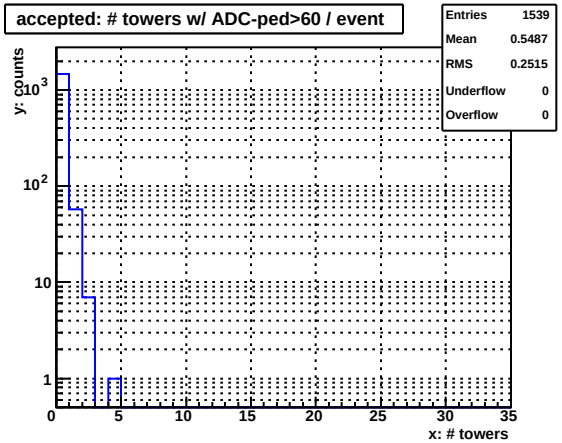

L2 DECISION time per input event
Entries 1539
Mean 1.221
RMS 0.763
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

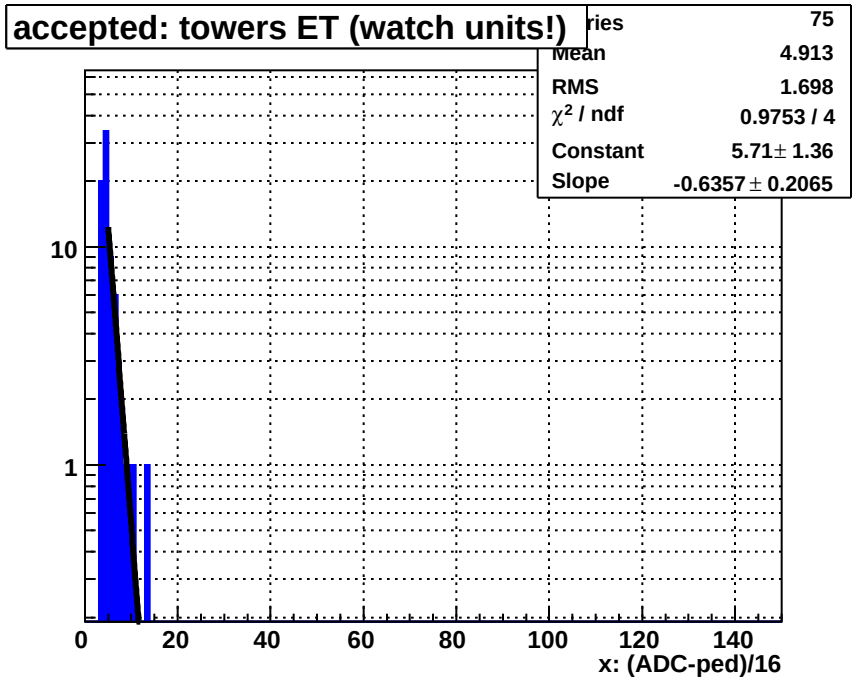

rate of DECISION
Entries 1539
Mean 48.83
RMS 9.775
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

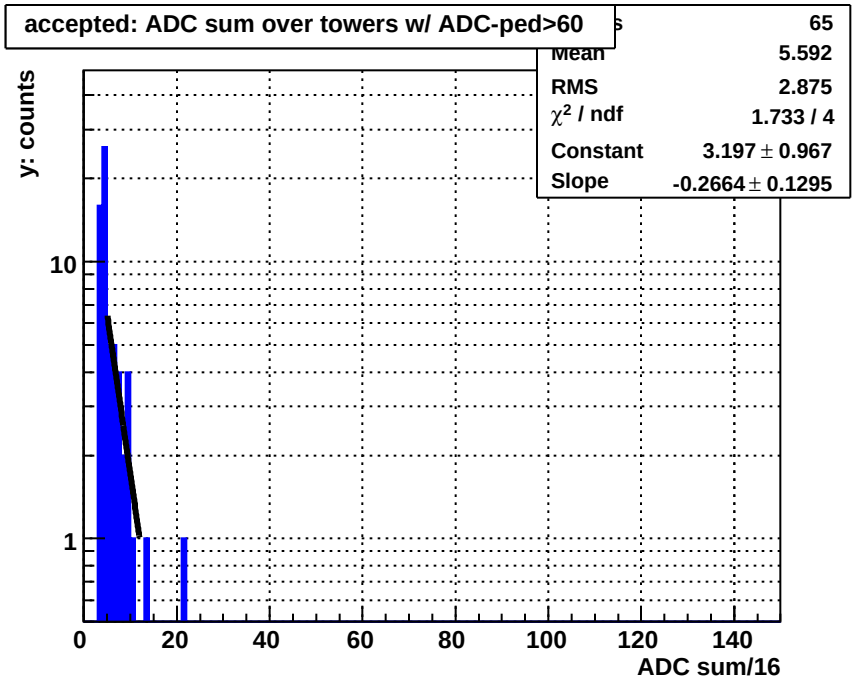

compute: # towers w/ ADC-ped>60 / event
Entries 13662
Mean 0.558
RMS 0.2581
Underflow 0
Overflow 0
y: counts
x: # towers

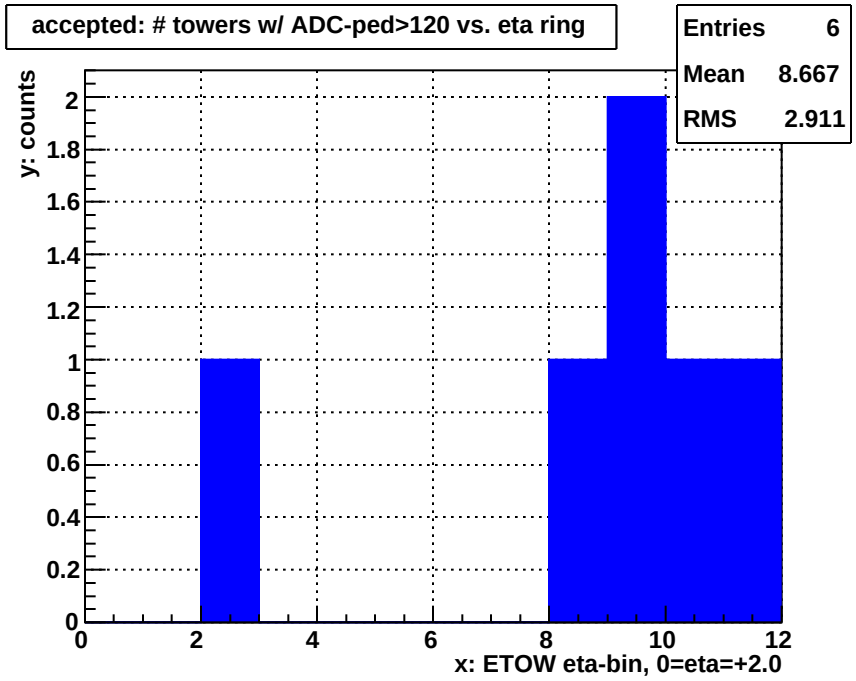

L2 COMP+DECI time per input event
Entries 1539
Mean 1.958
RMS 0.9401
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

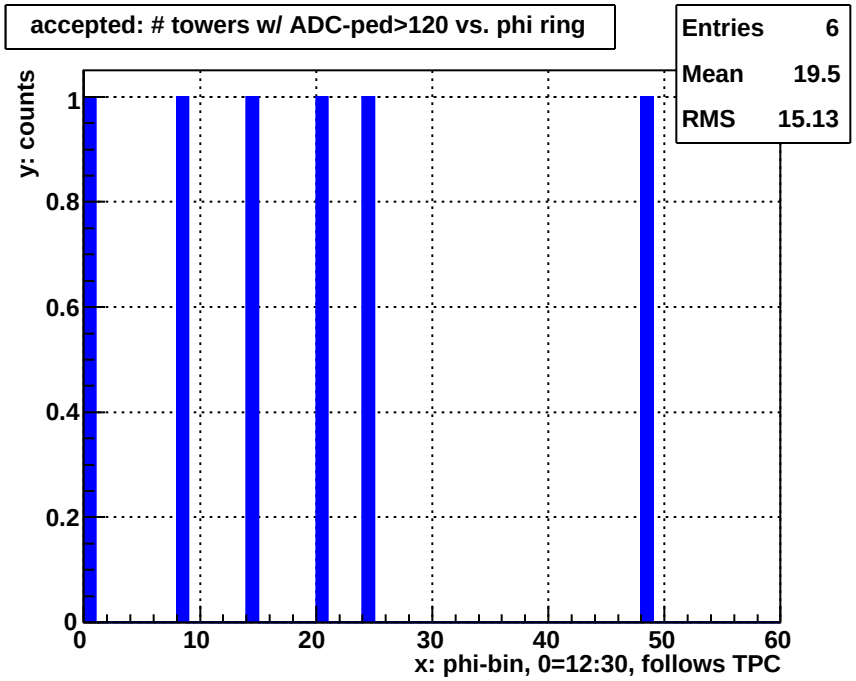

rate of ACCEPT
Entries 1539
Mean 48.83
RMS 9.775
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 1539
Mean 0.5487
RMS 0.2515
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 75
Mean 4.913
RMS 1.698
χ² / ndf 0.9753 / 4
Constant 5.71 ± 1.36
Slope -0.6357 ± 0.2065
10
1
0 20 40 60 80 100 120 140
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 65
Mean 5.592
RMS 2.875
χ² / ndf 1.733 / 4
Constant 3.197 ± 0.967
Slope -0.2664 ± 0.1295
y: counts
10
1
0 20 40 60 80 100 120 140
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 6
Mean 8.667
RMS 2.911
y: counts
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0 2 4 6 8 10 12
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 6
Mean 19.5
RMS 15.13
y: counts
1
0.8
0.6
0.4
0.2
0
0 10 20 30 40 50 60
x: phi-bin, 0=12:30, follows TPC