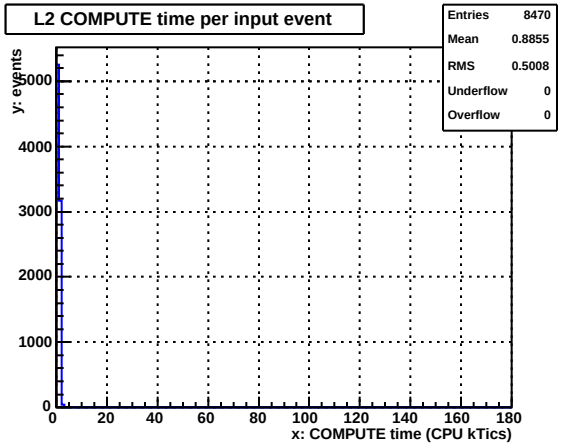
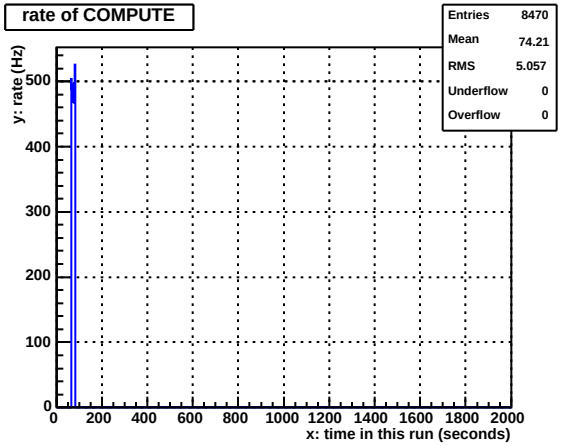
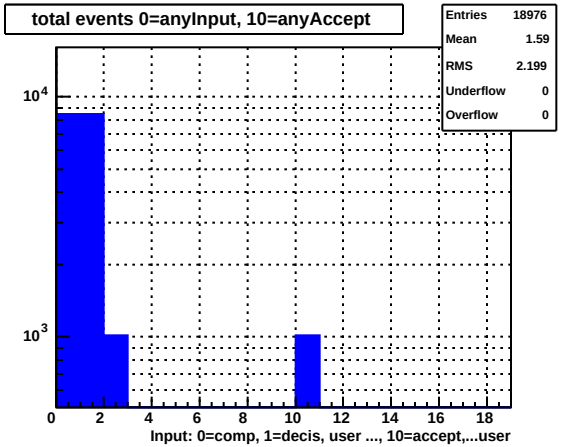
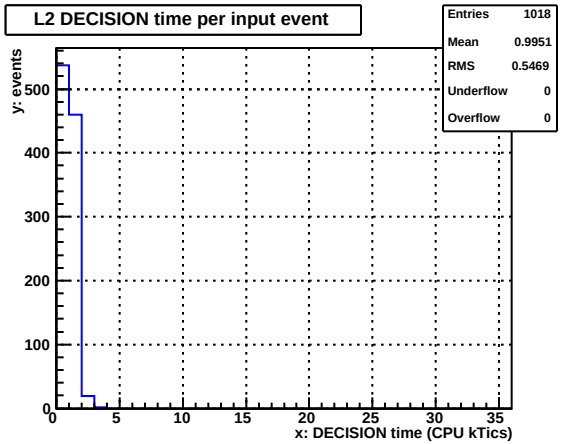
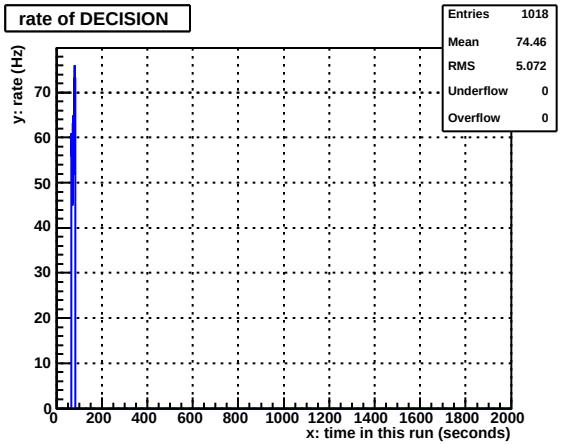
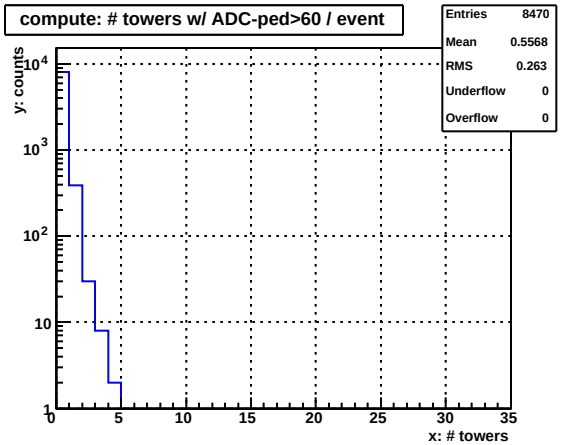

L2 COMPUTE time per input event
Entries 8470
Mean 0.8855
RMS 0.5008
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

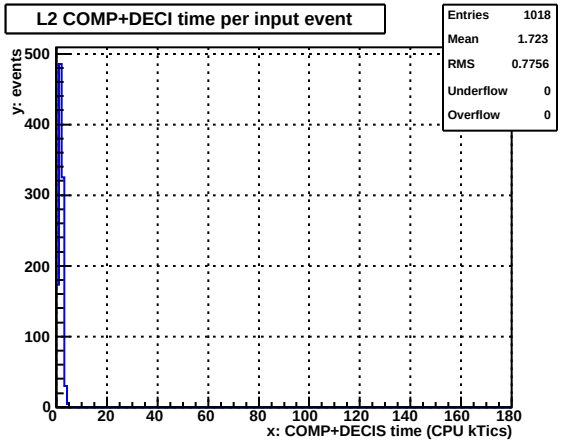

rate of COMPUTE
Entries 8470
Mean 74.21
RMS 5.057
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

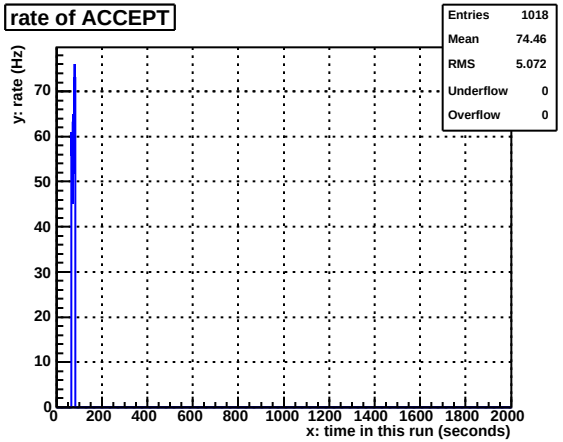

total events 0=anyInput, 10=anyAccept
Entries 18976
Mean 1.59
RMS 2.199
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

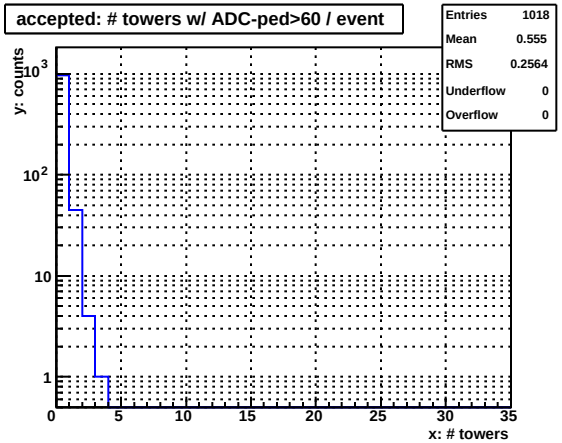

L2 DECISION time per input event
Entries 1018
Mean 0.9951
RMS 0.5469
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

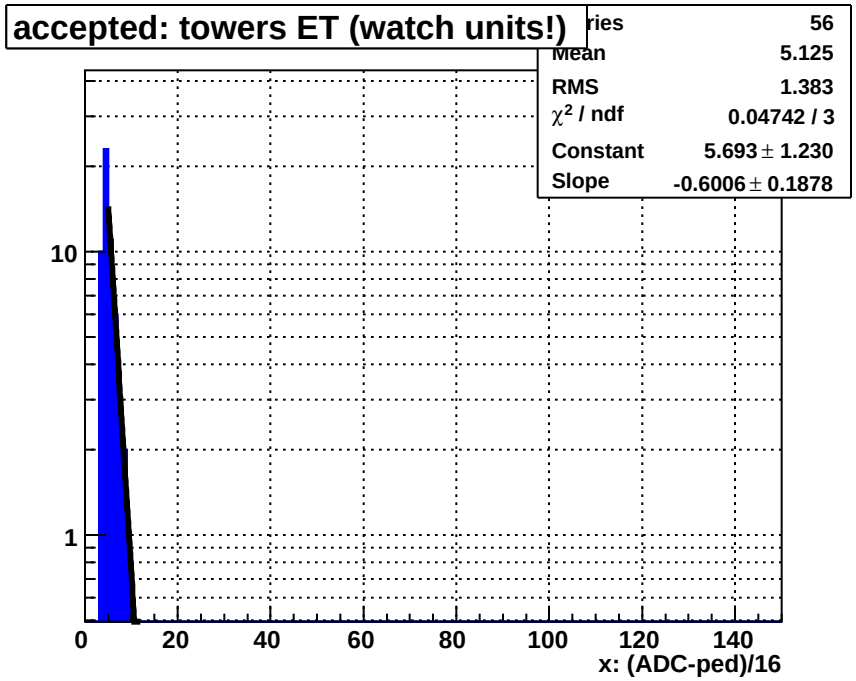

rate of DECISION
Entries 1018
Mean 74.46
RMS 5.072
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

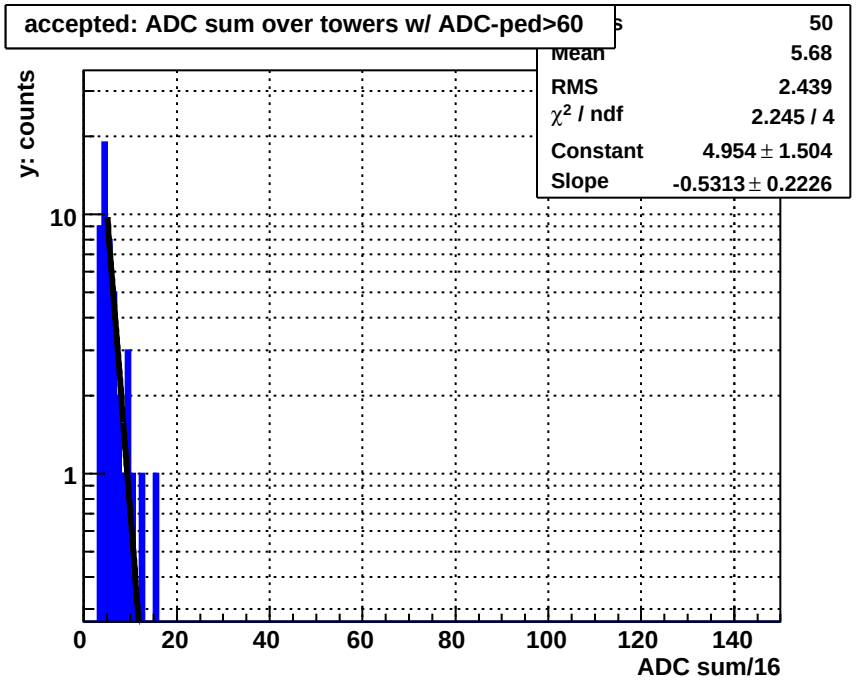

compute: # towers w/ ADC-ped>60 / event
Entries 8470
Mean 0.5568
RMS 0.263
Underflow 0
Overflow 0
y: counts
x: # towers

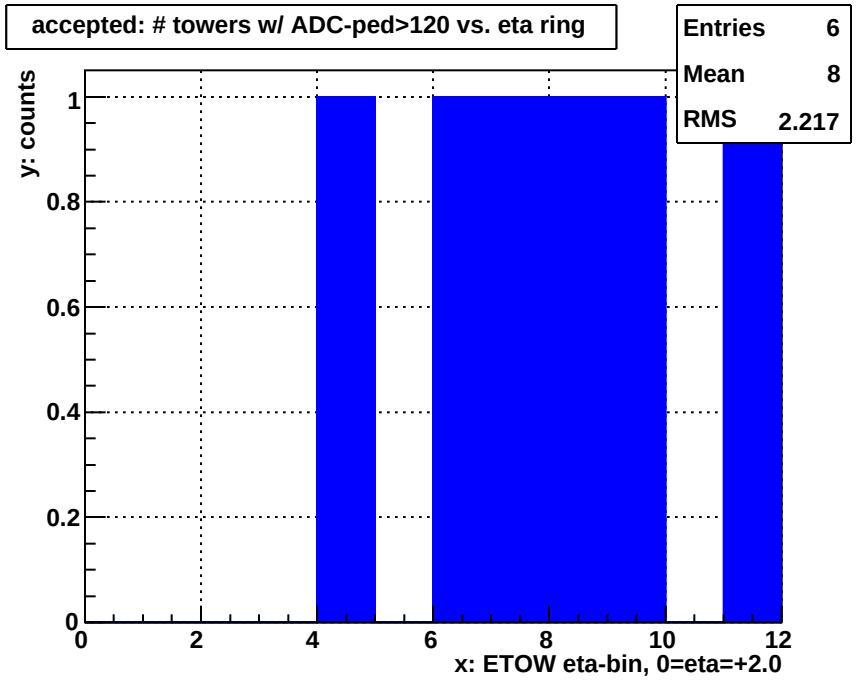

L2 COMP+DECI time per input event
Entries 1018
Mean 1.723
RMS 0.7756
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

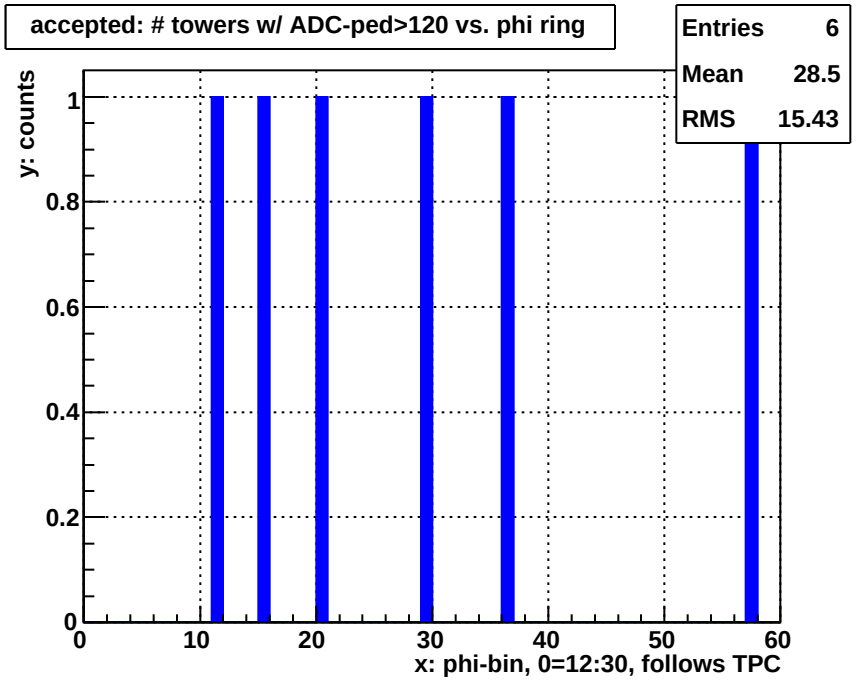

rate of ACCEPT
Entries 1018
Mean 74.46
RMS 5.072
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 1018
Mean 0.555
RMS 0.2564
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 56
Mean 5.125
RMS 1.383
χ² / ndf 0.04742 / 3
Constant 5.693 ± 1.230
Slope -0.6006 ± 0.1878
10
1
0 20 40 60 80 100 120 140
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 50
Mean 5.68
RMS 2.439
χ² / ndf 2.245 / 4
Constant 4.954 ± 1.504
Slope -0.5313 ± 0.2226
y: counts
10
1
0 20 40 60 80 100 120 140
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 6
Mean 8
RMS 2.217
y: counts
1
0.8
0.6
0.4
0.2
0
0 2 4 6 8 10 12
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 6
Mean 28.5
RMS 15.43
y: counts
1
0.8
0.6
0.4
0.2
0
0 10 20 30 40 50 60
x: phi-bin, 0=12:30, follows TPC