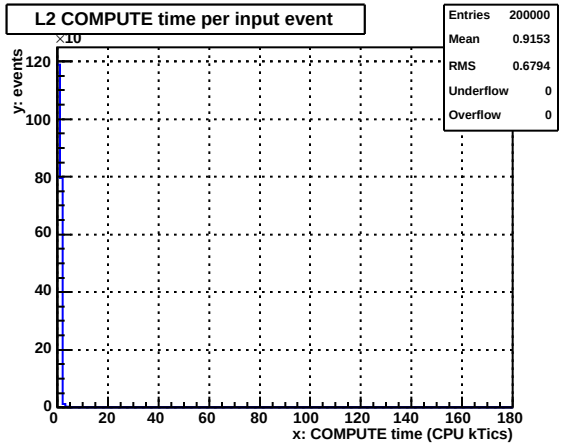
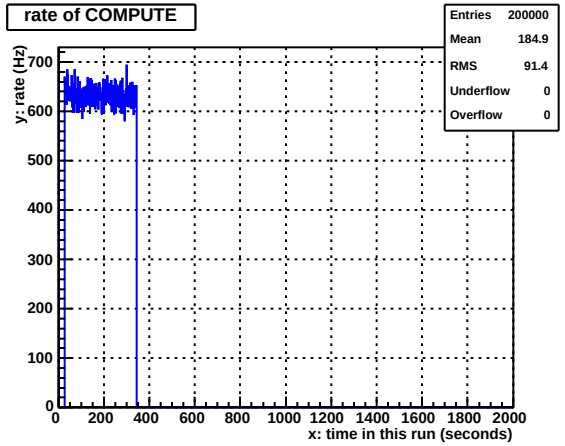
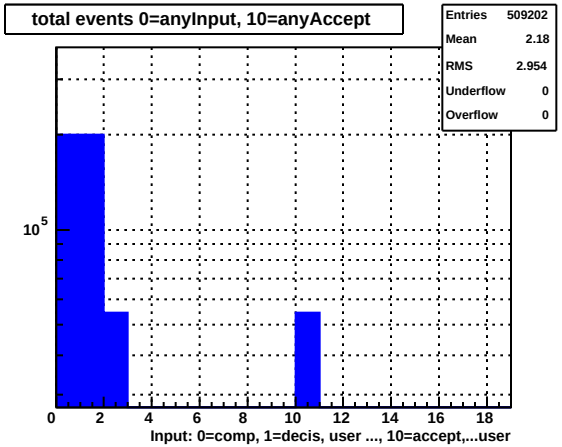
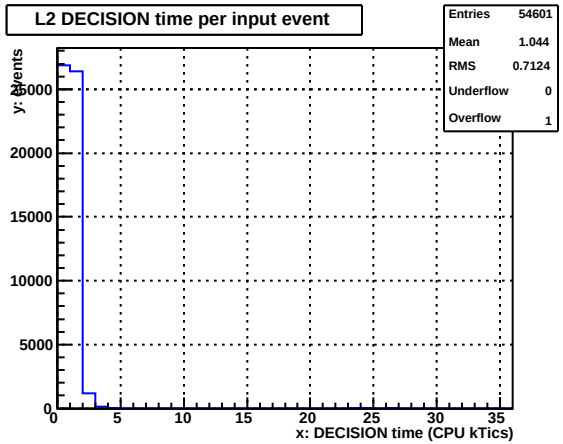
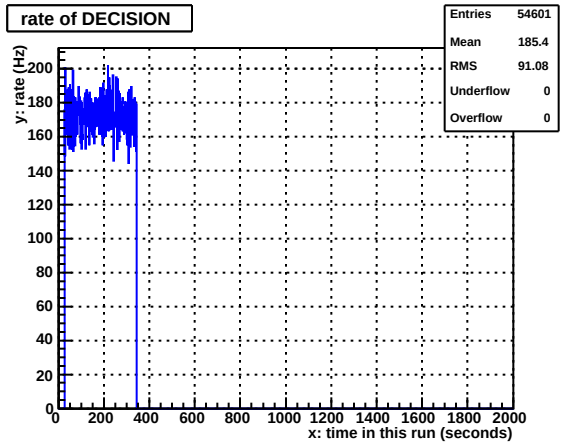
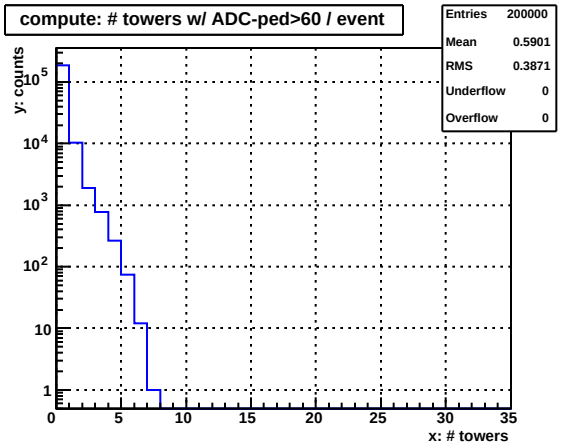

L2 COMPUTE time per input event
Entries 200000
Mean 0.9153
RMS 0.6794
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

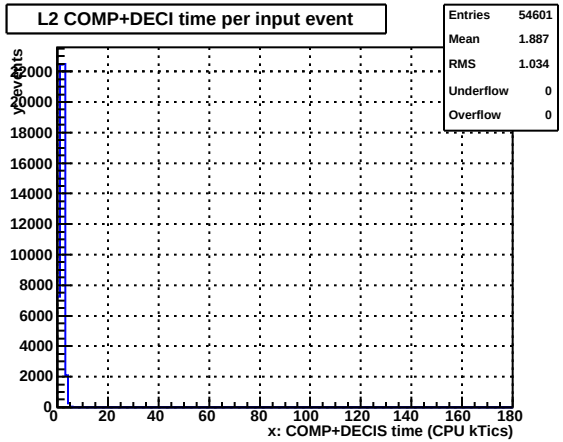

rate of COMPUTE
Entries 200000
Mean 184.9
RMS 91.4
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

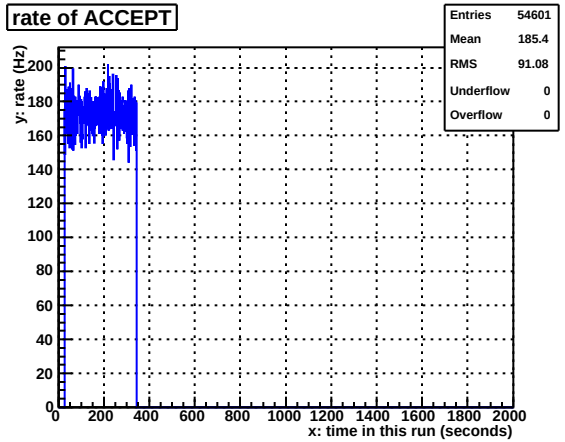

total events 0=anyInput, 10=anyAccept
Entries 509202
Mean 2.18
RMS 2.954
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

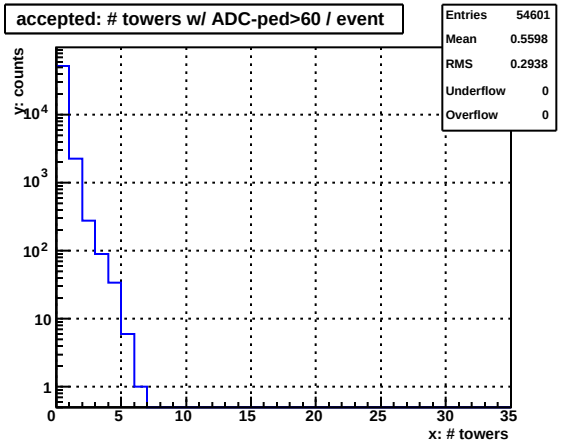

L2 DECISION time per input event
Entries 54601
Mean 1.044
RMS 0.7124
Underflow 0
Overflow 1
y: events
x: DECISION time (CPU kTics)

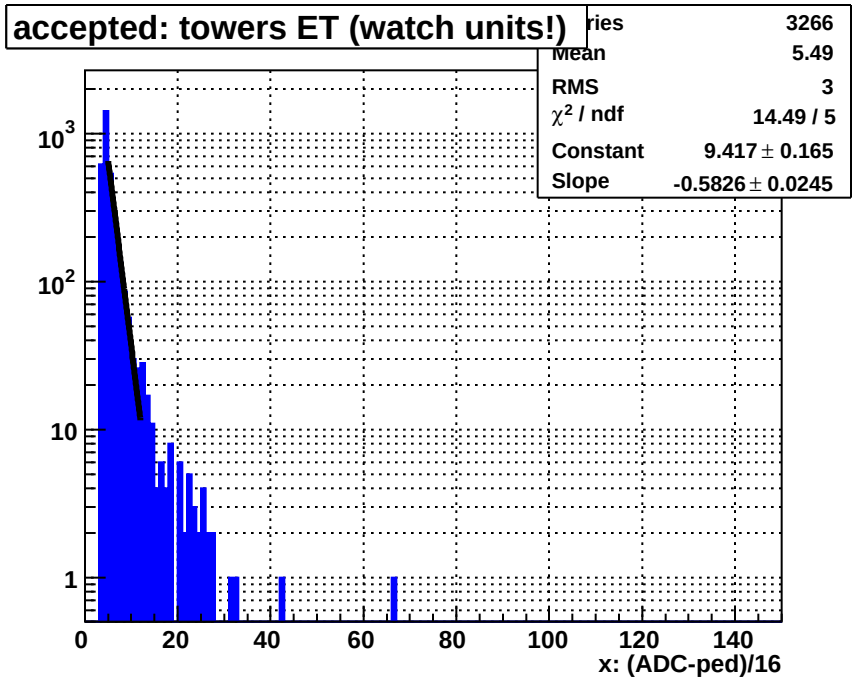

rate of DECISION
Entries 54601
Mean 185.4
RMS 91.08
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

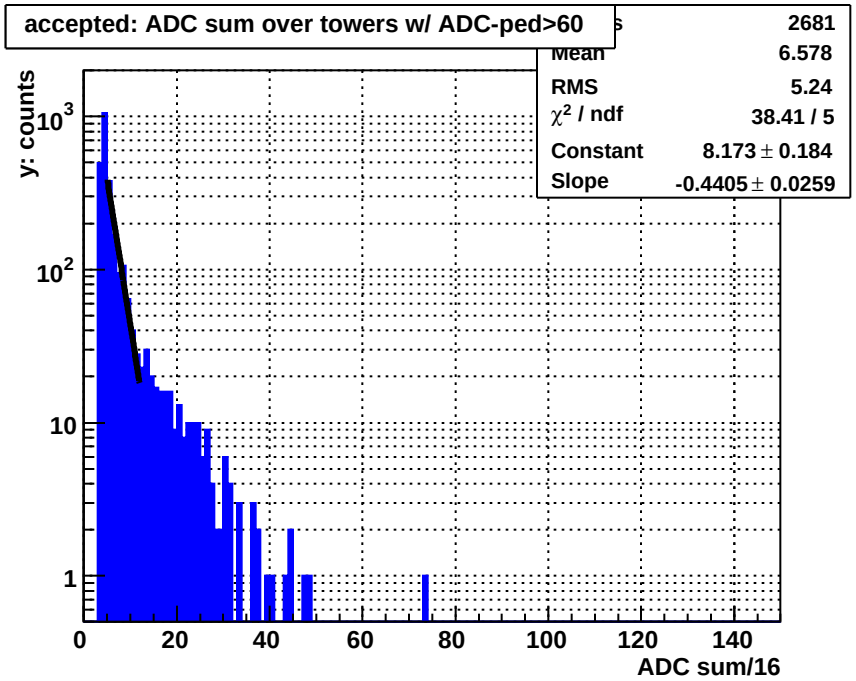

compute: # towers w/ ADC-ped>60 / event
Entries 200000
Mean 0.5901
RMS 0.3871
Underflow 0
Overflow 0
y: counts
x: # towers

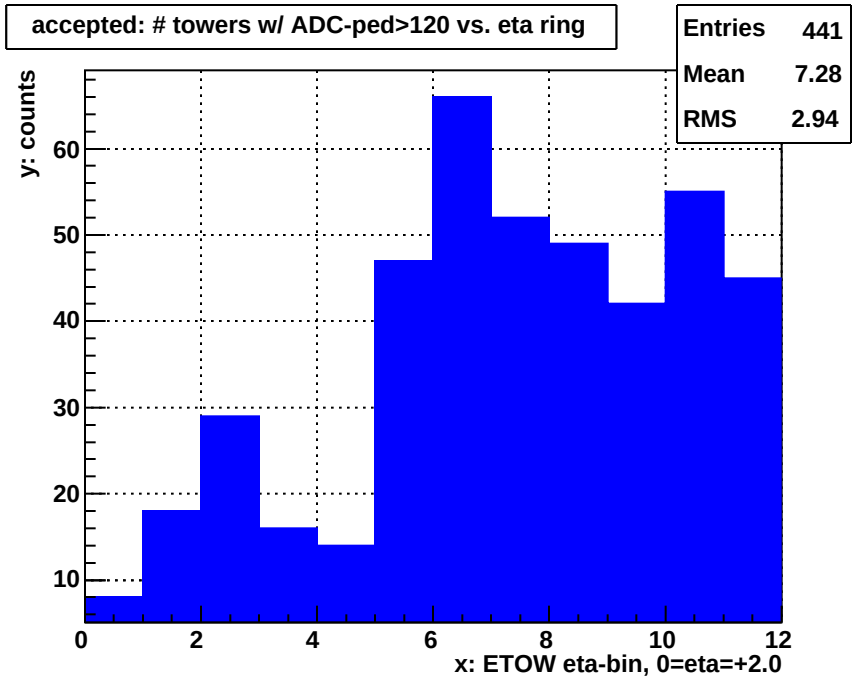

L2 COMP+DECI time per input event
Entries 54601
Mean 1.887
RMS 1.034
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

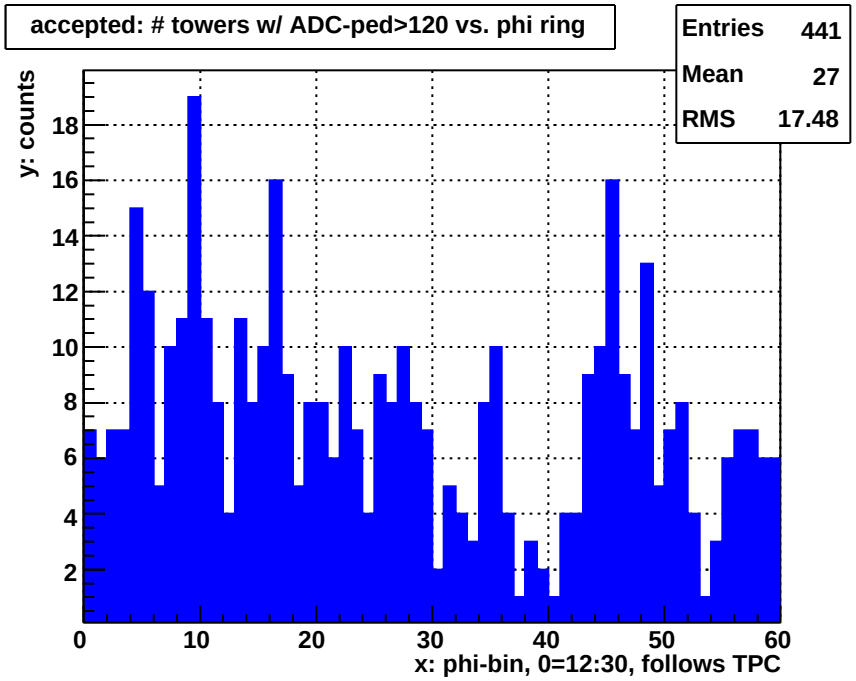

rate of ACCEPT
Entries 54601
Mean 185.4
RMS 91.08
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 54601
Mean 0.5598
RMS 0.2938
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 3266
Mean 5.49
RMS 3
χ² / ndf 14.49 / 5
Constant 9.417 ± 0.165
Slope -0.5826 ± 0.0245
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 2681
Mean 6.578
RMS 5.24
χ² / ndf 38.41 / 5
Constant 8.173 ± 0.184
Slope -0.4405 ± 0.0259
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 441
Mean 7.28
RMS 2.94
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 441
Mean 27
RMS 17.48
y: counts
x: phi-bin, 0=12:30, follows TPC