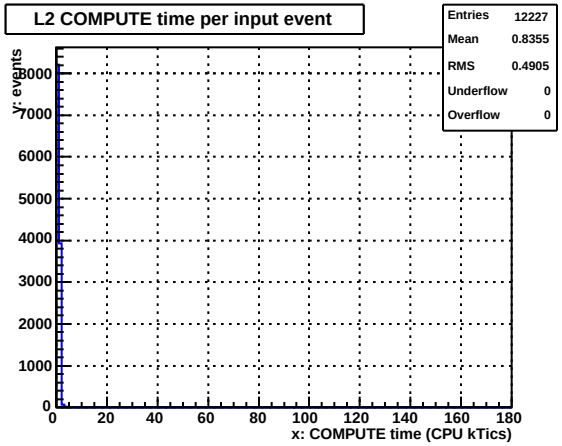
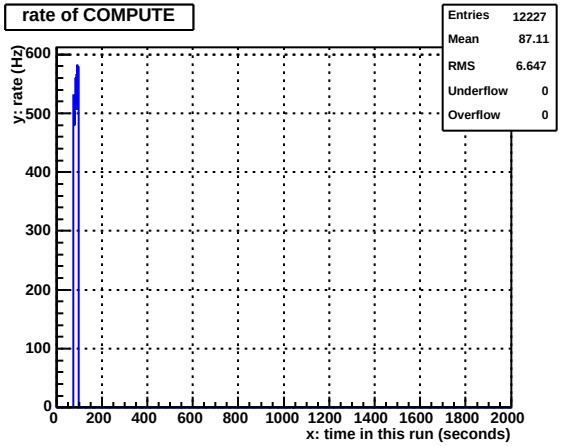
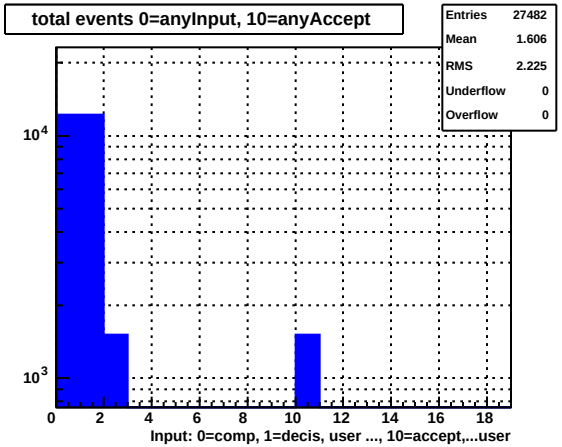
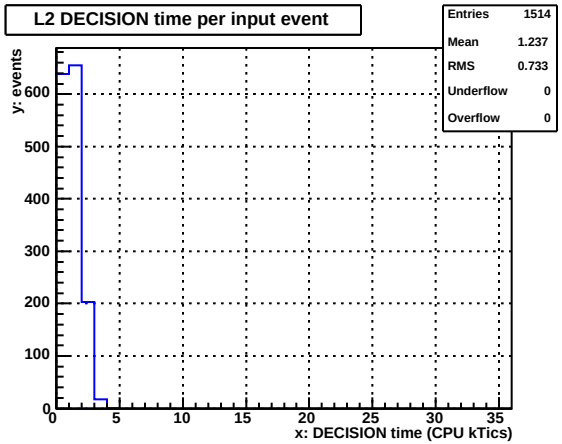
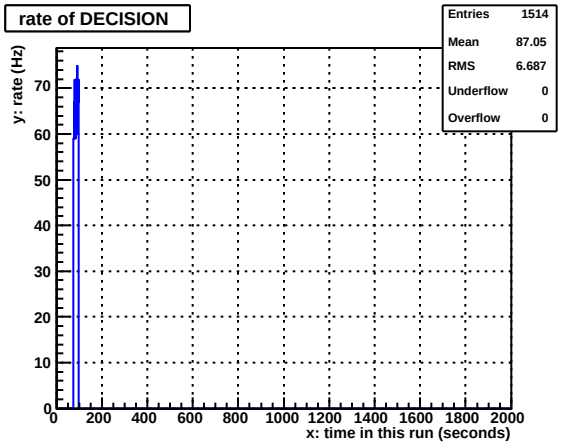
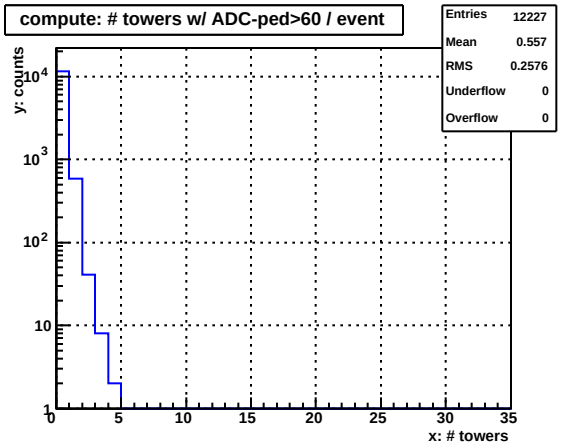

L2 COMPUTE time per input event
Entries 12227
Mean 0.8355
RMS 0.4905
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

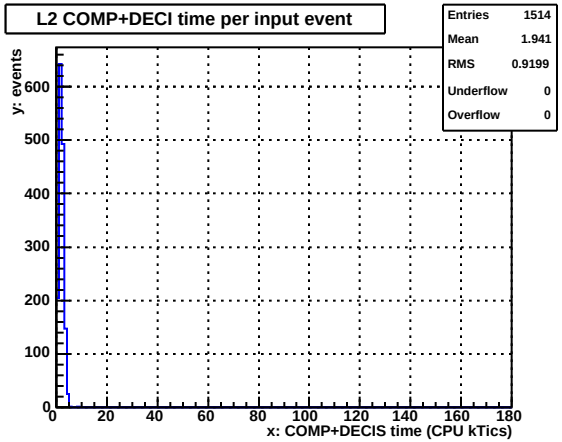

rate of COMPUTE
Entries 12227
Mean 87.11
RMS 6.647
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

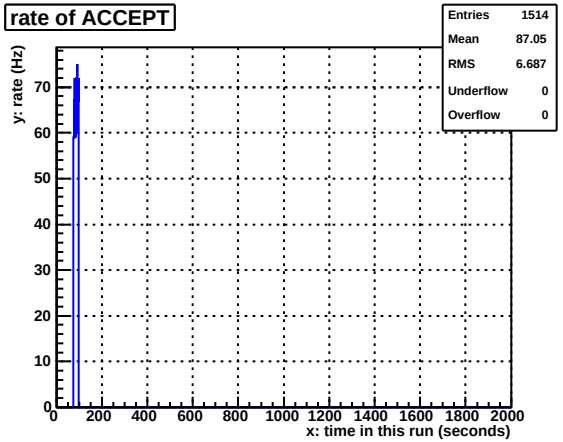

total events 0=anyInput, 10=anyAccept
Entries 27482
Mean 1.606
RMS 2.225
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

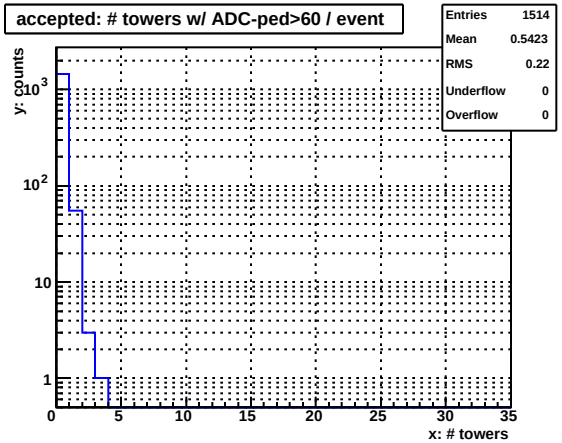

L2 DECISION time per input event
Entries 1514
Mean 1.237
RMS 0.733
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

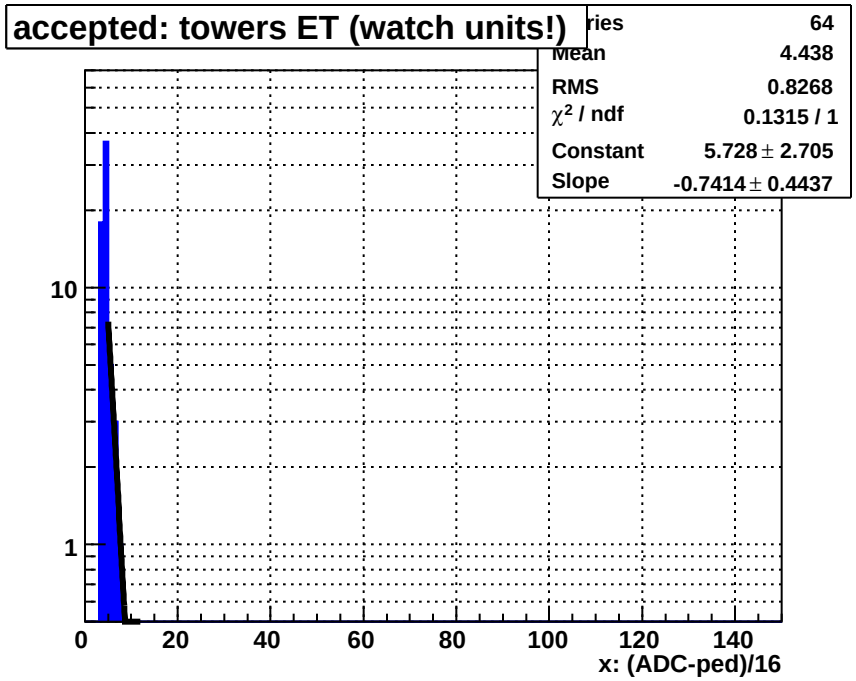

rate of DECISION
Entries 1514
Mean 87.05
RMS 6.687
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

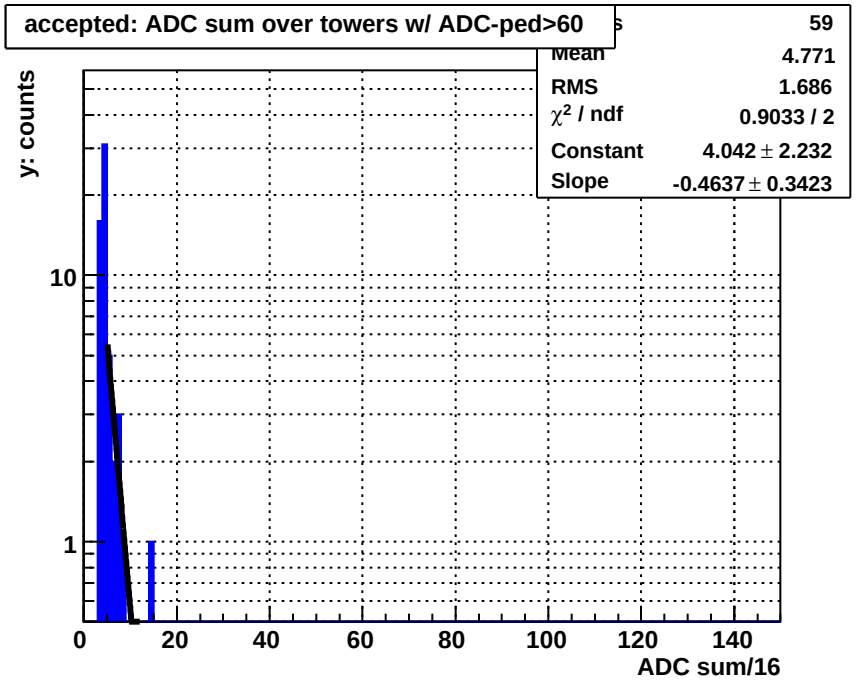

compute: # towers w/ ADC-ped>60 / event
Entries 12227
Mean 0.557
RMS 0.2576
Underflow 0
Overflow 0
y: counts
x: # towers

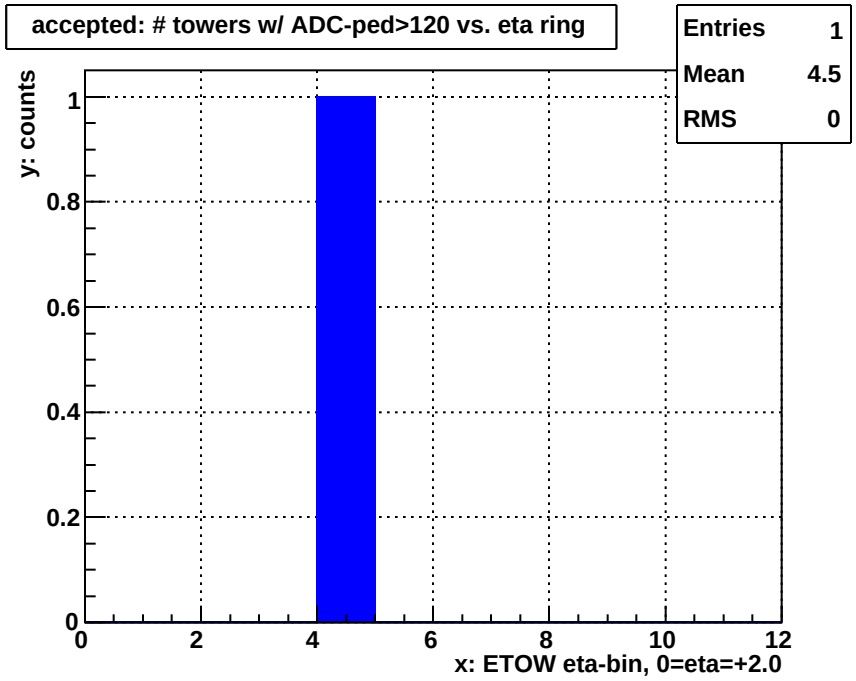

L2 COMP+DECI time per input event
Entries 1514
Mean 1.941
RMS 0.9199
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

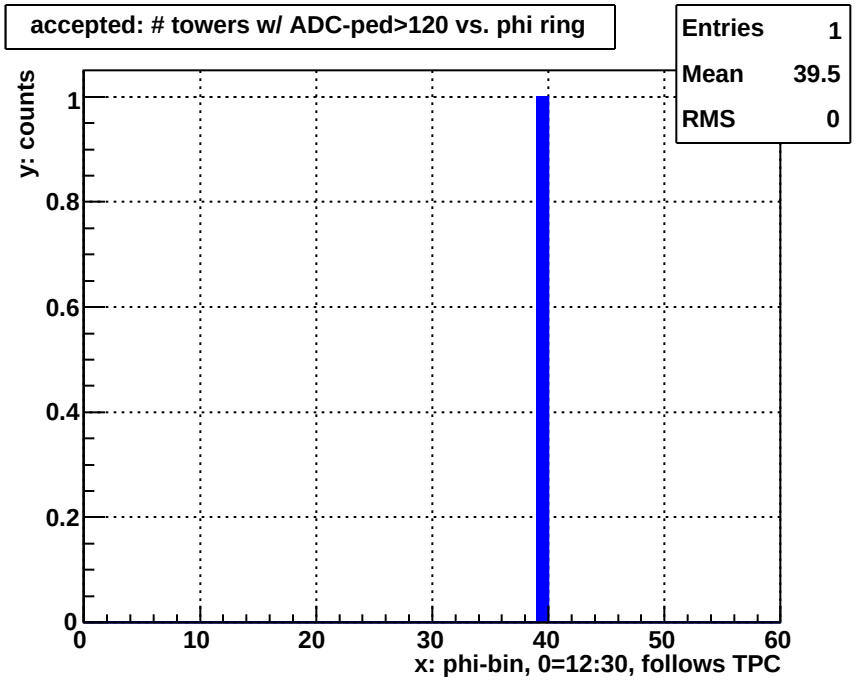

rate of ACCEPT
Entries 1514
Mean 87.05
RMS 6.687
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 1514
Mean 0.5423
RMS 0.22
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 64
Mean 4.438
RMS 0.8268
χ² / ndf 0.1315 / 1
Constant 5.728 ± 2.705
Slope -0.7414 ± 0.4437
10
1
0 20 40 60 80 100 120 140
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 59
Mean 4.771
RMS 1.686
χ² / ndf 0.9033 / 2
Constant 4.042 ± 2.232
Slope -0.4637 ± 0.3423
y: counts
10
1
0 20 40 60 80 100 120 140
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 1
Mean 4.5
RMS 0
y: counts
1
0.8
0.6
0.4
0.2
0
0 2 4 6 8 10 12
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 1
Mean 39.5
RMS 0
y: counts
1
0.8
0.6
0.4
0.2
0
0 10 20 30 40 50 60
x: phi-bin, 0=12:30, follows TPC