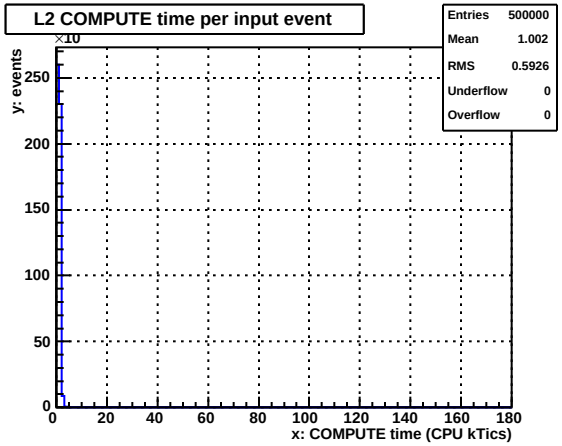
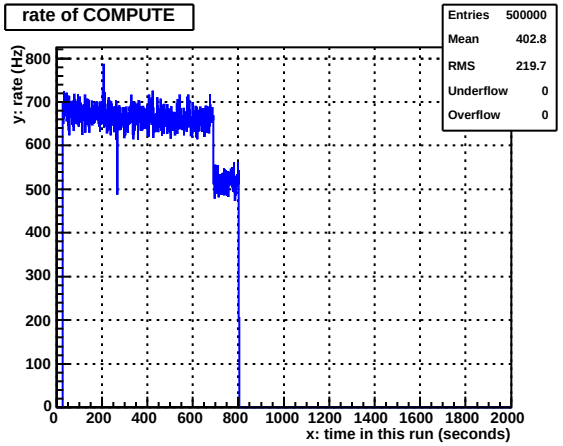
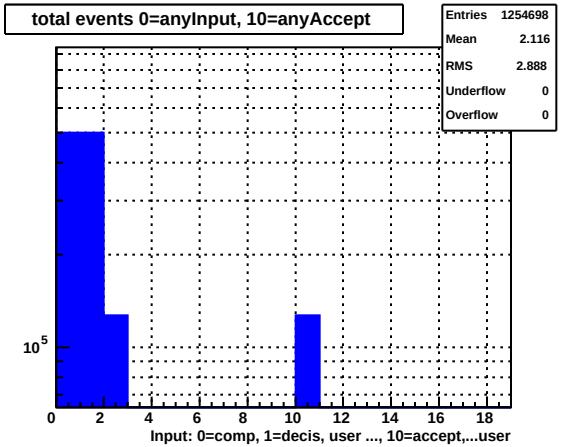
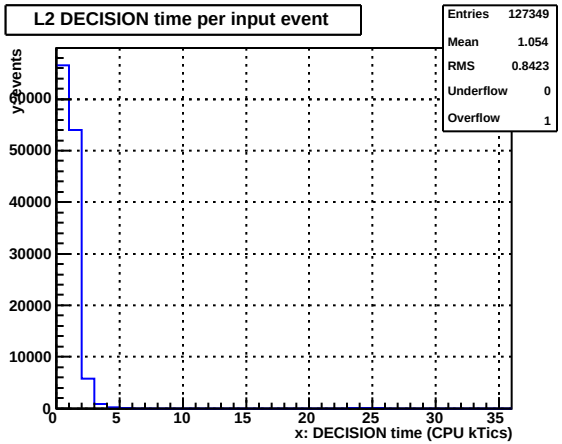
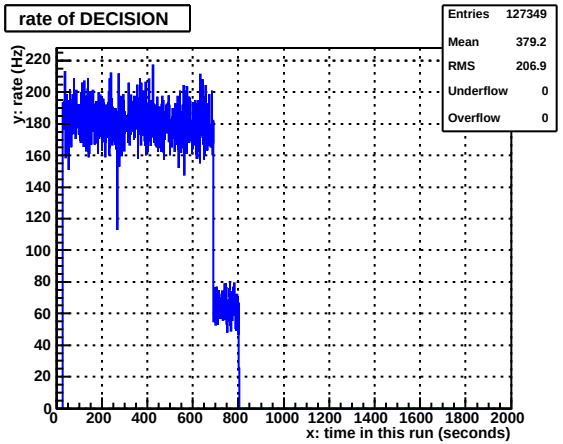
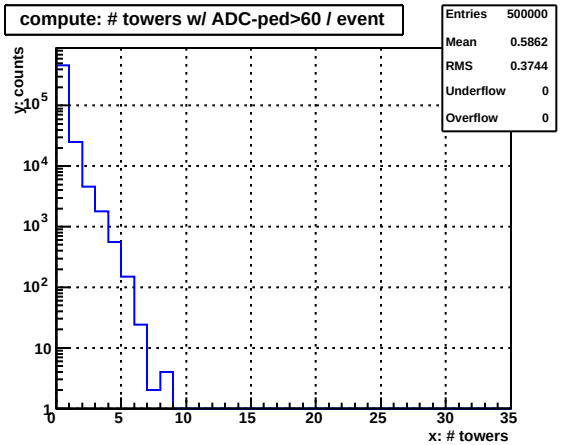

L2 COMPUTE time per input event
Entries 500000
Mean 1.002
RMS 0.5926
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

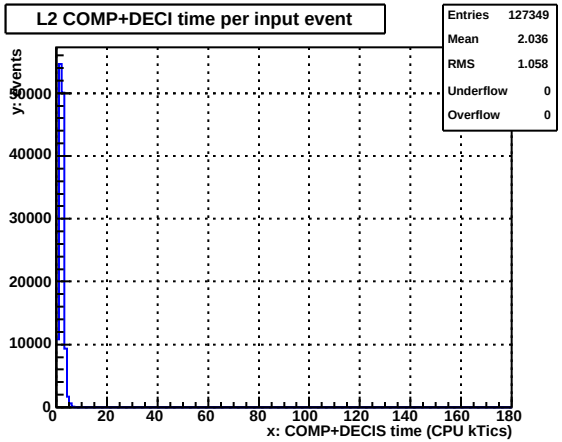

rate of COMPUTE
Entries 500000
Mean 402.8
RMS 219.7
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

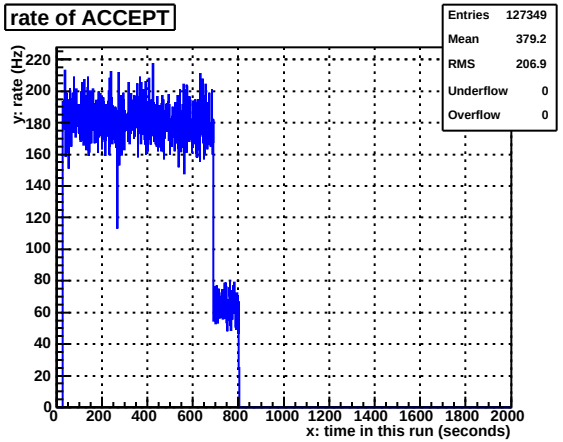

total events 0=anyInput, 10=anyAccept
Entries 1254698
Mean 2.116
RMS 2.888
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

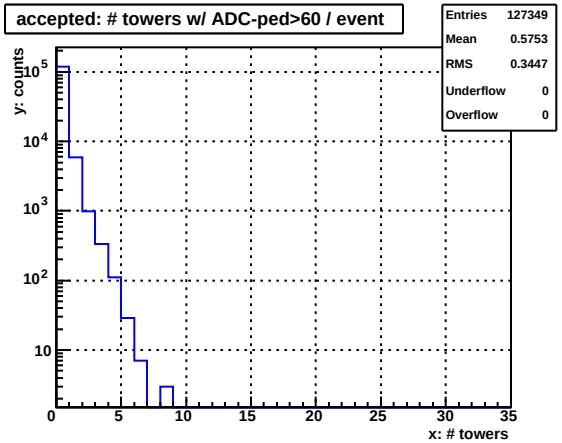

L2 DECISION time per input event
Entries 127349
Mean 1.054
RMS 0.8423
Underflow 0
Overflow 1
y: events
x: DECISION time (CPU kTics)

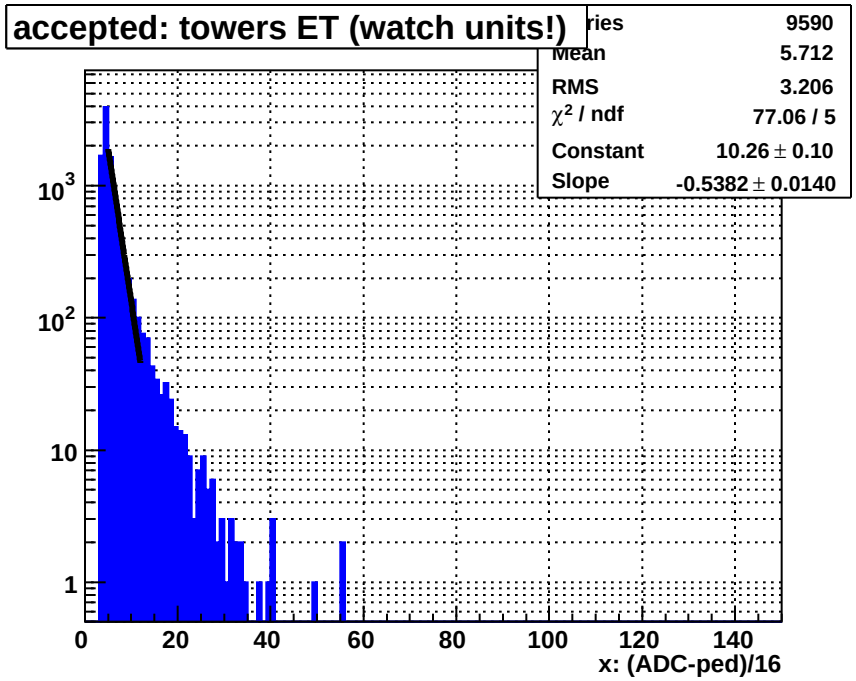

rate of DECISION
Entries 127349
Mean 379.2
RMS 206.9
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

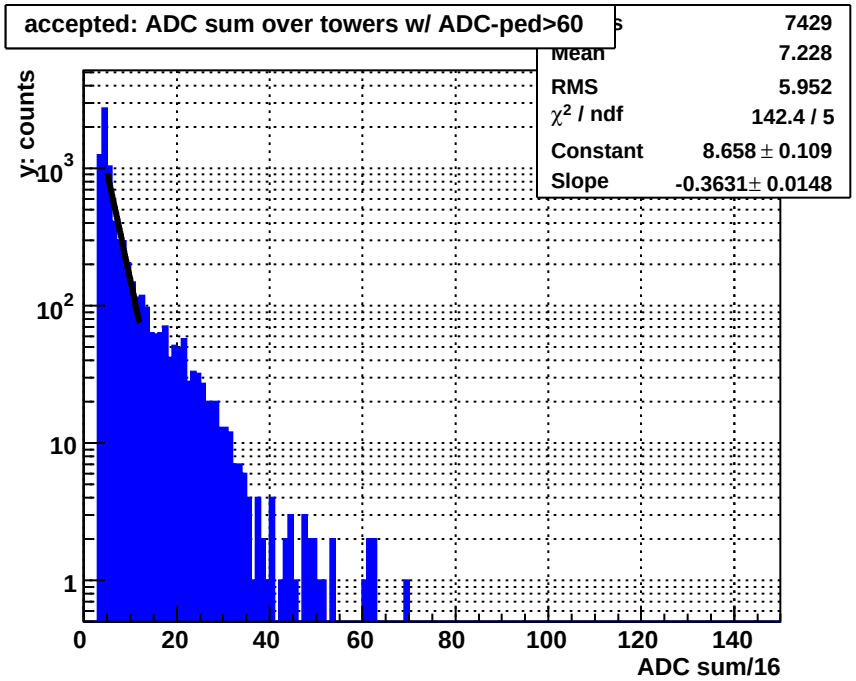

compute: # towers w/ ADC-ped>60 / event
Entries 500000
Mean 0.5862
RMS 0.3744
Underflow 0
Overflow 0
y: counts
x: # towers

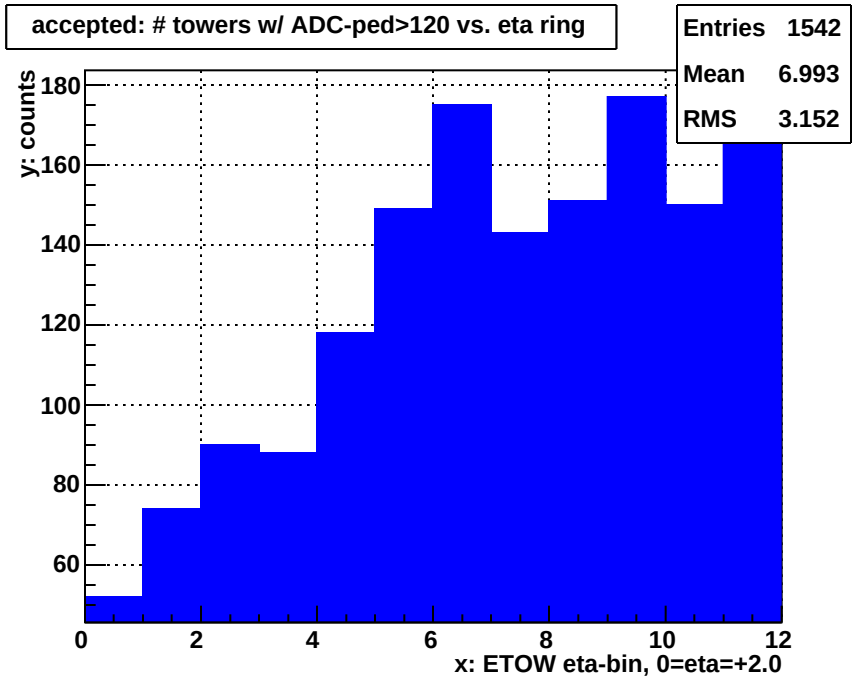

L2 COMP+DECI time per input event
Entries 127349
Mean 2.036
RMS 1.058
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

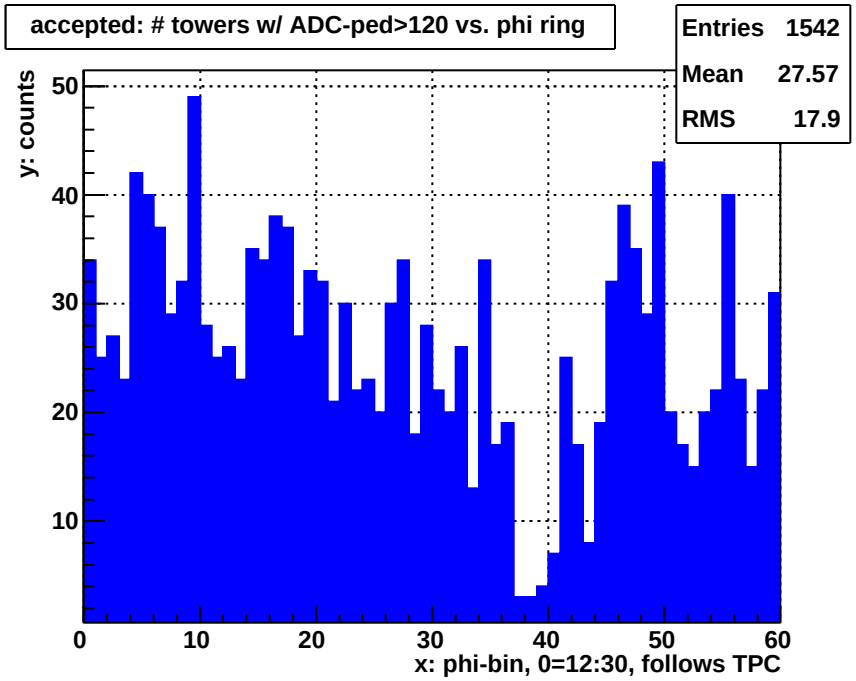

rate of ACCEPT
Entries 127349
Mean 379.2
RMS 206.9
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 127349
Mean 0.5753
RMS 0.3447
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 9590
Mean 5.712
RMS 3.206
χ² / ndf 77.06 / 5
Constant 10.26 ± 0.10
Slope -0.5382 ± 0.0140
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 7429
Mean 7.228
RMS 5.952
χ² / ndf 142.4 / 5
Constant 8.658 ± 0.109
Slope -0.3631 ± 0.0148
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 1542
Mean 6.993
RMS 3.152
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 1542
Mean 27.57
RMS 17.9
y: counts
x: phi-bin, 0=12:30, follows TPC