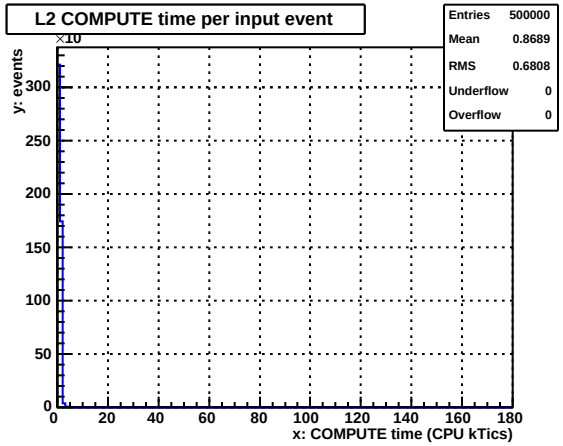
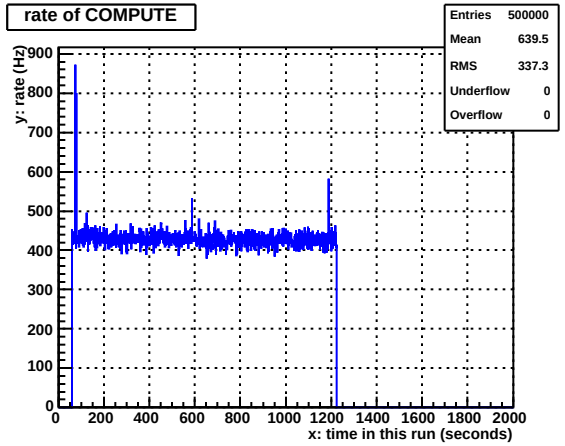
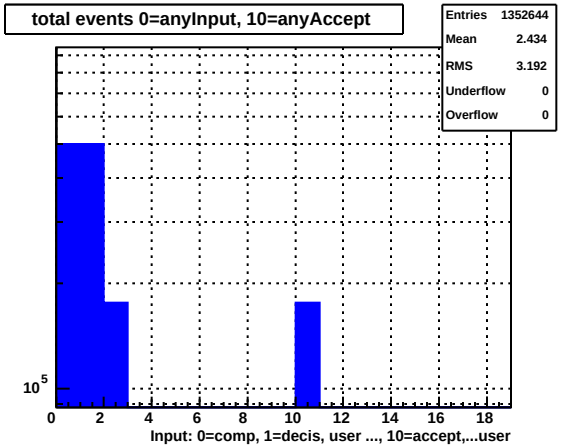
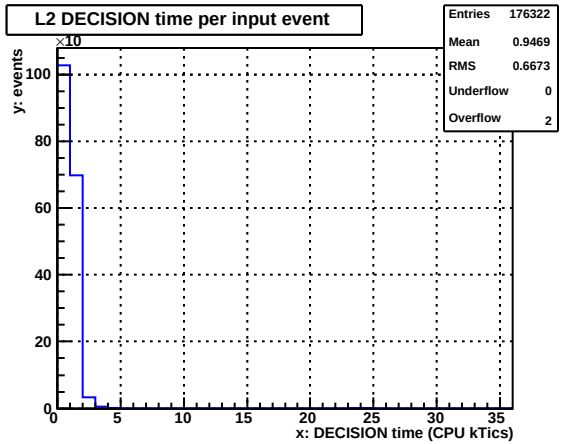
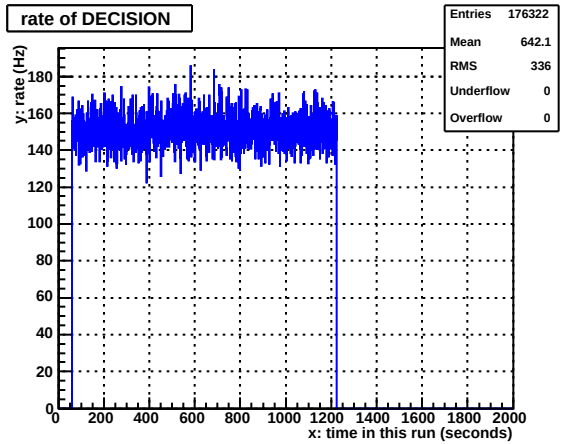
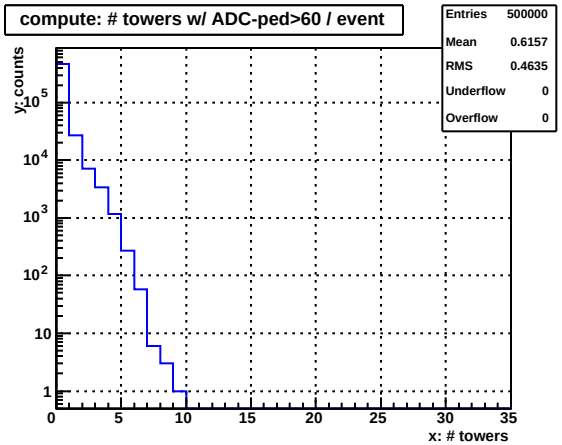

L2 COMPUTE time per input event
Entries 500000
Mean 0.8689
RMS 0.6808
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

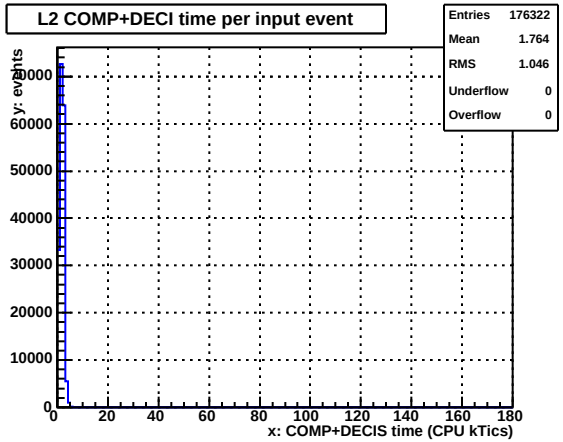

rate of COMPUTE
Entries 500000
Mean 639.5
RMS 337.3
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

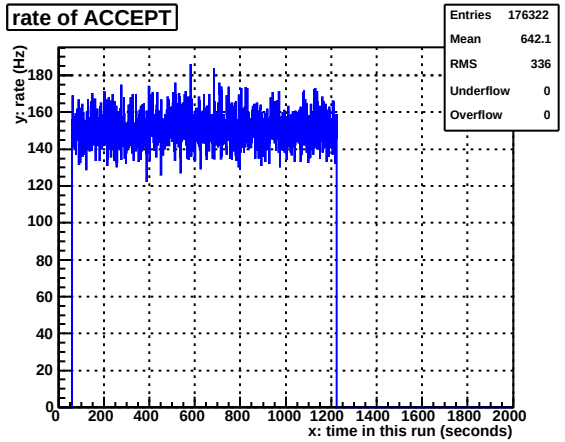

total events 0=anyInput, 10=anyAccept
Entries 1352644
Mean 2.434
RMS 3.192
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

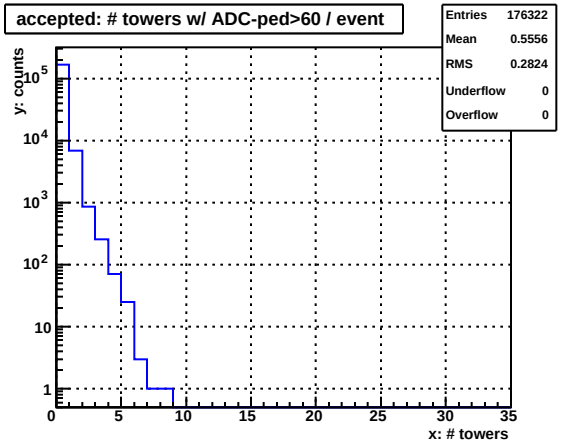

L2 DECISION time per input event
Entries 176322
Mean 0.9469
RMS 0.6673
Underflow 0
Overflow 2
y: events
x: DECISION time (CPU kTics)

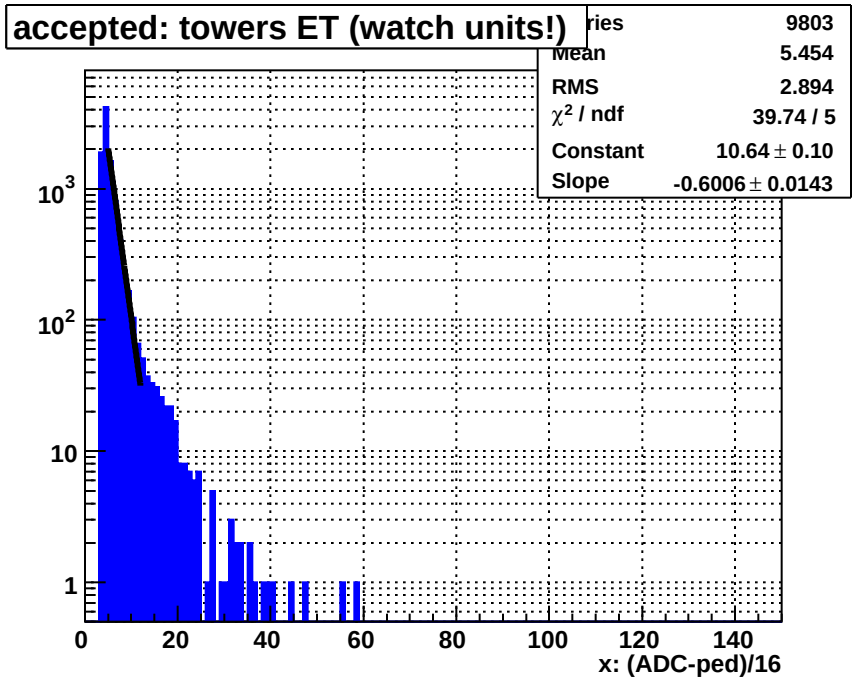

rate of DECISION
Entries 176322
Mean 642.1
RMS 336
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

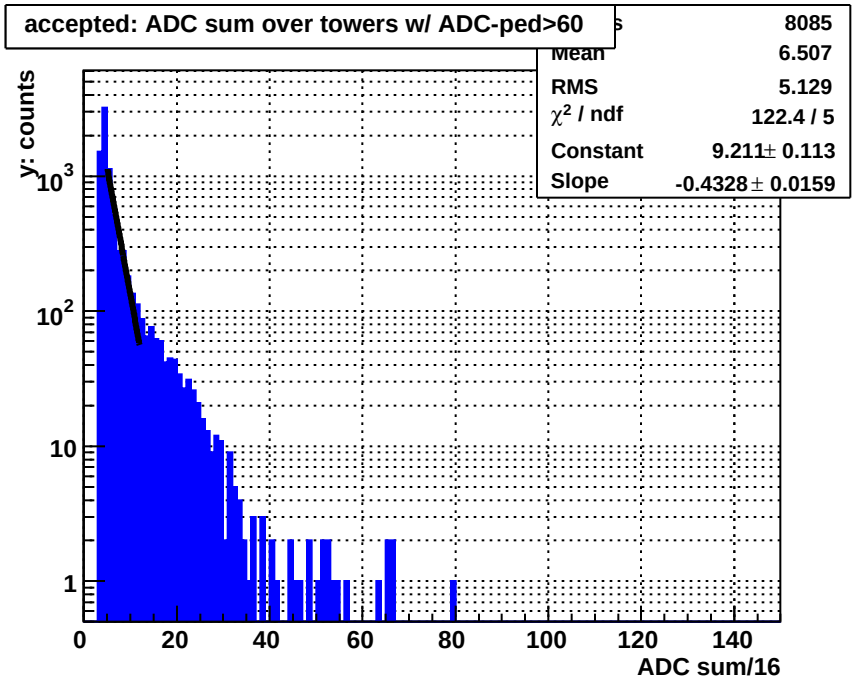

compute: # towers w/ ADC-ped>60 / event
Entries 500000
Mean 0.6157
RMS 0.4635
Underflow 0
Overflow 0
y: counts
x: # towers

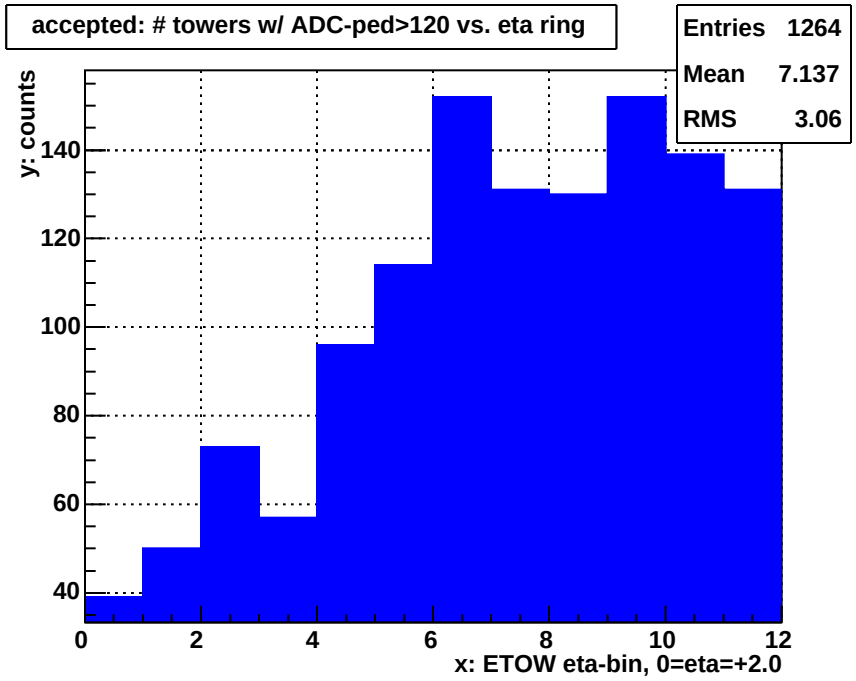

L2 COMP+DECI time per input event
Entries 176322
Mean 1.764
RMS 1.046
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

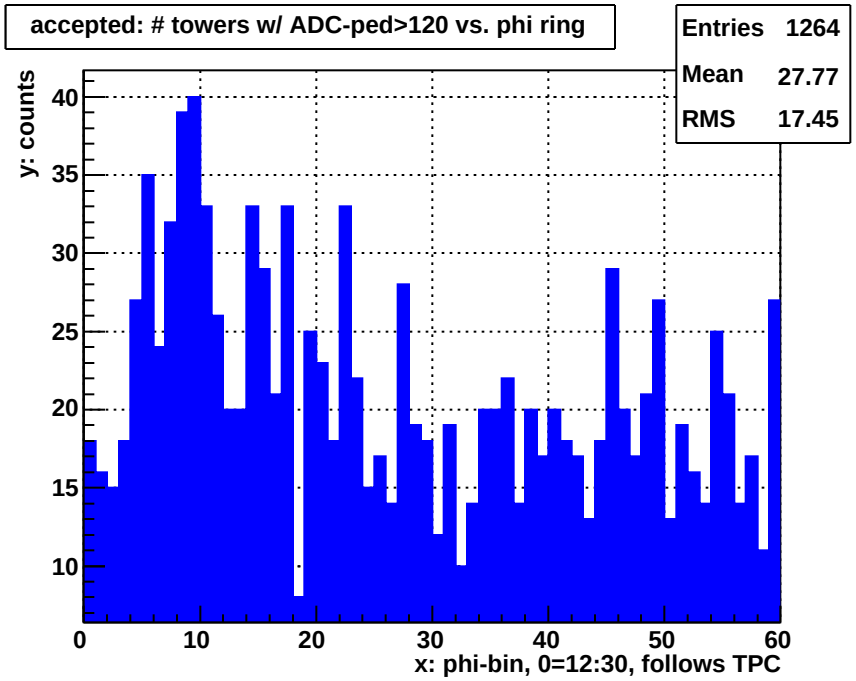

rate of ACCEPT
Entries 176322
Mean 642.1
RMS 336
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 176322
Mean 0.5556
RMS 0.2824
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 9803
Mean 5.454
RMS 2.894
χ² / ndf 39.74 / 5
Constant 10.64 ± 0.10
Slope -0.6006 ± 0.0143
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 8085
Mean 6.507
RMS 5.129
χ² / ndf 122.4 / 5
Constant 9.211± 0.113
Slope -0.4328 ± 0.0159
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 1264
Mean 7.137
RMS 3.06
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 1264
Mean 27.77
RMS 17.45
y: counts
x: phi-bin, 0=12:30, follows TPC