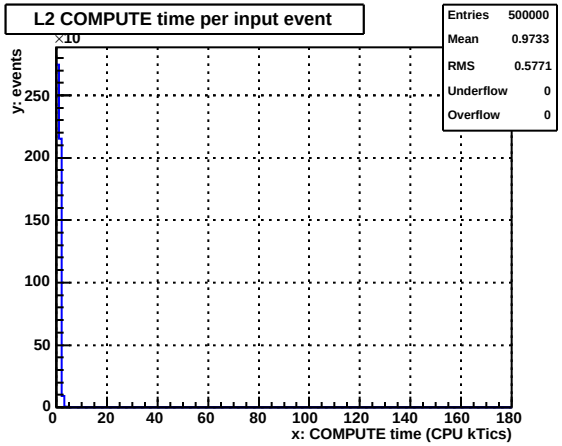
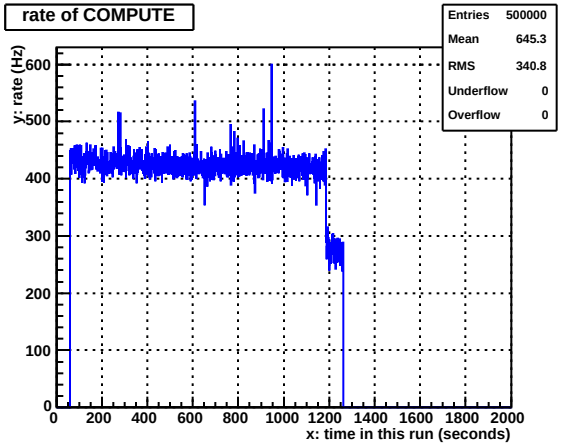
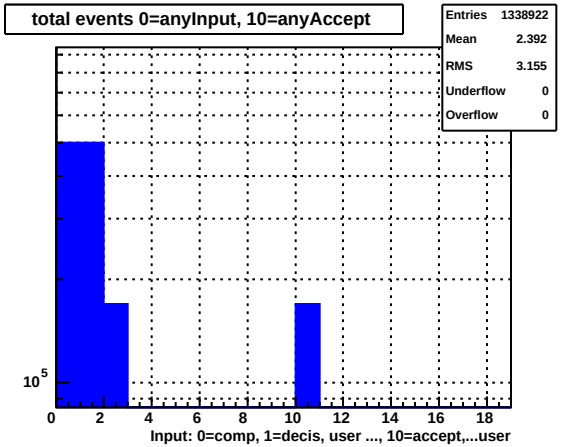
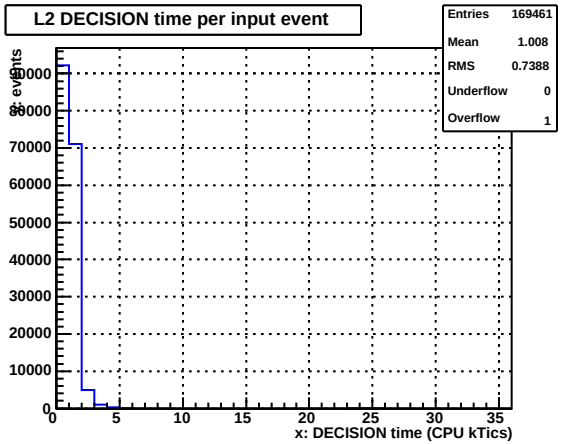
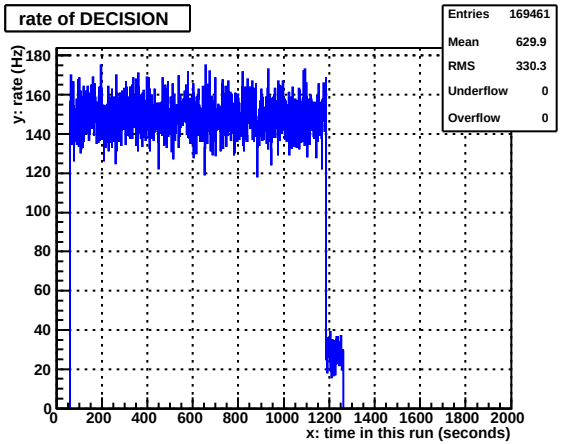
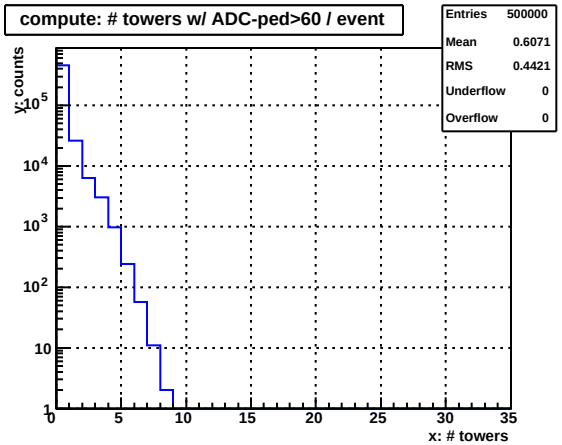

L2 COMPUTE time per input event
Entries 500000
Mean 0.9733
RMS 0.5771
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

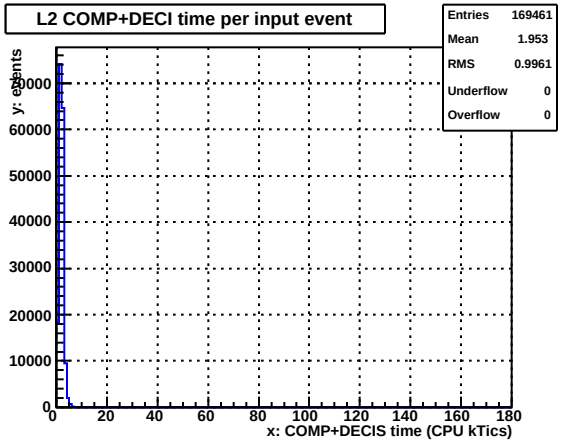

rate of COMPUTE
Entries 500000
Mean 645.3
RMS 340.8
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

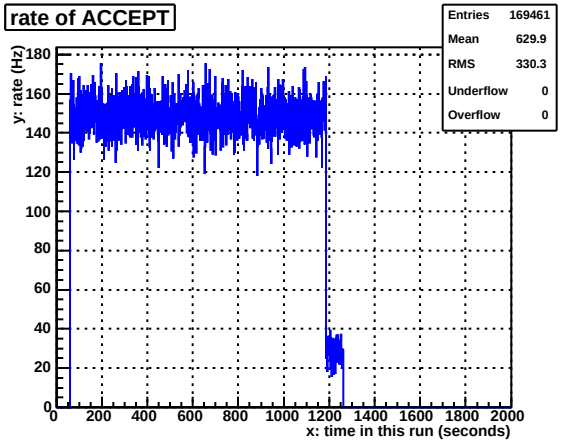

total events 0=anyInput, 10=anyAccept
Entries 1338922
Mean 2.392
RMS 3.155
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

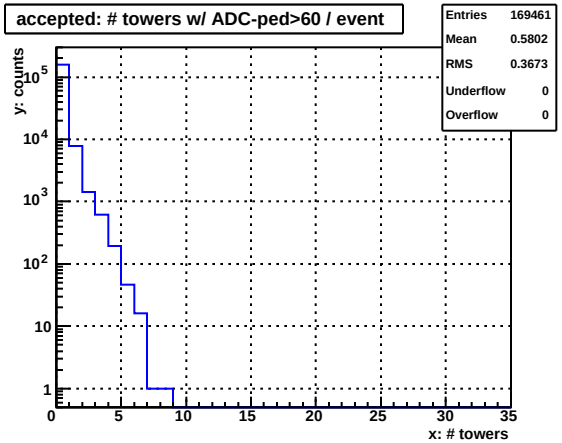

L2 DECISION time per input event
Entries 169461
Mean 1.008
RMS 0.7388
Underflow 0
Overflow 1
y: events
x: DECISION time (CPU kTics)

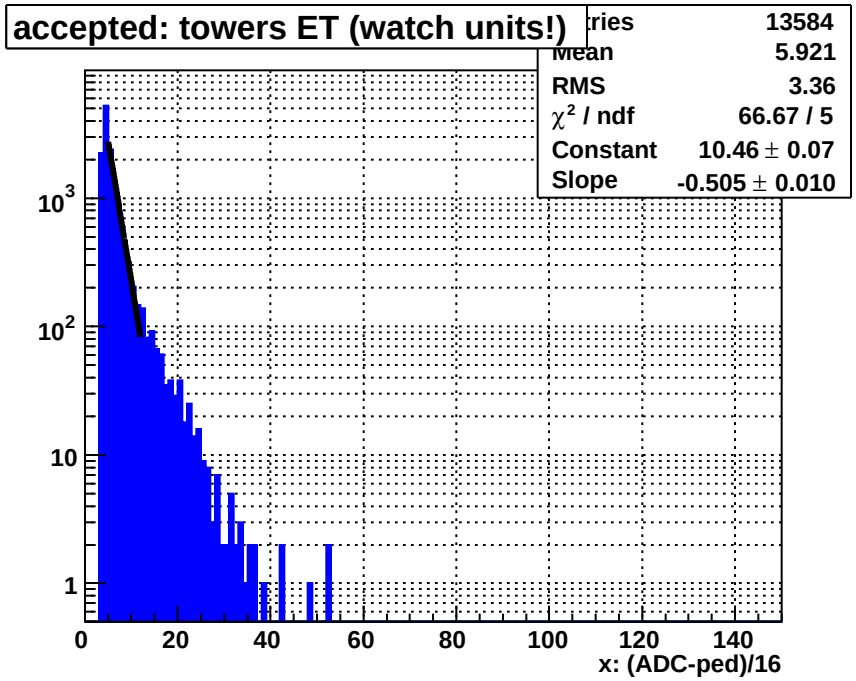

rate of DECISION
Entries 169461
Mean 629.9
RMS 330.3
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

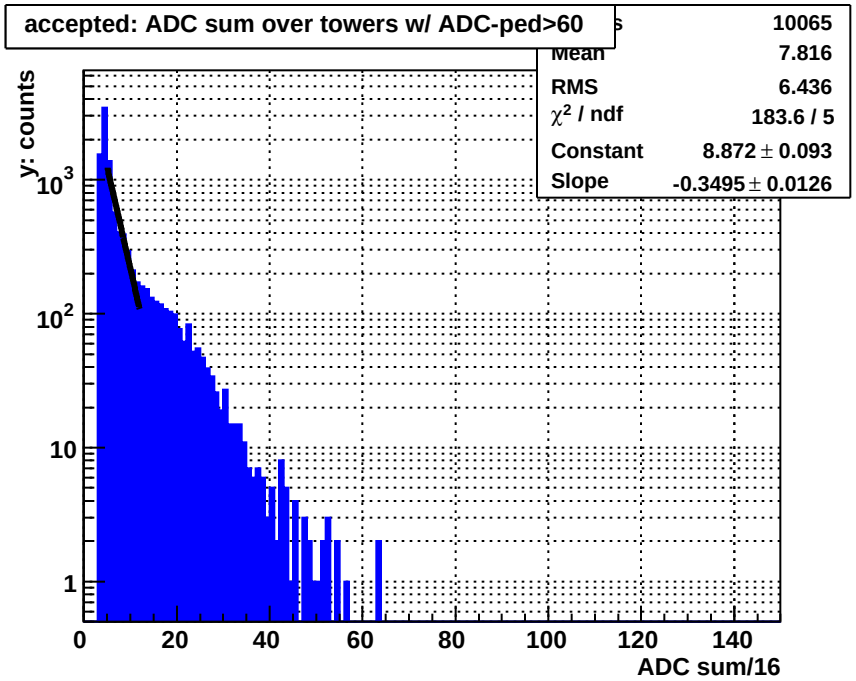

compute: # towers w/ ADC-ped>60 / event
Entries 500000
Mean 0.6071
RMS 0.4421
Underflow 0
Overflow 0
y: counts
x: # towers

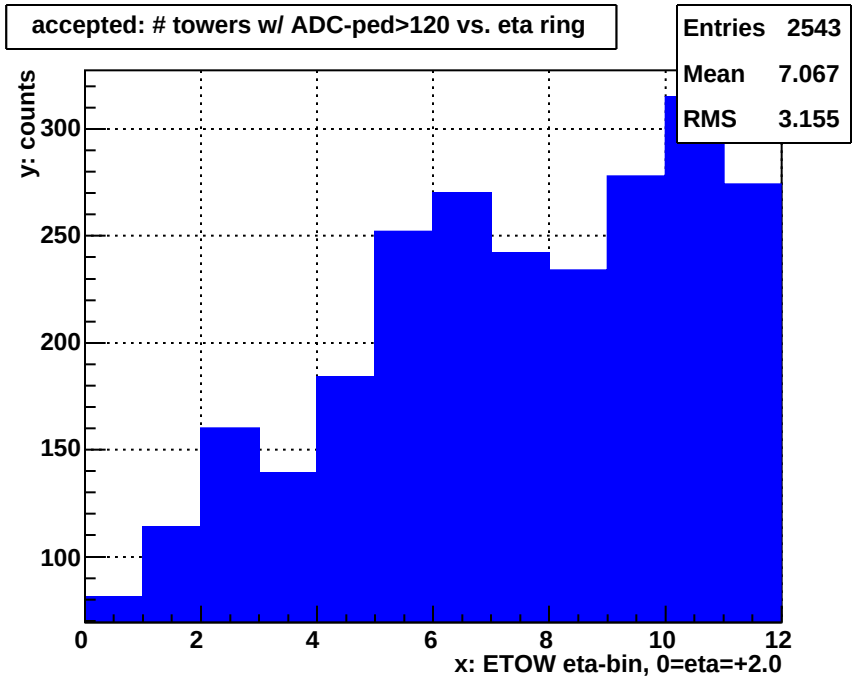

L2 COMP+DECI time per input event
Entries 169461
Mean 1.953
RMS 0.9961
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

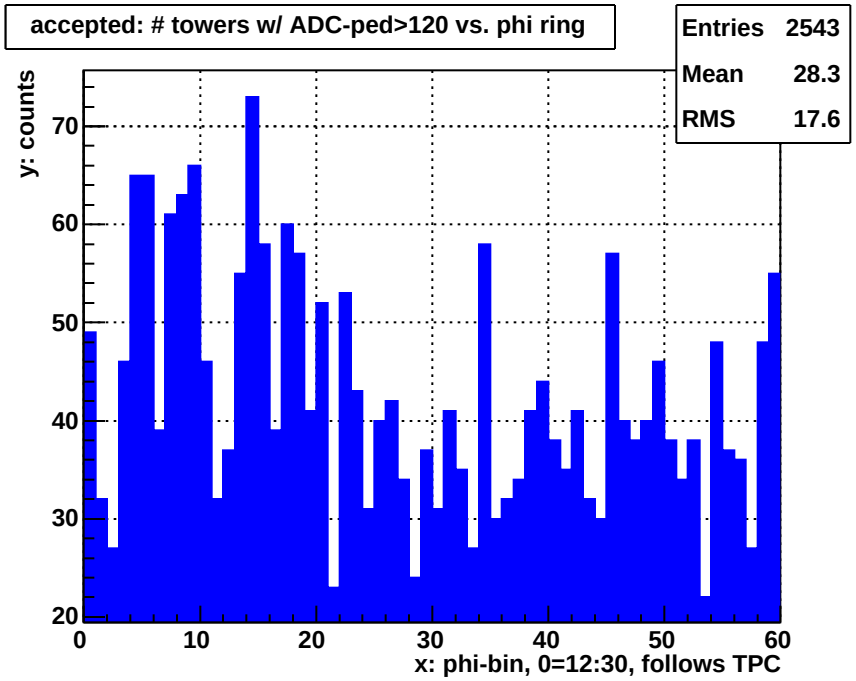

rate of ACCEPT
Entries 169461
Mean 629.9
RMS 330.3
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 169461
Mean 0.5802
RMS 0.3673
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
Entries 13584
Mean 5.921
RMS 3.36
χ² / ndf 66.67 / 5
Constant 10.46 ± 0.07
Slope -0.505 ± 0.010
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
Entries 10065
Mean 7.816
RMS 6.436
χ² / ndf 183.6 / 5
Constant 8.872 ± 0.093
Slope -0.3495 ± 0.0126
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 2543
Mean 7.067
RMS 3.155
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 2543
Mean 28.3
RMS 17.6
y: counts
x: phi-bin, 0=12:30, follows TPC