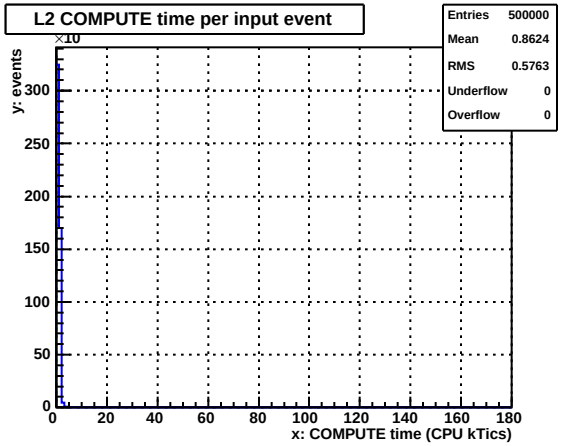
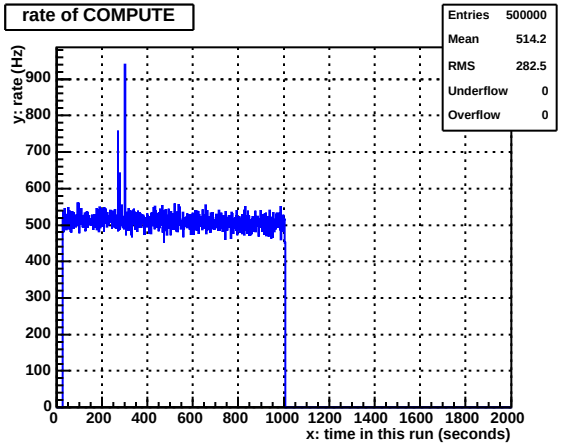
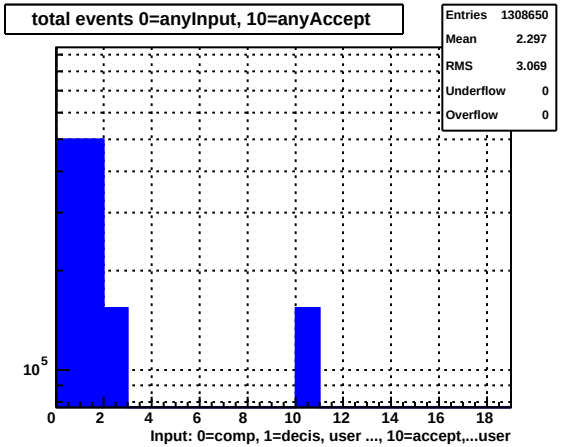
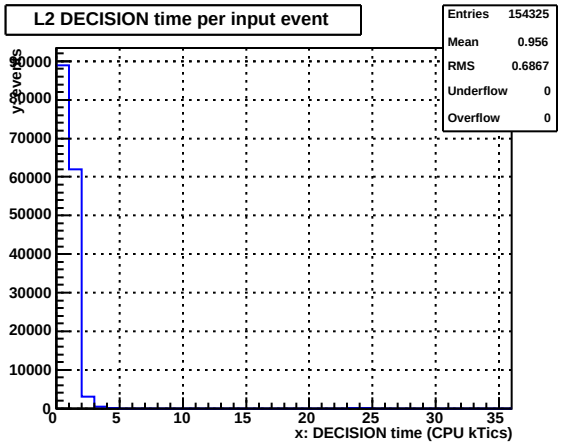
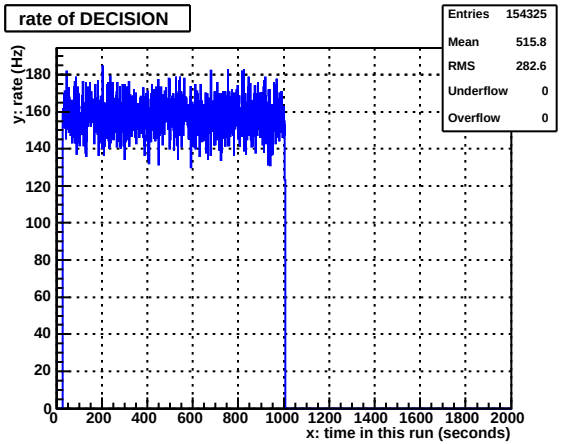
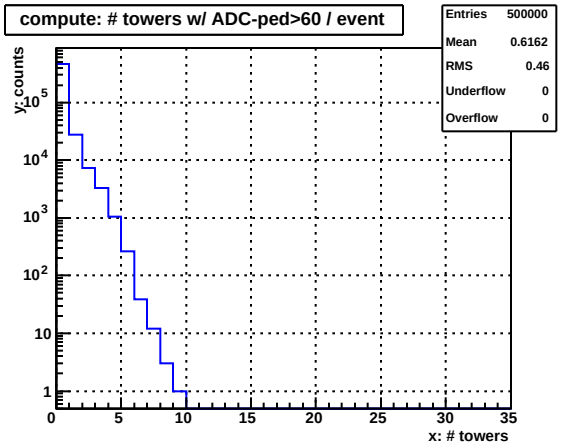

L2 COMPUTE time per input event
Entries 500000
Mean 0.8624
RMS 0.5763
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

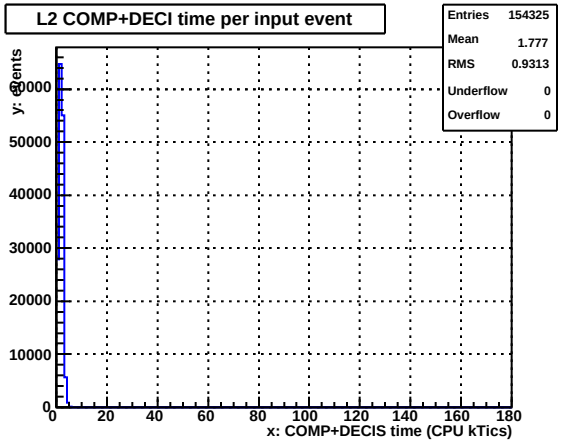

rate of COMPUTE
Entries 500000
Mean 514.2
RMS 282.5
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

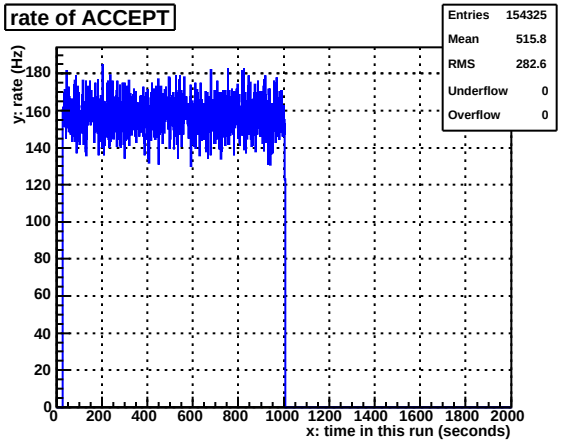

total events 0=anyInput, 10=anyAccept
Entries 1308650
Mean 2.297
RMS 3.069
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

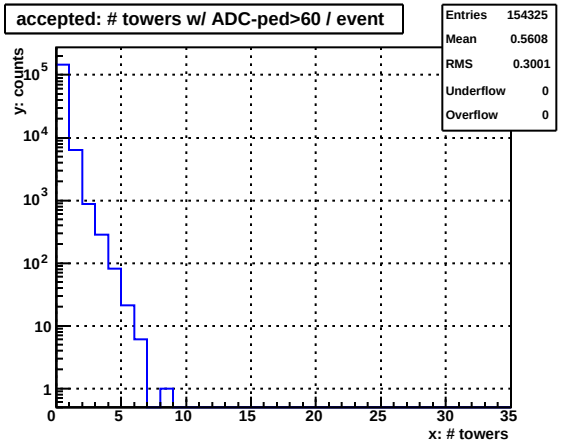

L2 DECISION time per input event
Entries 154325
Mean 0.956
RMS 0.6867
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

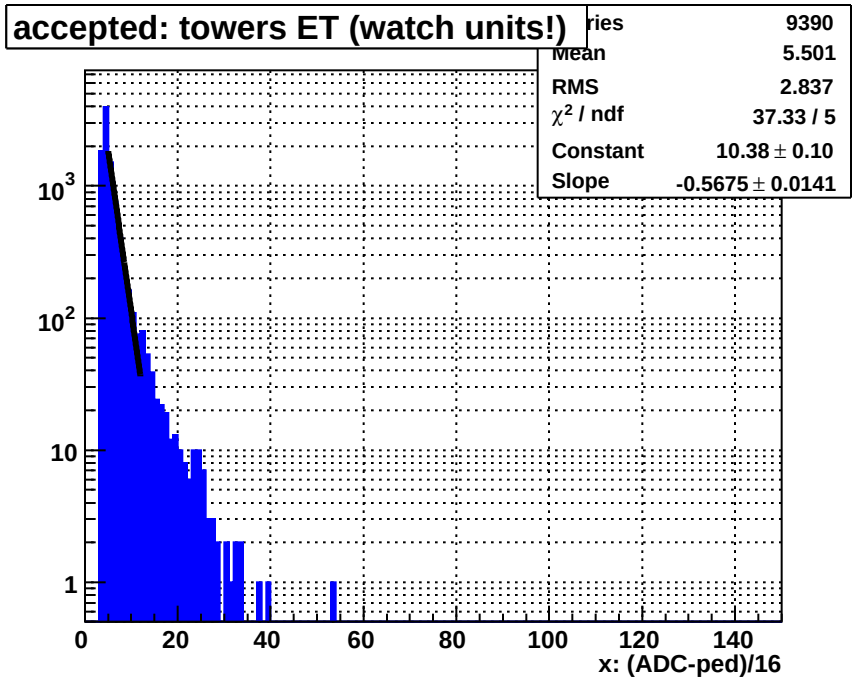

rate of DECISION
Entries 154325
Mean 515.8
RMS 282.6
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

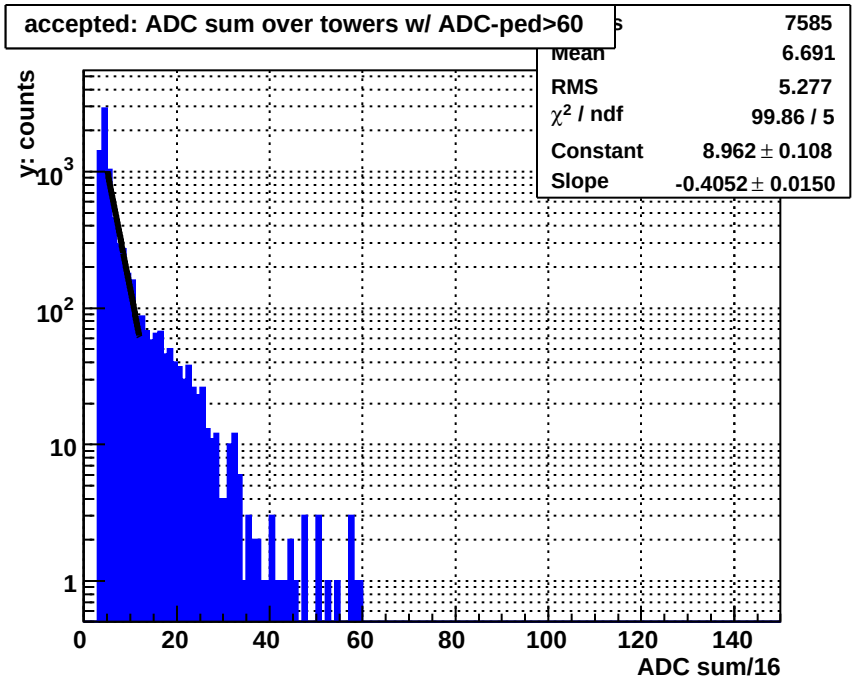

compute: # towers w/ ADC-ped>60 / event
Entries 500000
Mean 0.6162
RMS 0.46
Underflow 0
Overflow 0
y: counts
x: # towers

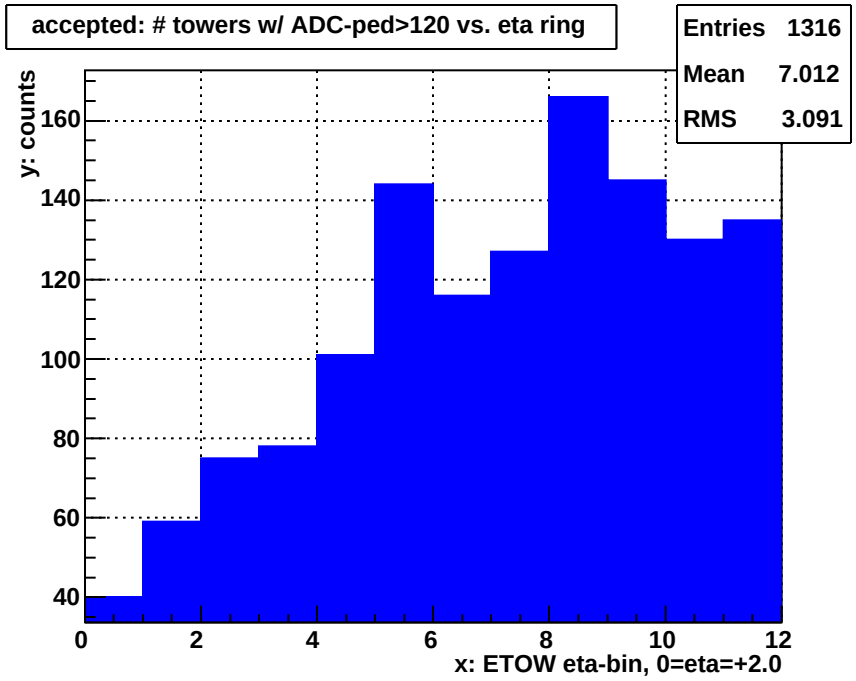

L2 COMP+DECI time per input event
Entries 154325
Mean 1.777
RMS 0.9313
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

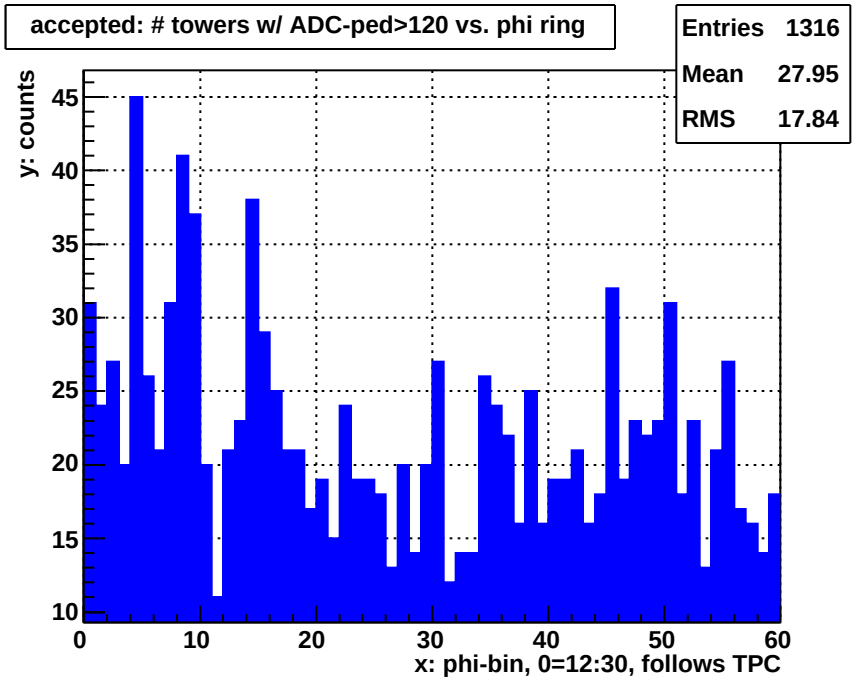

rate of ACCEPT
Entries 154325
Mean 515.8
RMS 282.6
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 154325
Mean 0.5608
RMS 0.3001
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 9390
Mean 5.501
RMS 2.837
χ² / ndf 37.33 / 5
Constant 10.38 ± 0.10
Slope -0.5675 ± 0.0141
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 7585
Mean 6.691
RMS 5.277
χ² / ndf 99.86 / 5
Constant 8.962 ± 0.108
Slope -0.4052 ± 0.0150
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 1316
Mean 7.012
RMS 3.091
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 1316
Mean 27.95
RMS 17.84
y: counts
x: phi-bin, 0=12:30, follows TPC