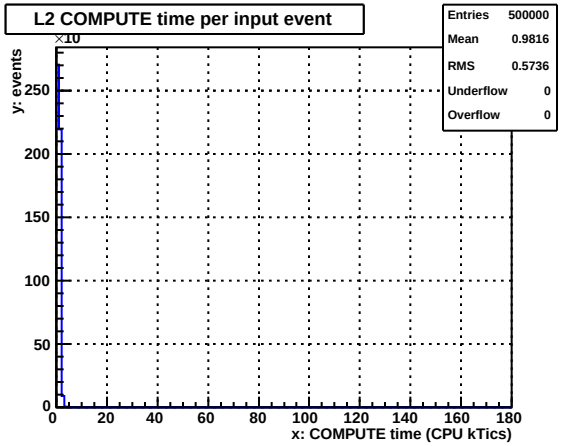
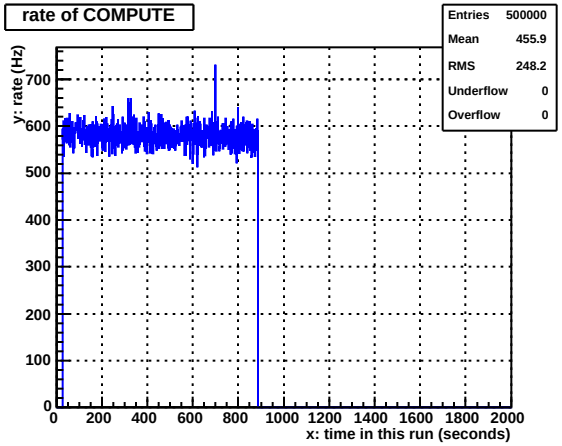
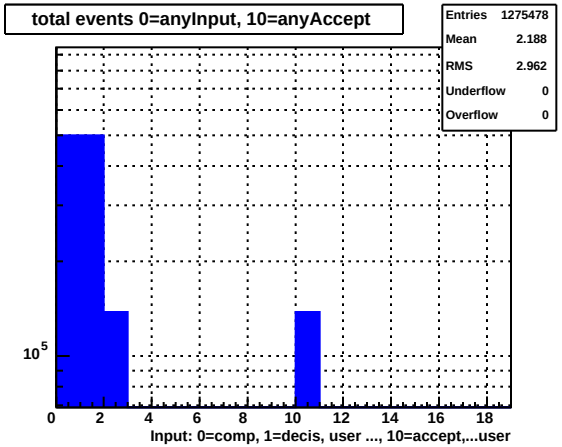
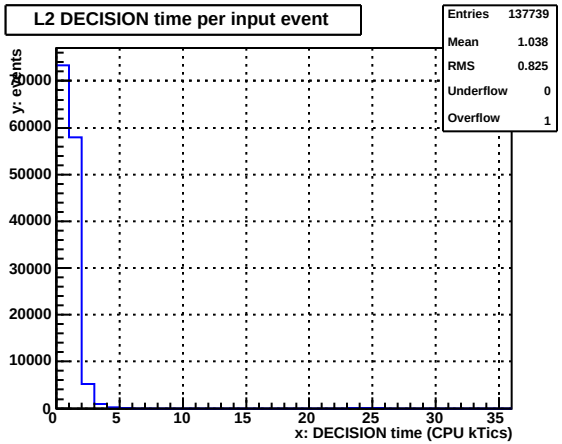
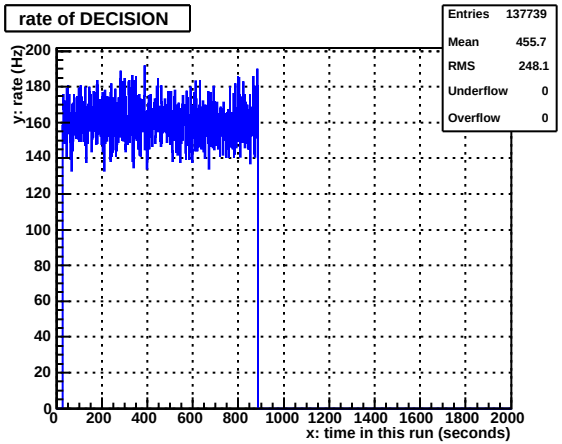
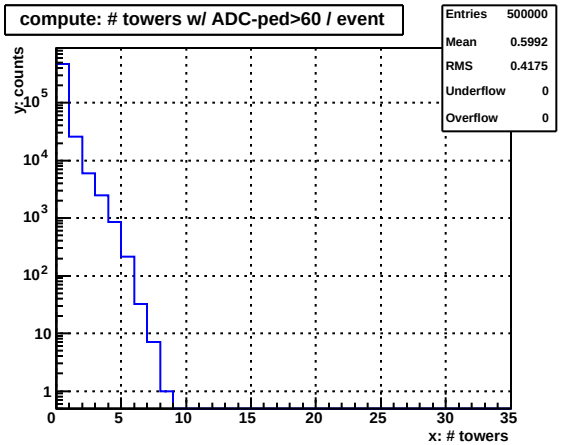

L2 COMPUTE time per input event
Entries 500000
Mean 0.9816
RMS 0.5736
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

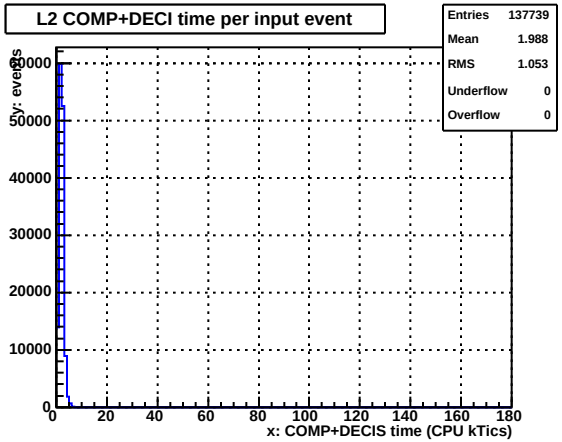

rate of COMPUTE
Entries 500000
Mean 455.9
RMS 248.2
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

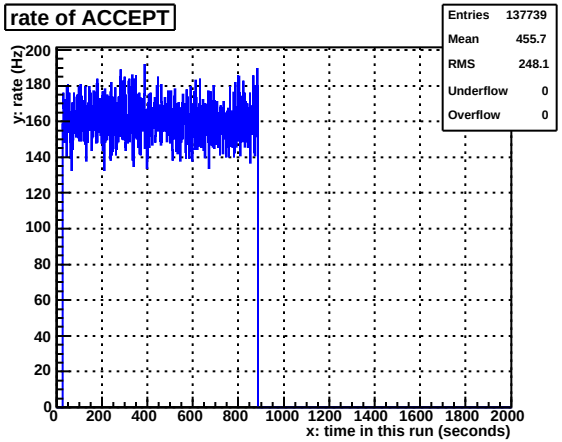

total events 0=anyInput, 10=anyAccept
Entries 1275478
Mean 2.188
RMS 2.962
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

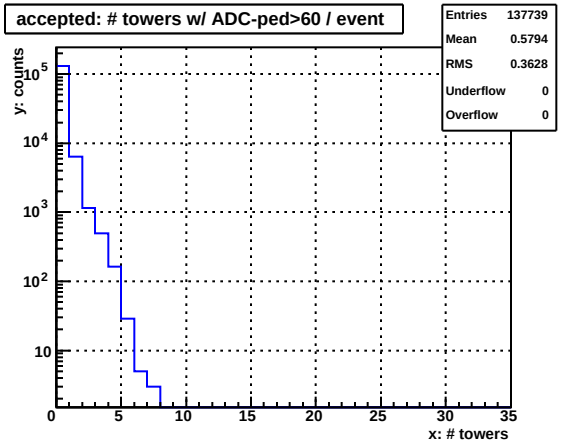

L2 DECISION time per input event
Entries 137739
Mean 1.038
RMS 0.825
Underflow 0
Overflow 1
y: events
x: DECISION time (CPU kTics)

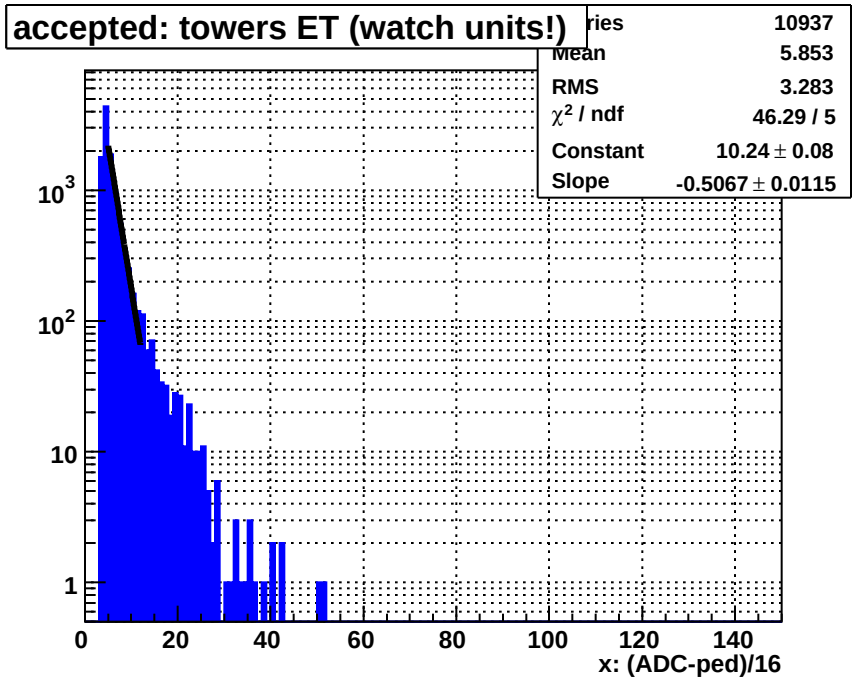

rate of DECISION
Entries 137739
Mean 455.7
RMS 248.1
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

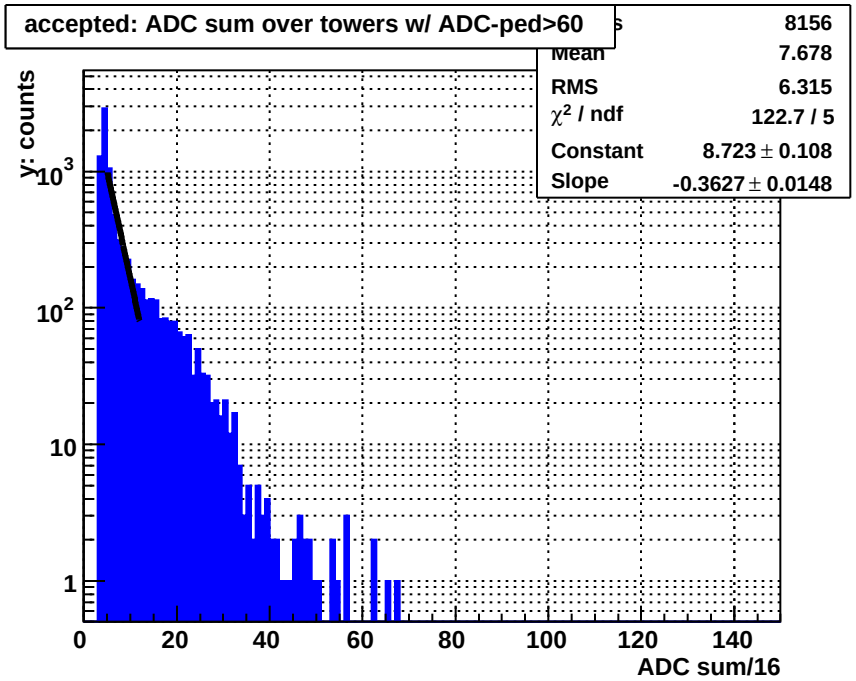

compute: # towers w/ ADC-ped>60 / event
Entries 500000
Mean 0.5992
RMS 0.4175
Underflow 0
Overflow 0
y: counts
x: # towers

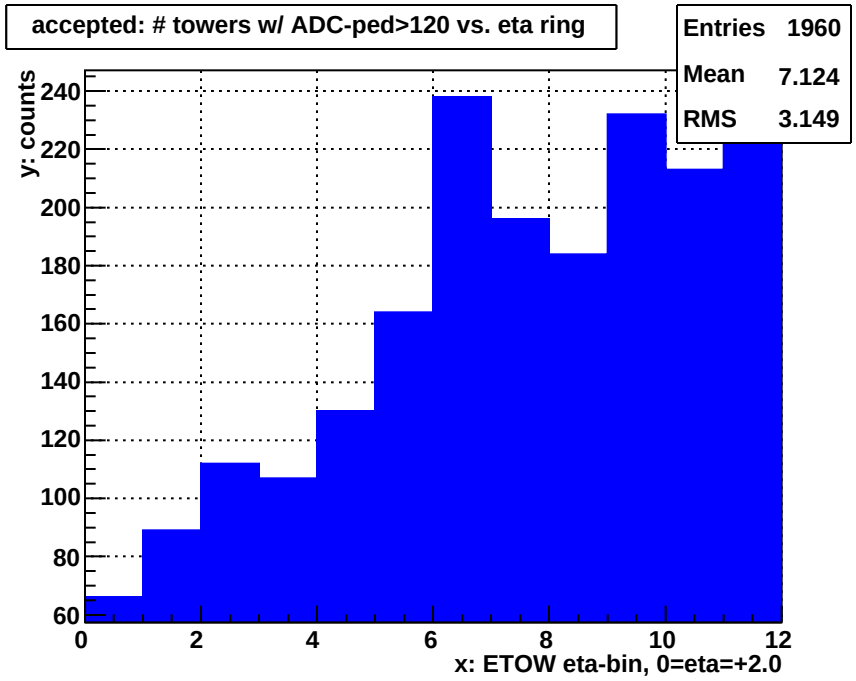

L2 COMP+DECI time per input event
Entries 137739
Mean 1.988
RMS 1.053
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

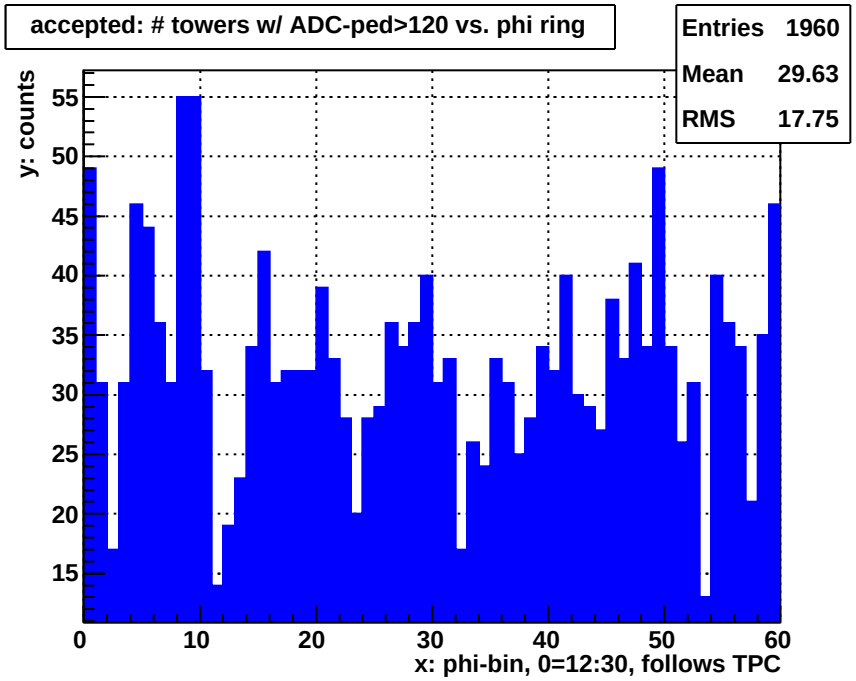

rate of ACCEPT
Entries 137739
Mean 455.7
RMS 248.1
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 137739
Mean 0.5794
RMS 0.3628
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 10937
Mean 5.853
RMS 3.283
χ² / ndf 46.29 / 5
Constant 10.24 ± 0.08
Slope -0.5067 ± 0.0115
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 8156
Mean 7.678
RMS 6.315
χ² / ndf 122.7 / 5
Constant 8.723 ± 0.108
Slope -0.3627 ± 0.0148
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 1960
Mean 7.124
RMS 3.149
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 1960
Mean 29.63
RMS 17.75
y: counts
x: phi-bin, 0=12:30, follows TPC