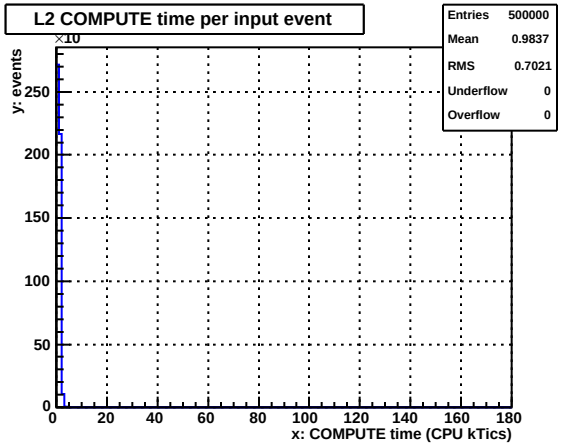
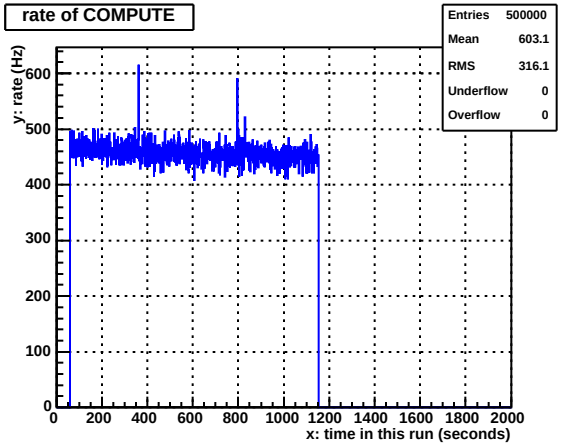
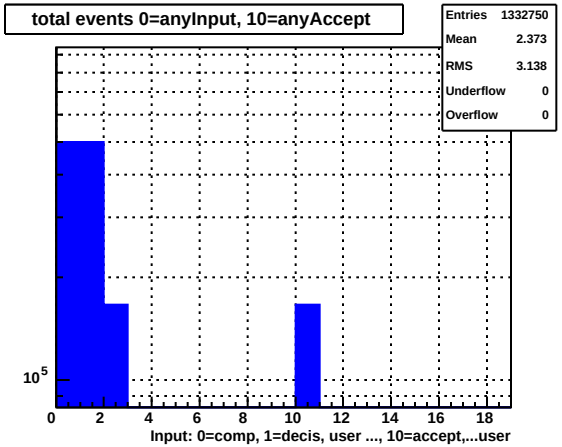
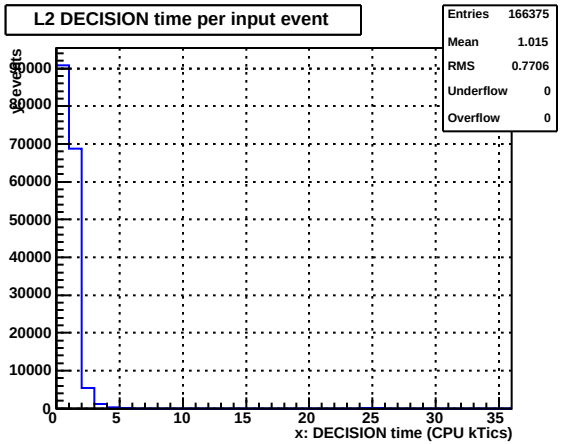
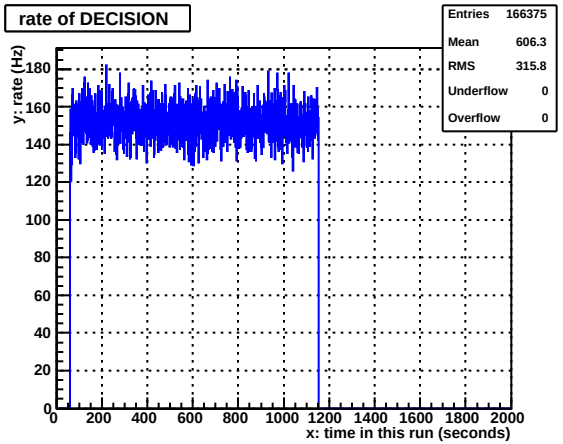
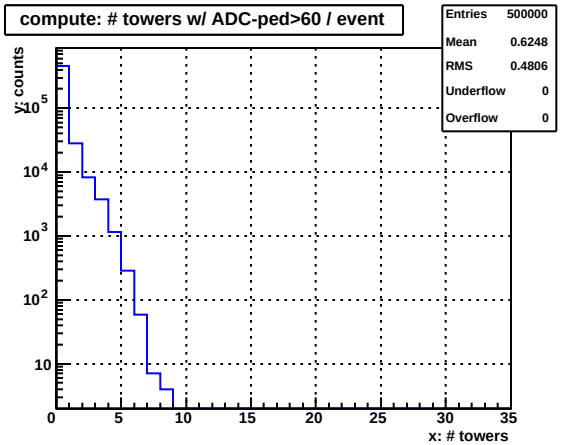

L2 COMPUTE time per input event
Entries 500000
Mean 0.9837
RMS 0.7021
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

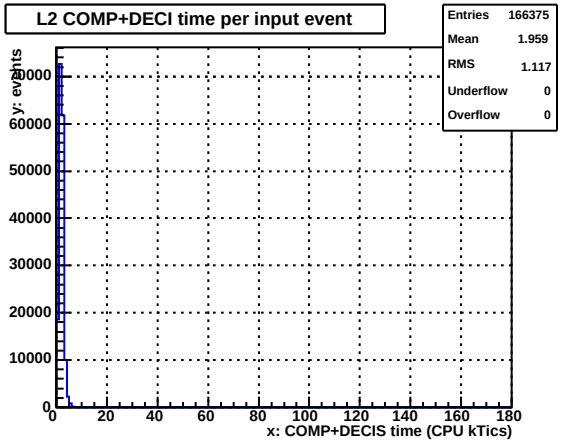

rate of COMPUTE
Entries 500000
Mean 603.1
RMS 316.1
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

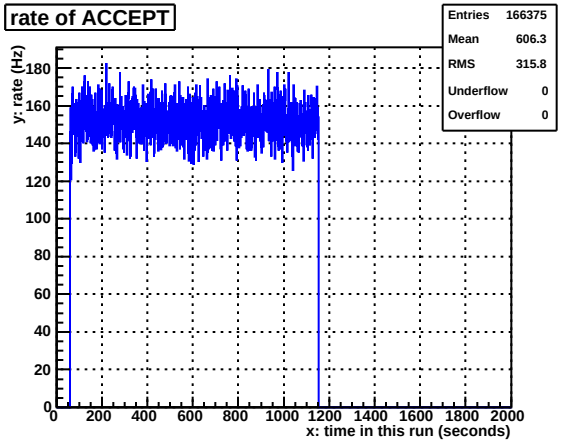

total events 0=anyInput, 10=anyAccept
Entries 1332750
Mean 2.373
RMS 3.138
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

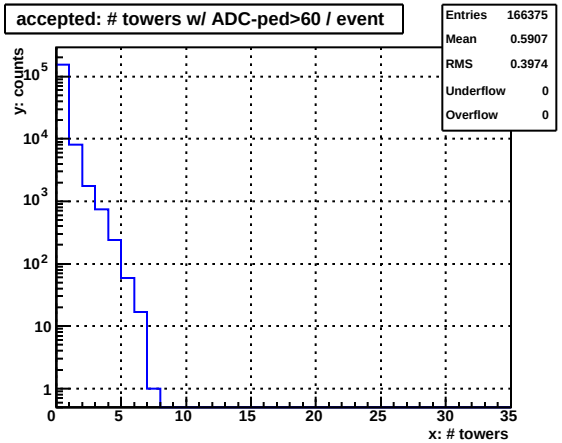

L2 DECISION time per input event
Entries 166375
Mean 1.015
RMS 0.7706
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

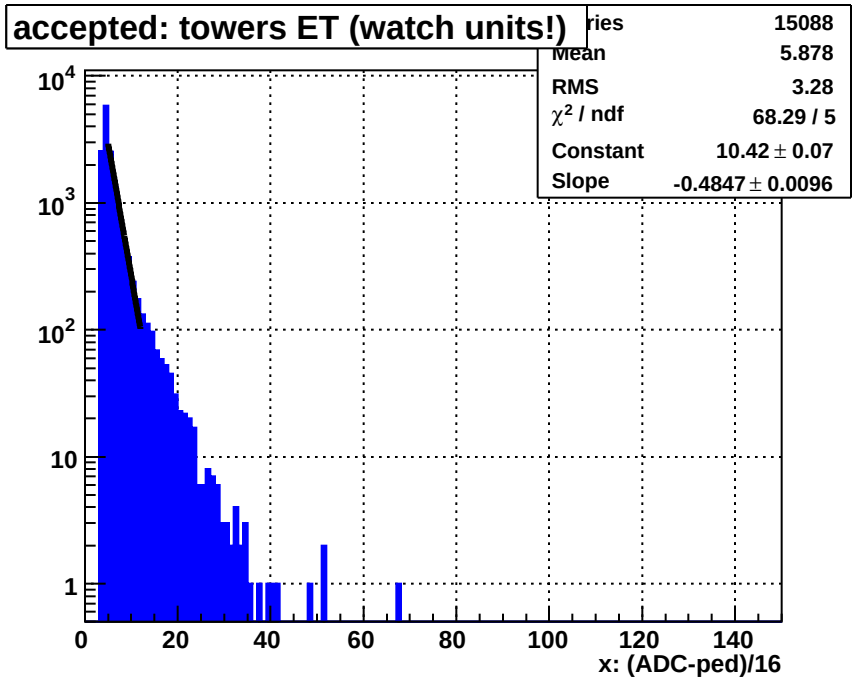

rate of DECISION
Entries 166375
Mean 606.3
RMS 315.8
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

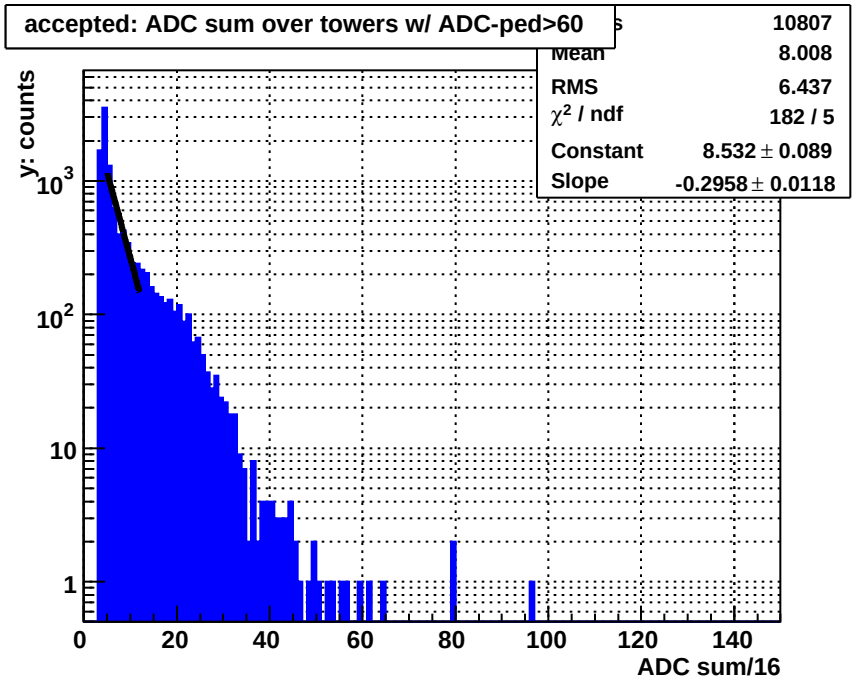

compute: # towers w/ ADC-ped>60 / event
Entries 500000
Mean 0.6248
RMS 0.4806
Underflow 0
Overflow 0
y: counts
x: # towers

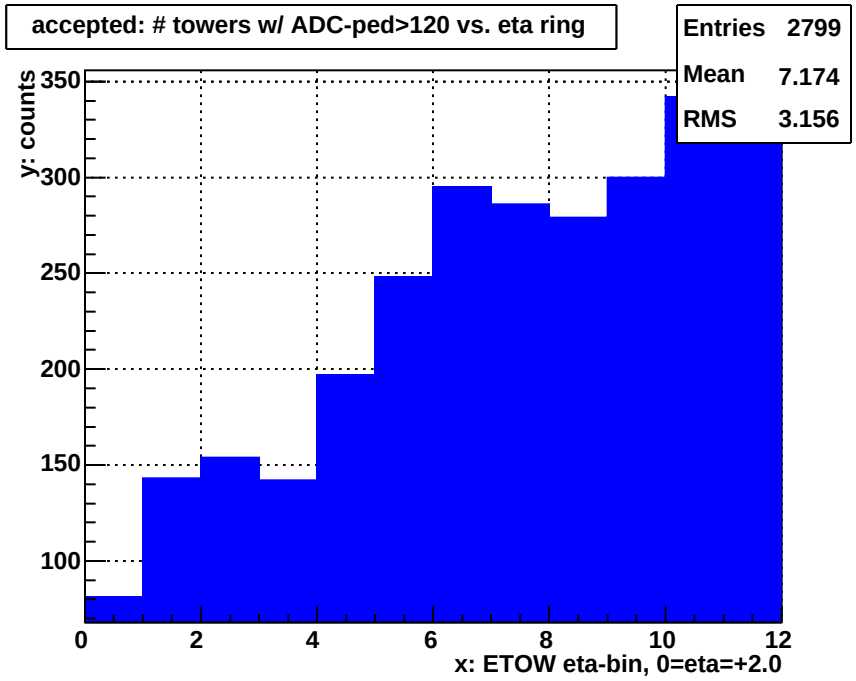

L2 COMP+DECI time per input event
Entries 166375
Mean 1.959
RMS 1.117
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

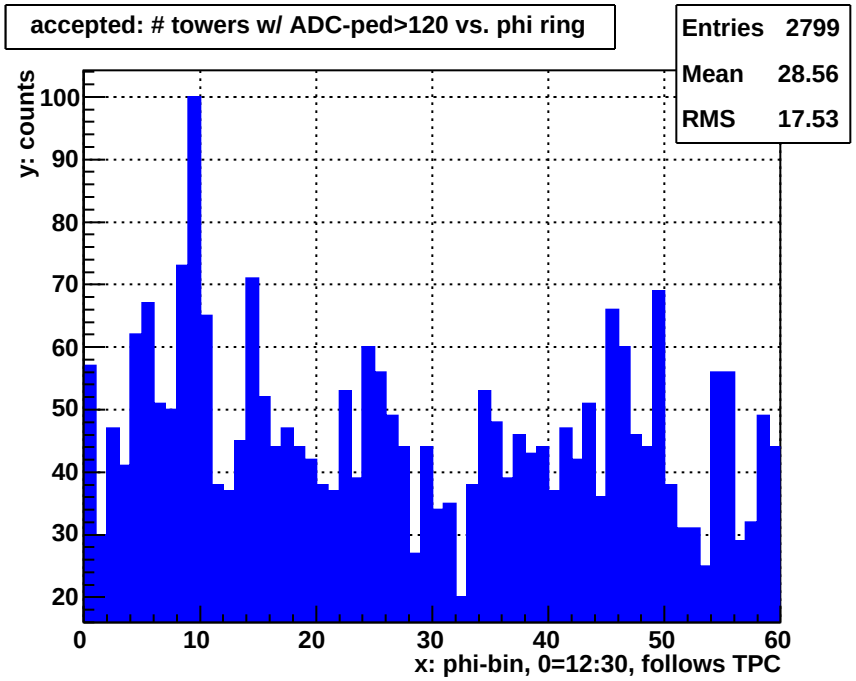

rate of ACCEPT
Entries 166375
Mean 606.3
RMS 315.8
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 166375
Mean 0.5907
RMS 0.3974
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 15088
Mean 5.878
RMS 3.28
χ² / ndf 68.29 / 5
Constant 10.42 ± 0.07
Slope -0.4847 ± 0.0096
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 10807
Mean 8.008
RMS 6.437
χ² / ndf 182 / 5
Constant 8.532 ± 0.089
Slope -0.2958 ± 0.0118
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 2799
Mean 7.174
RMS 3.156
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 2799
Mean 28.56
RMS 17.53
y: counts
x: phi-bin, 0=12:30, follows TPC