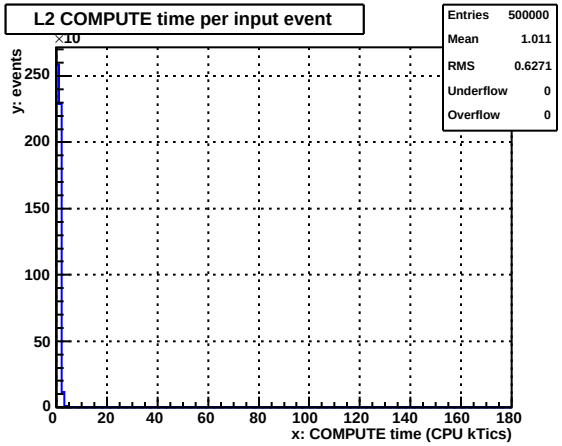
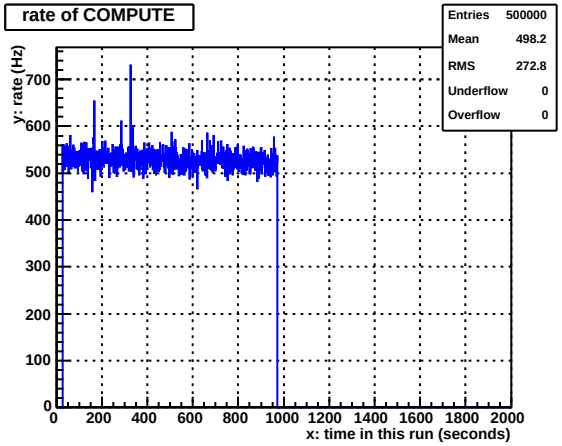
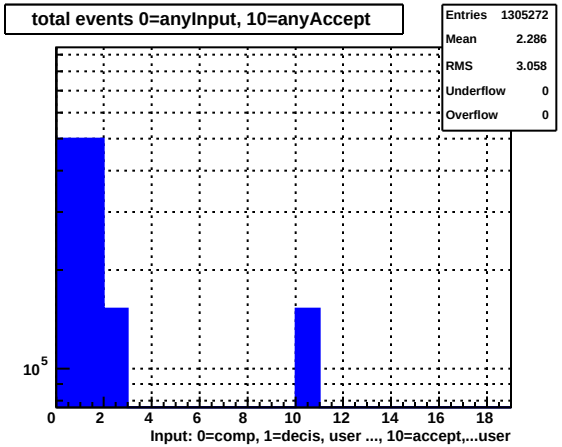
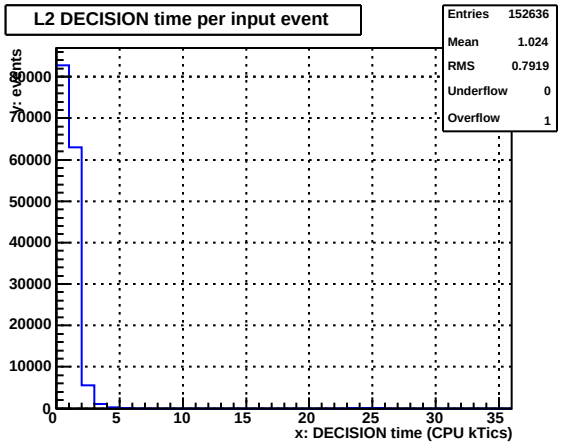
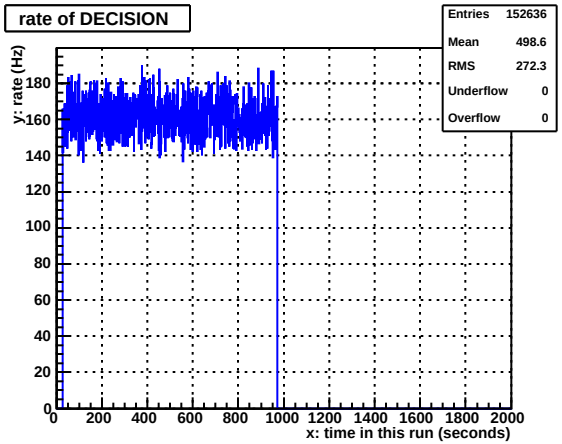
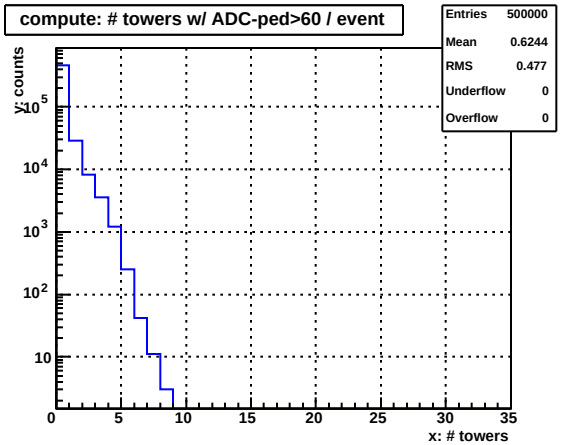

L2 COMPUTE time per input event
Entries 500000
Mean 1.011
RMS 0.6271
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

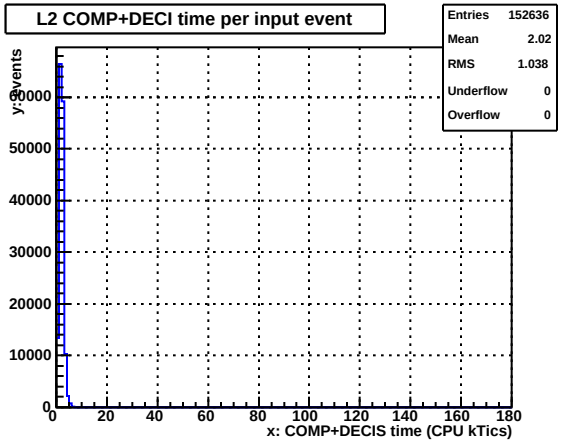

rate of COMPUTE
Entries 500000
Mean 498.2
RMS 272.8
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

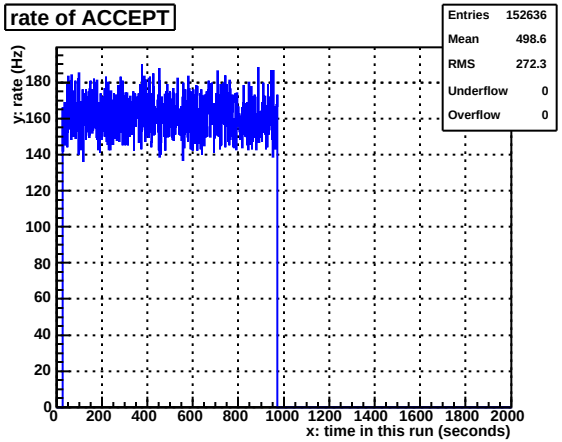

total events 0=anyInput, 10=anyAccept
Entries 1305272
Mean 2.286
RMS 3.058
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

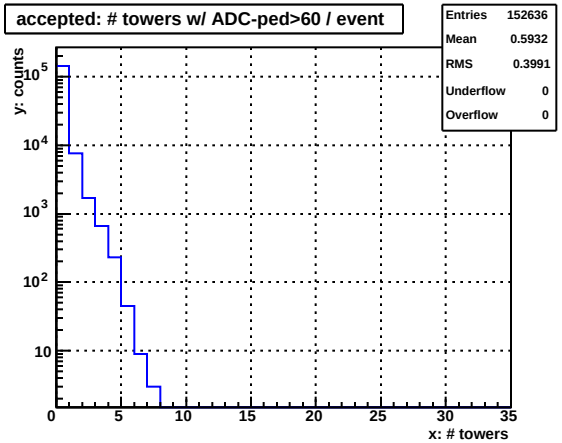

L2 DECISION time per input event
Entries 152636
Mean 1.024
RMS 0.7919
Underflow 0
Overflow 1
y: events
x: DECISION time (CPU kTics)

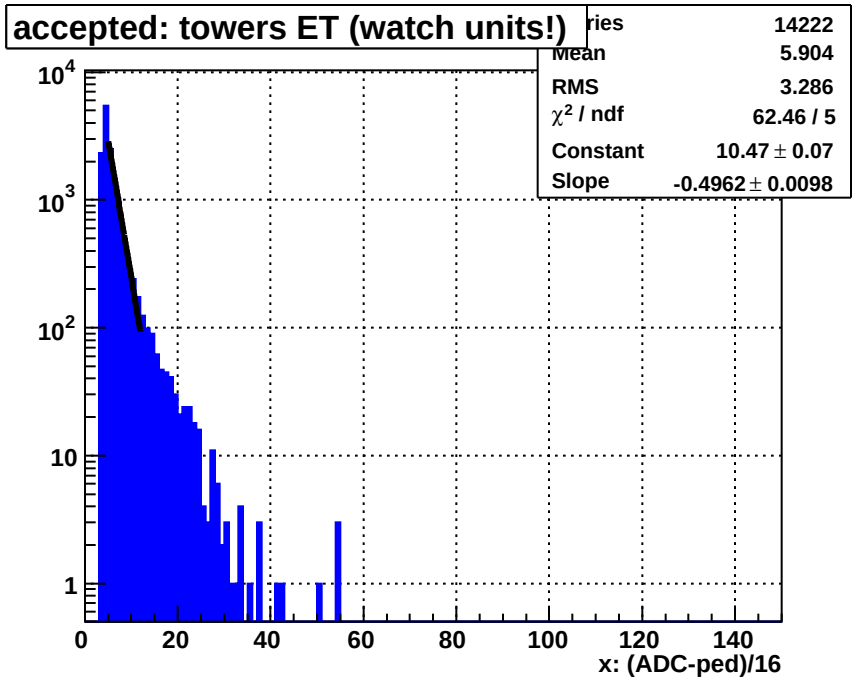

rate of DECISION
Entries 152636
Mean 498.6
RMS 272.3
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

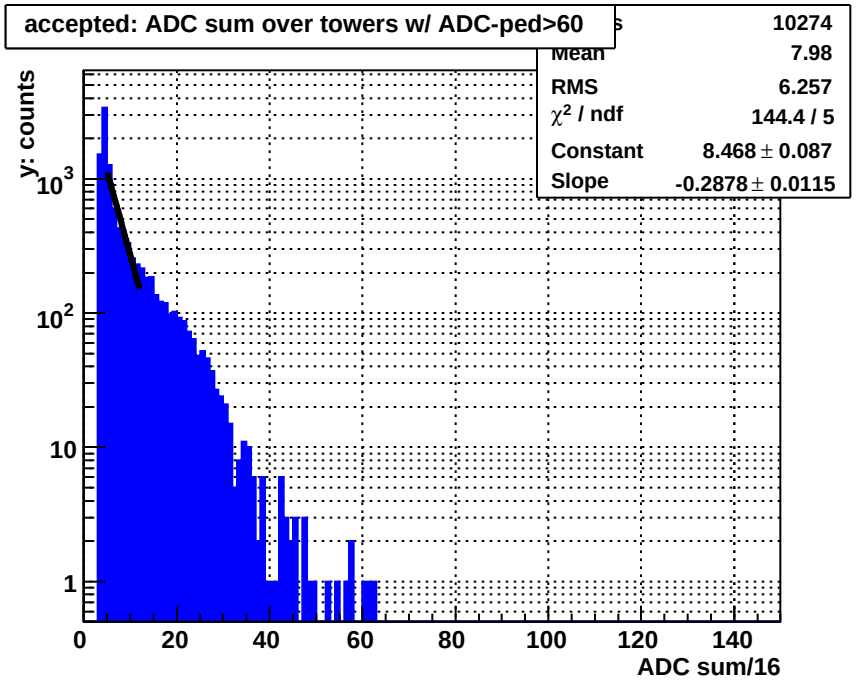

compute: # towers w/ ADC-ped>60 / event
Entries 500000
Mean 0.6244
RMS 0.477
Underflow 0
Overflow 0
y: counts
x: # towers

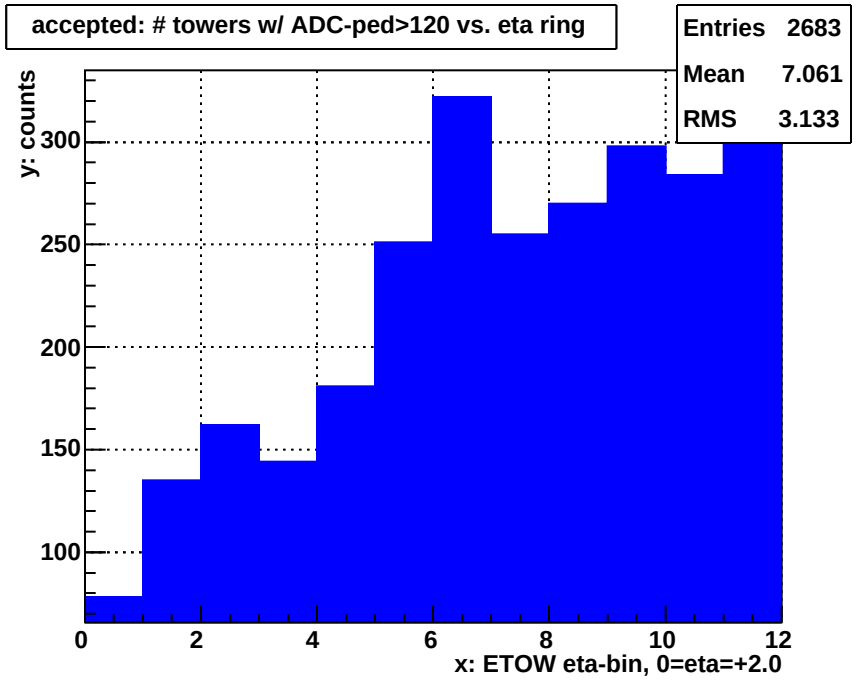

L2 COMP+DECI time per input event
Entries 152636
Mean 2.02
RMS 1.038
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

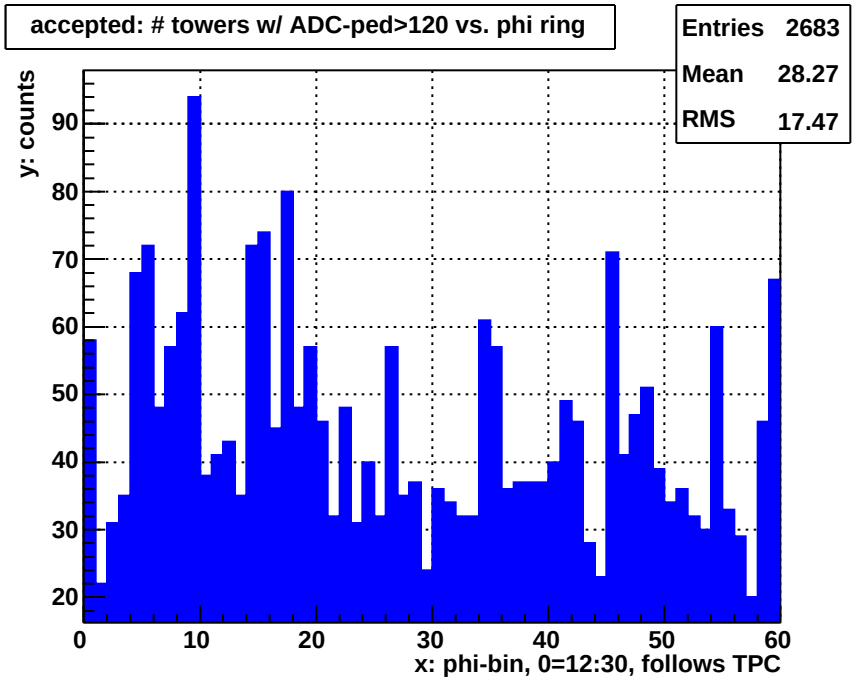

rate of ACCEPT
Entries 152636
Mean 498.6
RMS 272.3
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 152636
Mean 0.5932
RMS 0.3991
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 14222
Mean 5.904
RMS 3.286
χ² / ndf 62.46 / 5
Constant 10.47 ± 0.07
Slope -0.4962 ± 0.0098
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 10274
Mean 7.98
RMS 6.257
χ² / ndf 144.4 / 5
Constant 8.468 ± 0.087
Slope -0.2878 ± 0.0115
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 2683
Mean 7.061
RMS 3.133
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 2683
Mean 28.27
RMS 17.47
y: counts
x: phi-bin, 0=12:30, follows TPC