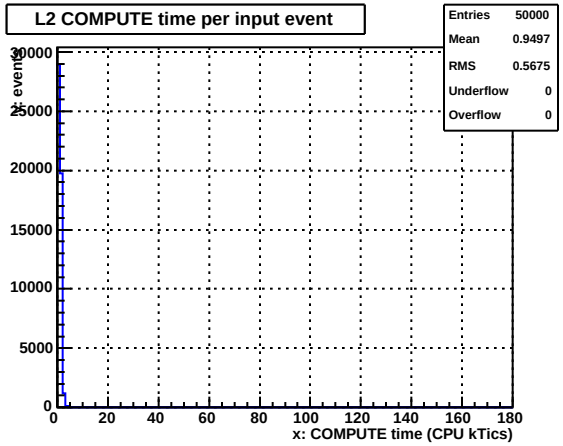
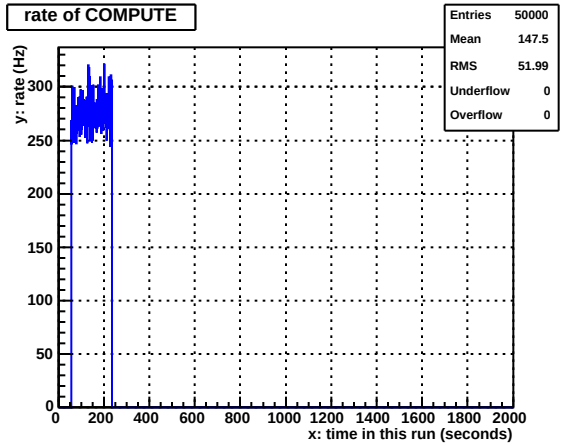
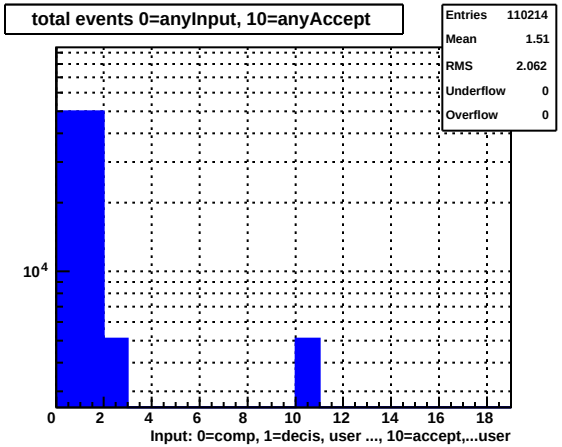
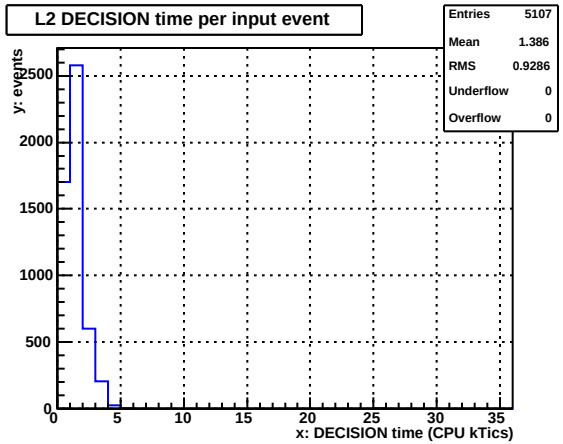
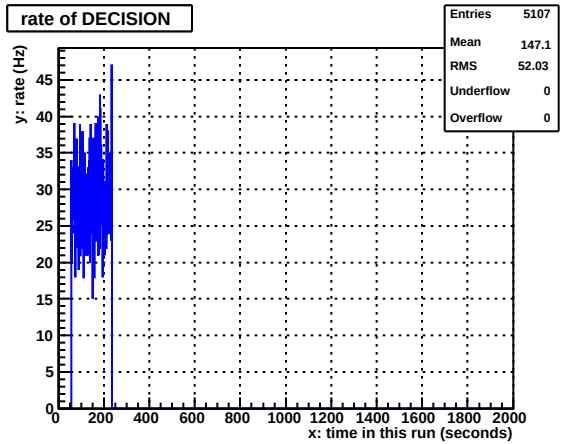
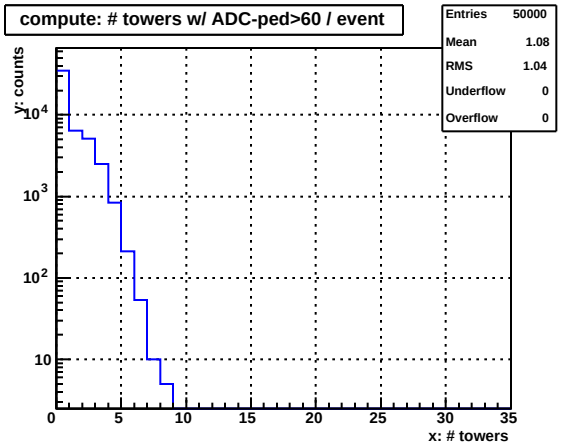

L2 COMPUTE time per input event
Entries 50000
Mean 0.9497
RMS 0.5675
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

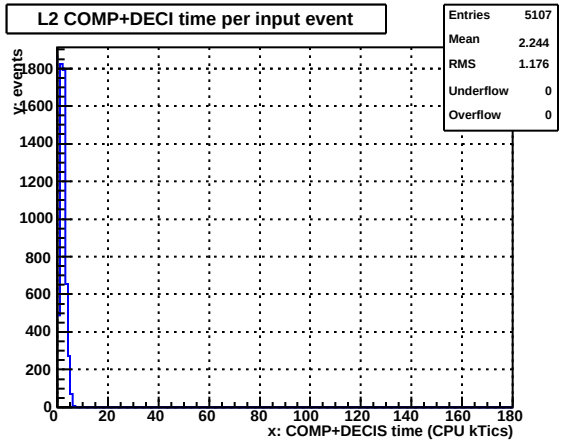

rate of COMPUTE
Entries 50000
Mean 147.5
RMS 51.99
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

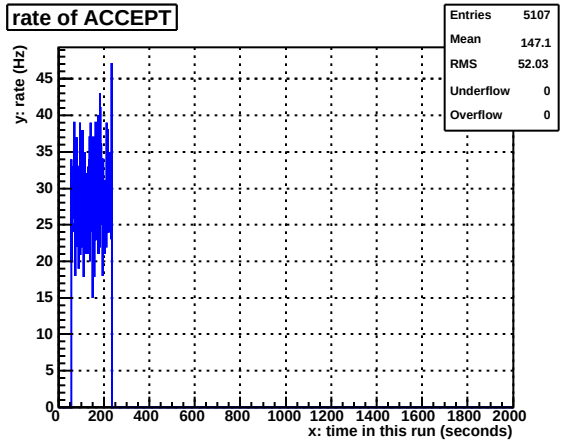

total events 0=anyInput, 10=anyAccept
Entries 110214
Mean 1.51
RMS 2.062
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

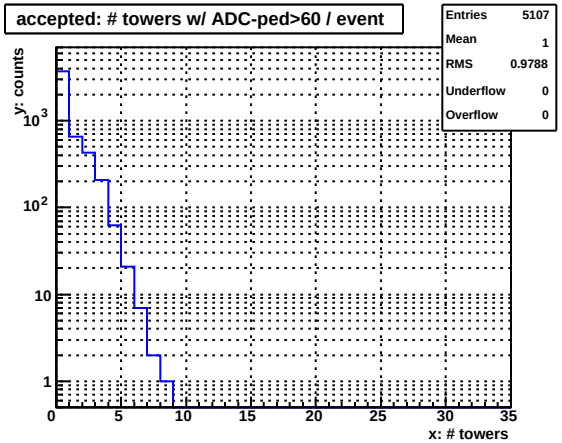

L2 DECISION time per input event
Entries 5107
Mean 1.386
RMS 0.9286
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

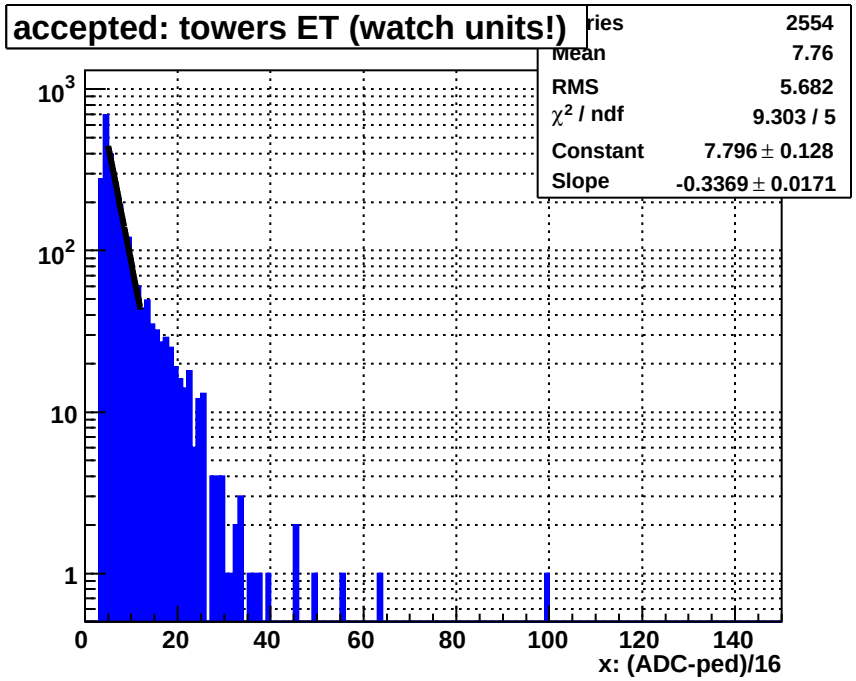

rate of DECISION
Entries 5107
Mean 147.1
RMS 52.03
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

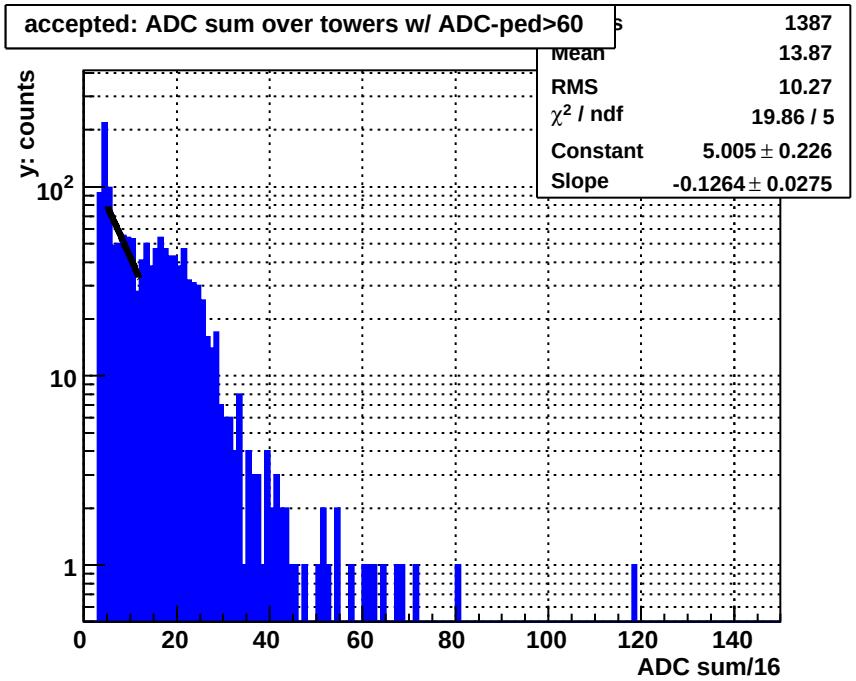

compute: # towers w/ ADC-ped>60 / event
Entries 50000
Mean 1.08
RMS 1.04
Underflow 0
Overflow 0
y: counts
x: # towers

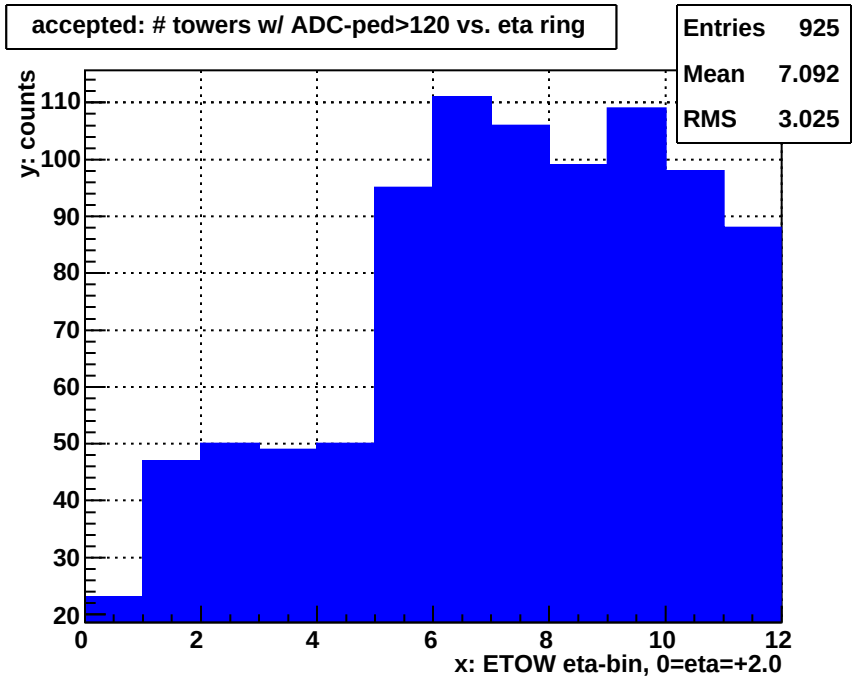

L2 COMP+DECI time per input event
Entries 5107
Mean 2.244
RMS 1.176
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

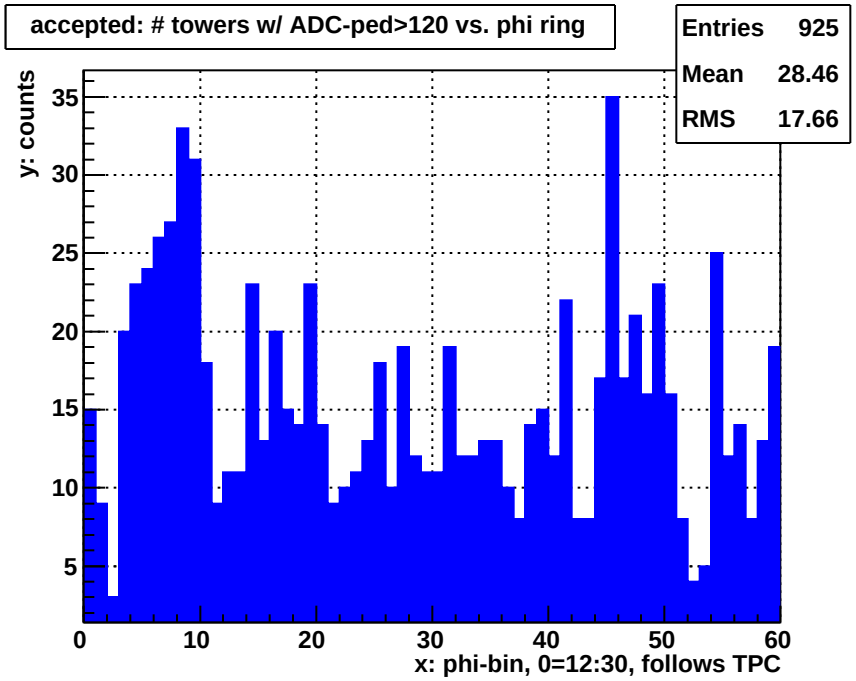

rate of ACCEPT
Entries 5107
Mean 147.1
RMS 52.03
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 5107
Mean 1
RMS 0.9788
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 2554
Mean 7.76
RMS 5.682
χ² / ndf 9.303 / 5
Constant 7.796 ± 0.128
Slope -0.3369 ± 0.0171
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 1387
Mean 13.87
RMS 10.27
χ² / ndf 19.86 / 5
Constant 5.005 ± 0.226
Slope -0.1264 ± 0.0275
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 925
Mean 7.092
RMS 3.025
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 925
Mean 28.46
RMS 17.66
y: counts
x: phi-bin, 0=12:30, follows TPC