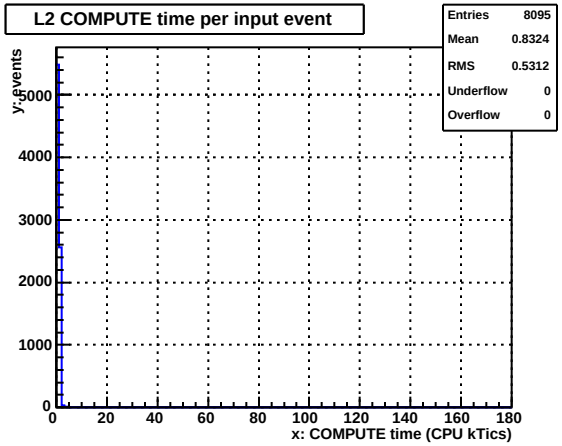
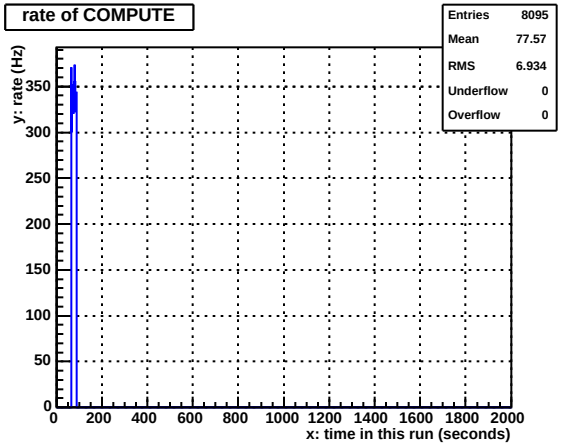
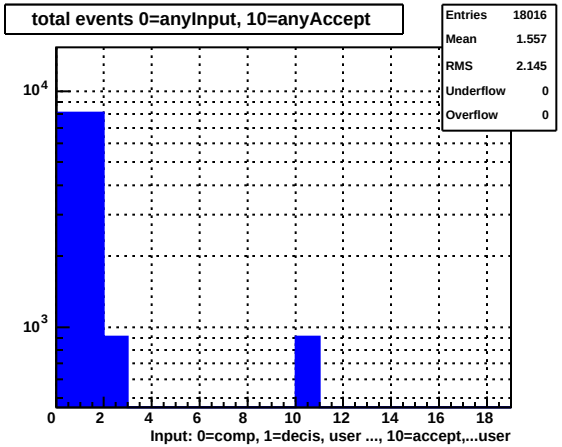
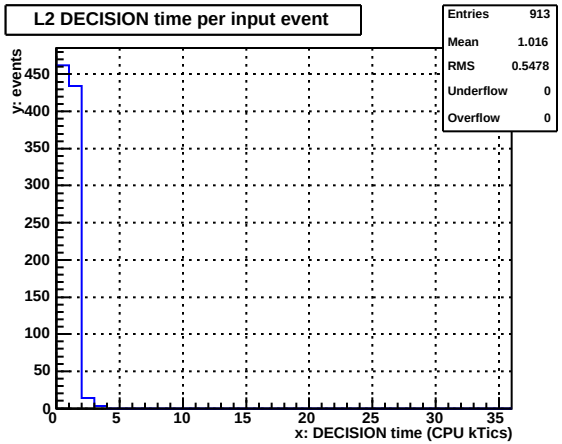
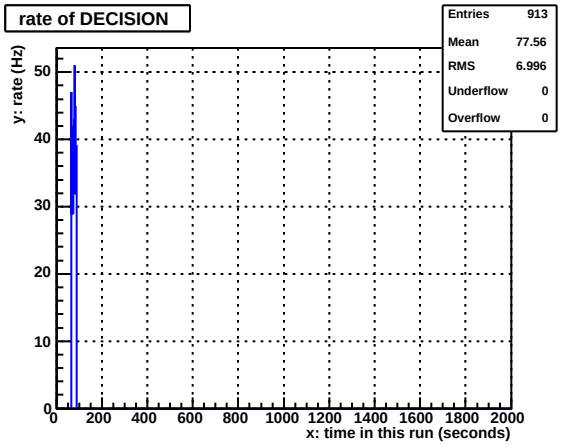
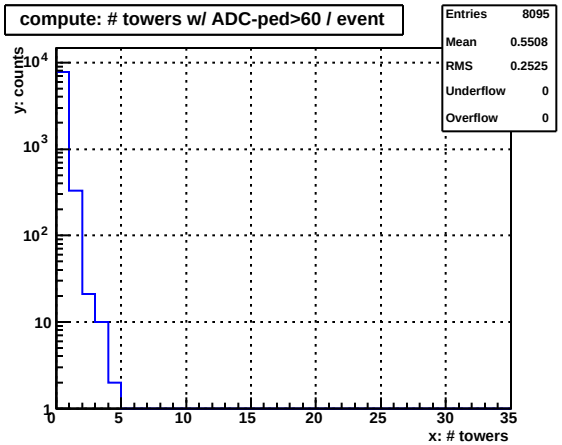

L2 COMPUTE time per input event
Entries 8095
Mean 0.8324
RMS 0.5312
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

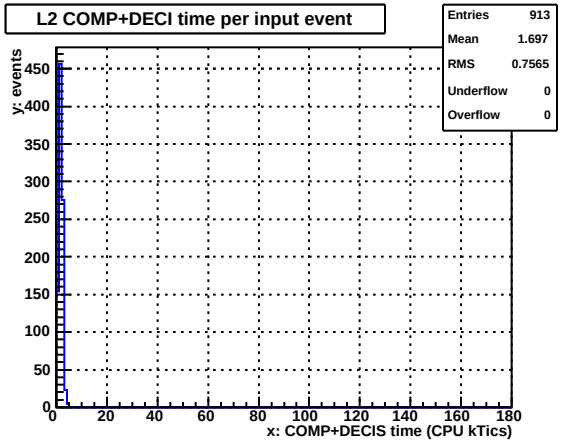

rate of COMPUTE
Entries 8095
Mean 77.57
RMS 6.934
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

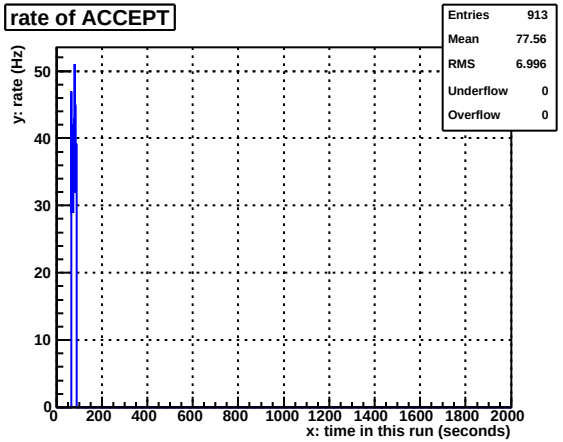

total events 0=anyInput, 10=anyAccept
Entries 18016
Mean 1.557
RMS 2.145
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

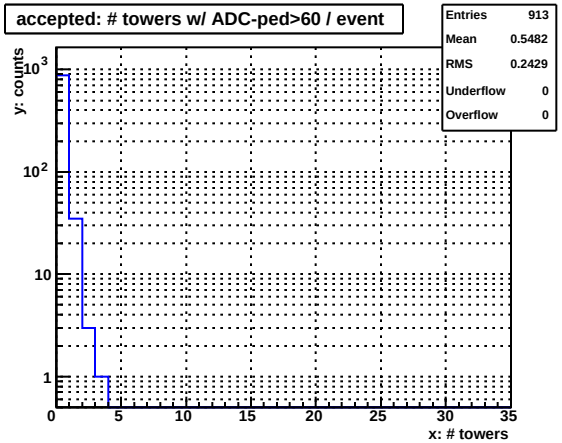

L2 DECISION time per input event
Entries 913
Mean 1.016
RMS 0.5478
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

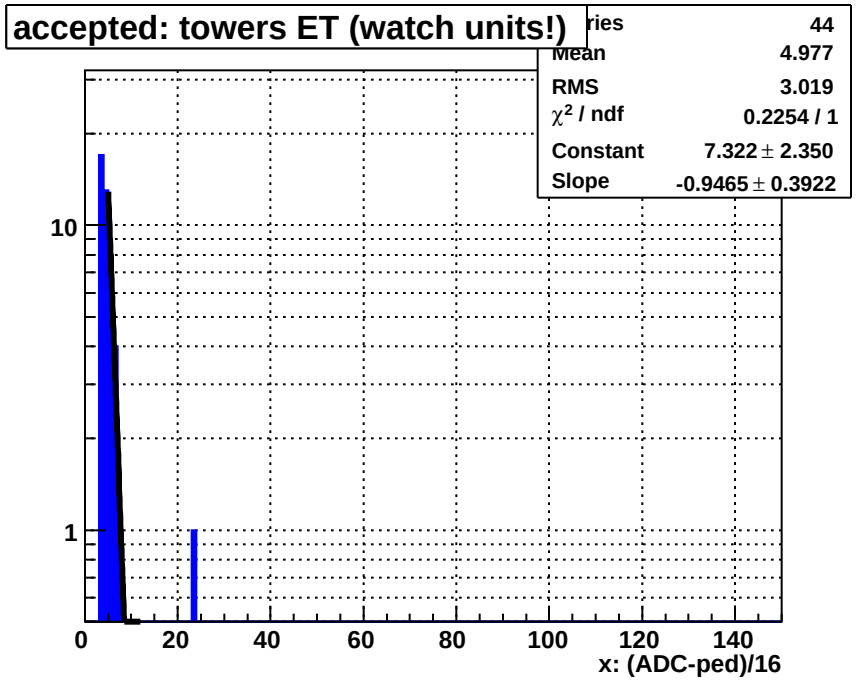

rate of DECISION
Entries 913
Mean 77.56
RMS 6.996
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

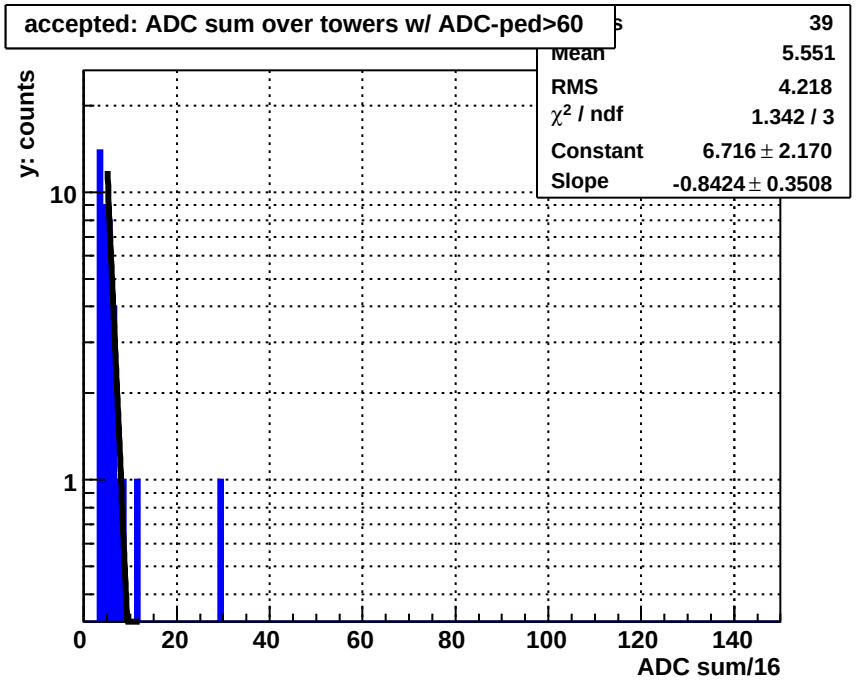

compute: # towers w/ ADC-ped>60 / event
Entries 8095
Mean 0.5508
RMS 0.2525
Underflow 0
Overflow 0
y: counts
x: # towers

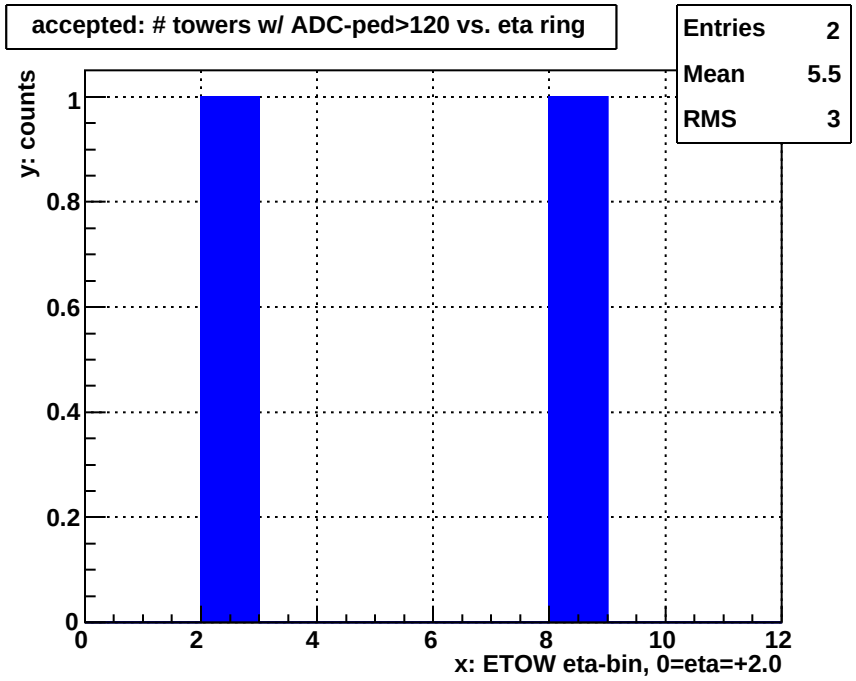

L2 COMP+DECI time per input event
Entries 913
Mean 1.697
RMS 0.7565
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

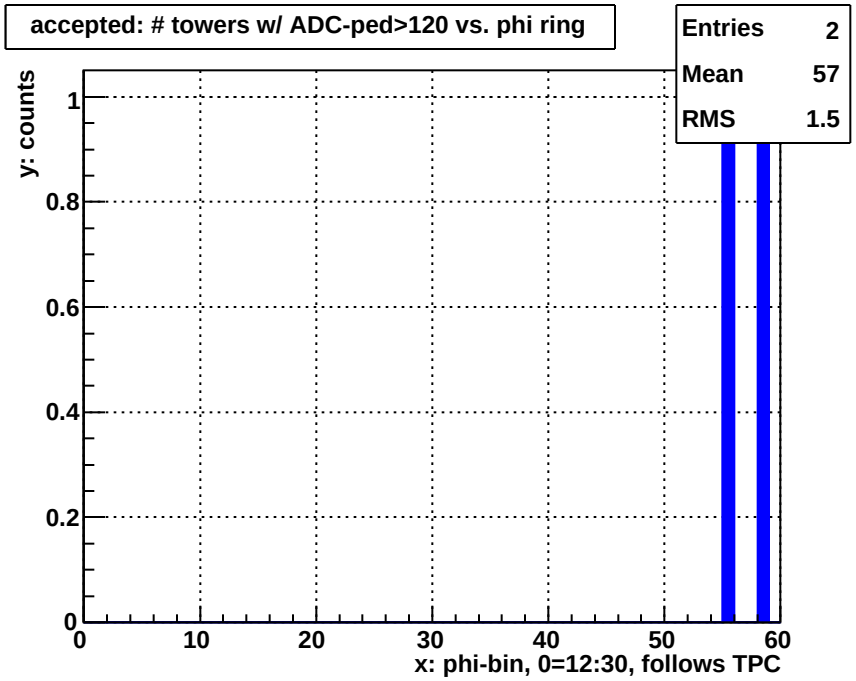

rate of ACCEPT
Entries 913
Mean 77.56
RMS 6.996
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 913
Mean 0.5482
RMS 0.2429
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 44
Mean 4.977
RMS 3.019
χ² / ndf 0.2254 / 1
Constant 7.322 ± 2.350
Slope -0.9465 ± 0.3922
10
1
0 20 40 60 80 100 120 140
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 39
Mean 5.551
RMS 4.218
χ² / ndf 1.342 / 3
Constant 6.716 ± 2.170
Slope -0.8424 ± 0.3508
y: counts
10
1
0 20 40 60 80 100 120 140
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 2
Mean 5.5
RMS 3
y: counts
1
0.8
0.6
0.4
0.2
0
0 2 4 6 8 10 12
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 2
Mean 57
RMS 1.5
y: counts
1
0.8
0.6
0.4
0.2
0
0 10 20 30 40 50 60
x: phi-bin, 0=12:30, follows TPC