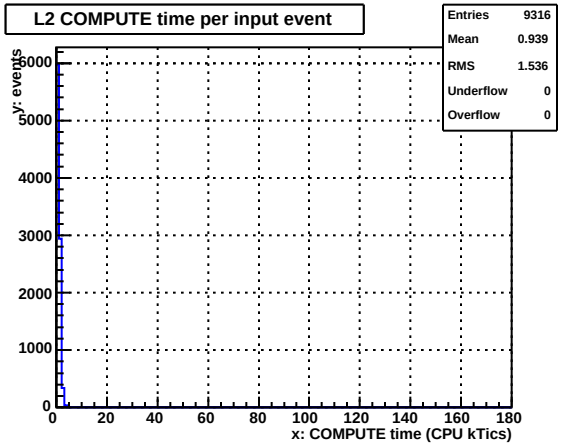
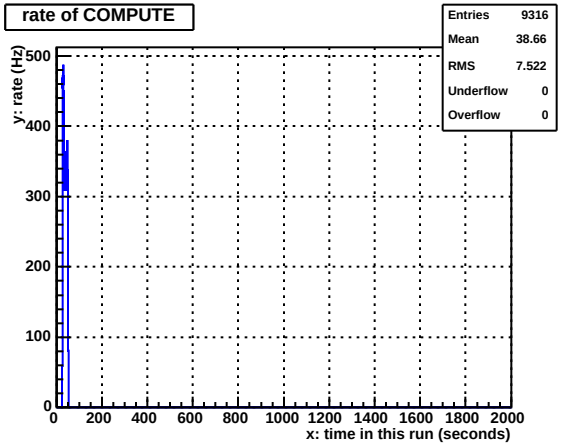
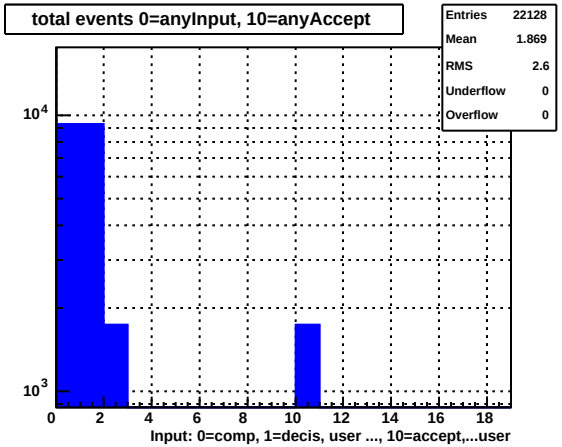
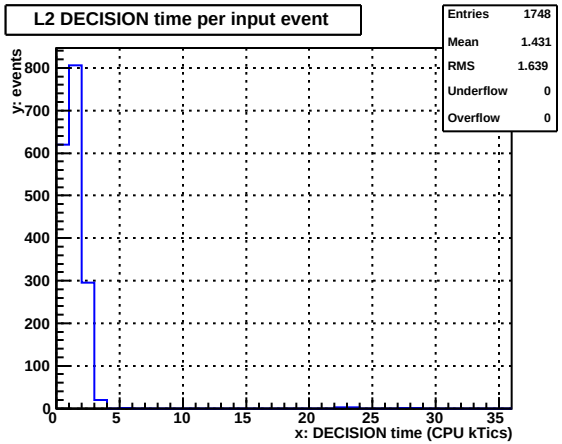
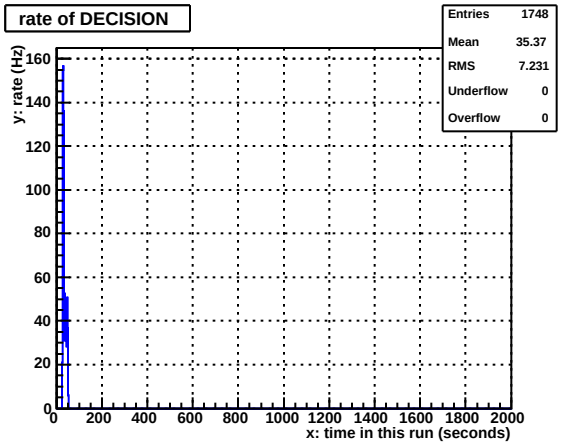
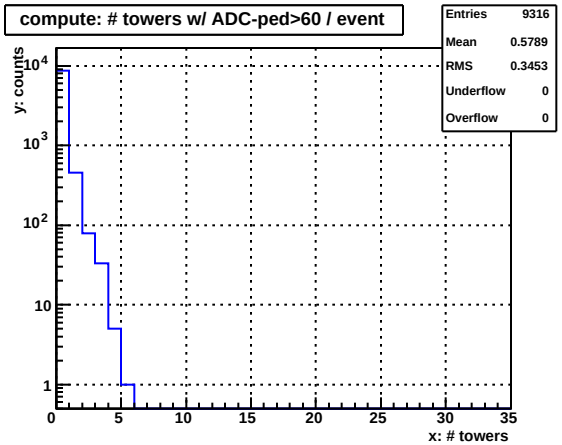

L2 COMPUTE time per input event
Entries 9316
Mean 0.939
RMS 1.536
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

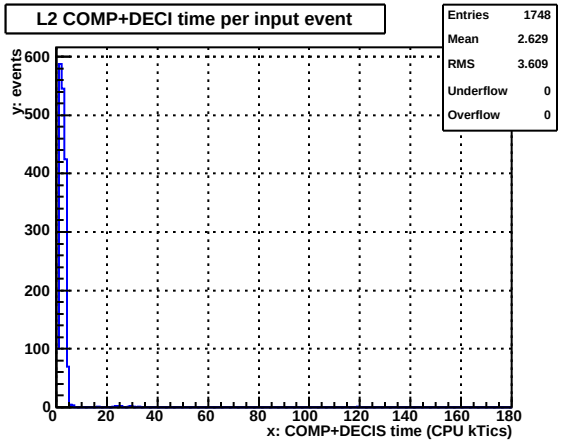

rate of COMPUTE
Entries 9316
Mean 38.66
RMS 7.522
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

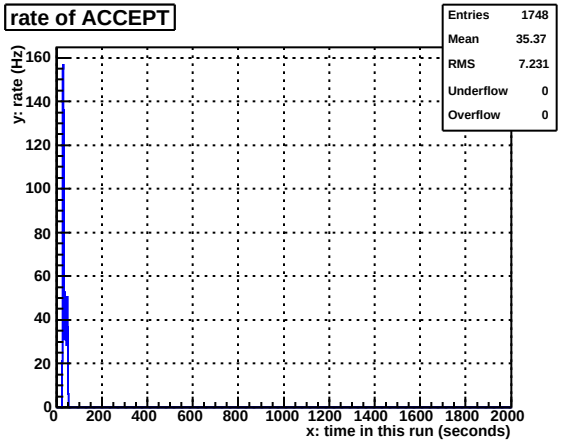

total events 0=anyInput, 10=anyAccept
Entries 22128
Mean 1.869
RMS 2.6
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

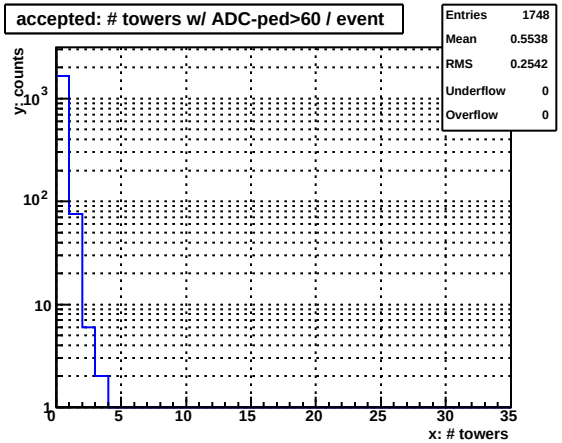

L2 DECISION time per input event
Entries 1748
Mean 1.431
RMS 1.639
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

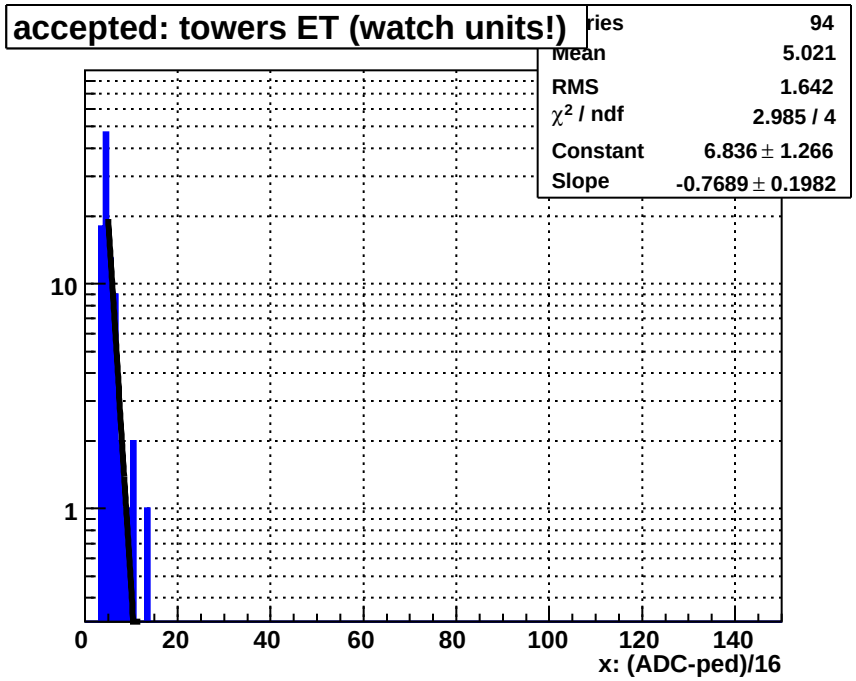

rate of DECISION
Entries 1748
Mean 35.37
RMS 7.231
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

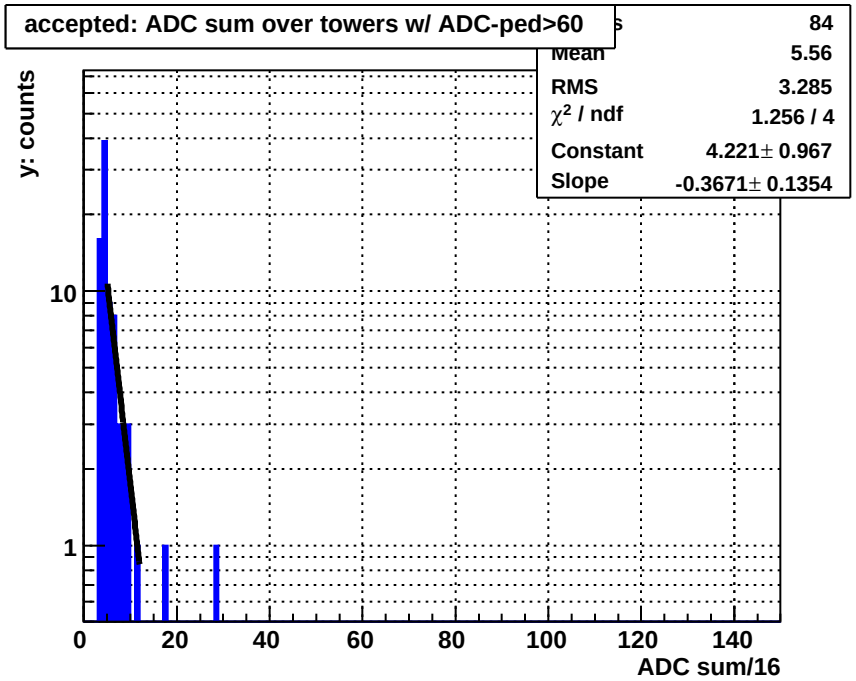

compute: # towers w/ ADC-ped>60 / event
Entries 9316
Mean 0.5789
RMS 0.3453
Underflow 0
Overflow 0
y: counts
x: # towers

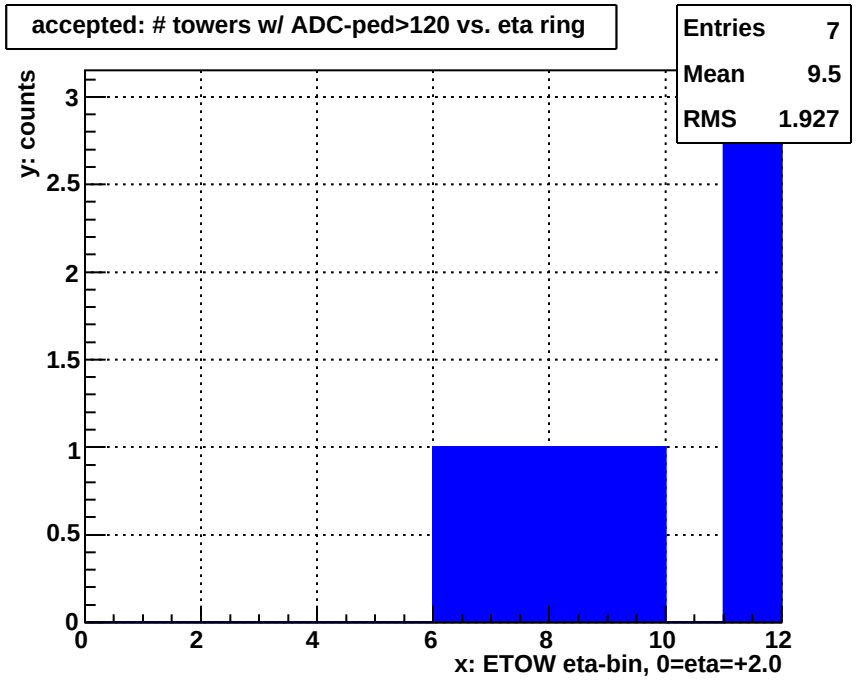

L2 COMP+DECI time per input event
Entries 1748
Mean 2.629
RMS 3.609
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

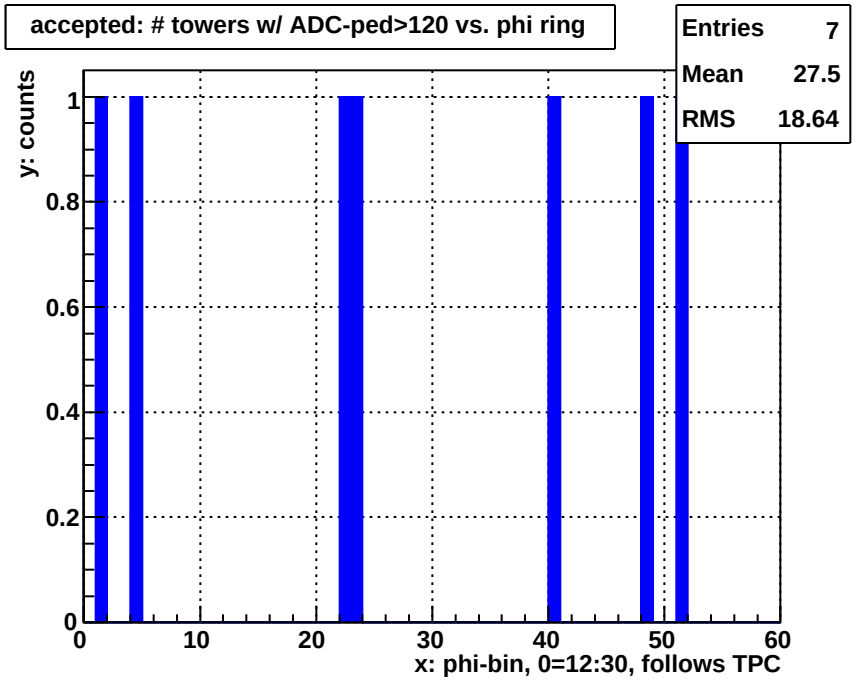

rate of ACCEPT
Entries 1748
Mean 35.37
RMS 7.231
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 1748
Mean 0.5538
RMS 0.2542
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 94
Mean 5.021
RMS 1.642
χ² / ndf 2.985 / 4
Constant 6.836 ± 1.266
Slope -0.7689 ± 0.1982
10
1
0 20 40 60 80 100 120 140
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 84
Mean 5.56
RMS 3.285
χ² / ndf 1.256 / 4
Constant 4.221 ± 0.967
Slope -0.3671 ± 0.1354
y: counts
10
1
0 20 40 60 80 100 120 140
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 7
Mean 9.5
RMS 1.927
y: counts
3
2.5
2
1.5
1
0.5
0
0 2 4 6 8 10 12
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 7
Mean 27.5
RMS 18.64
y: counts
1
0.8
0.6
0.4
0.2
0
0 10 20 30 40 50 60
x: phi-bin, 0=12:30, follows TPC