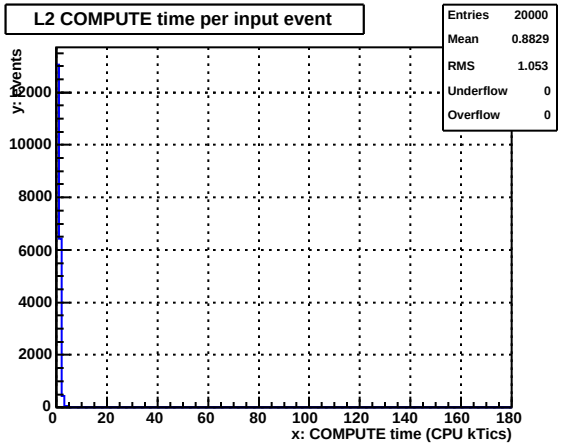
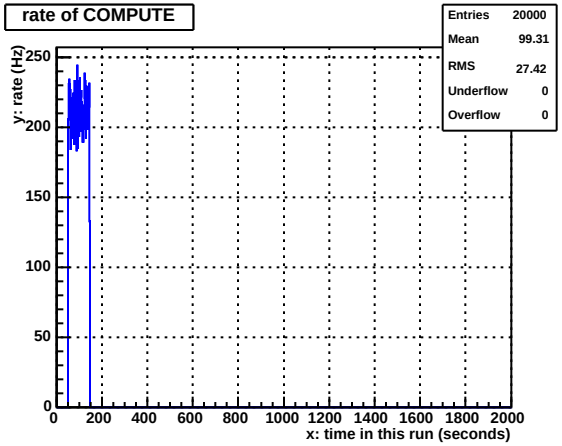
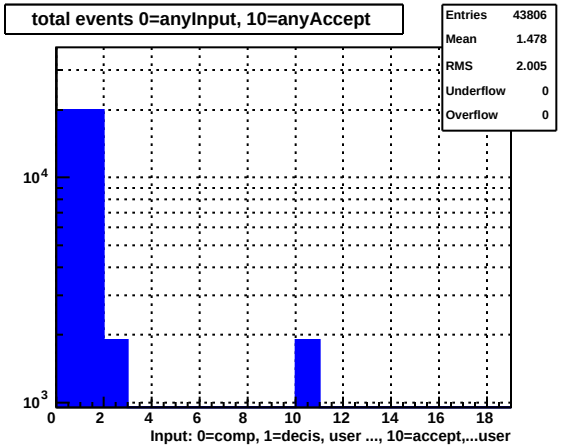
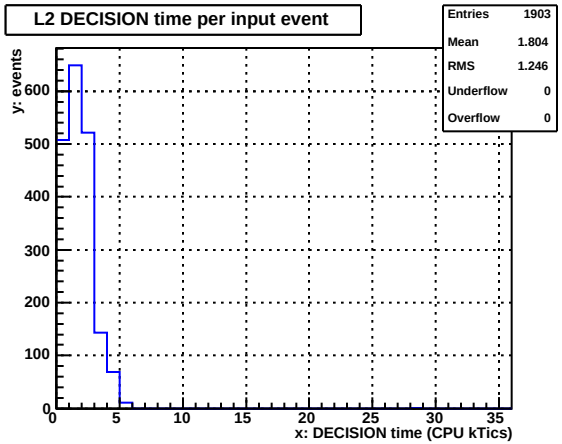
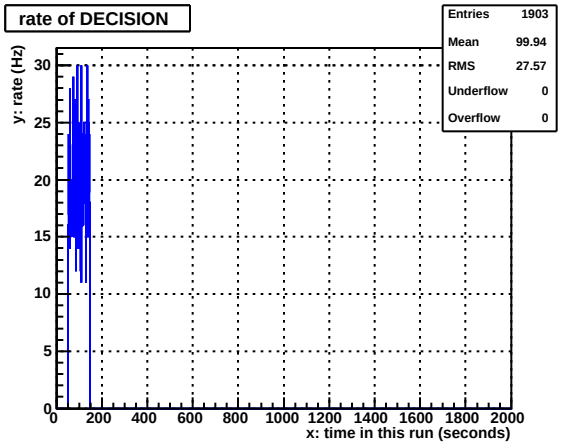
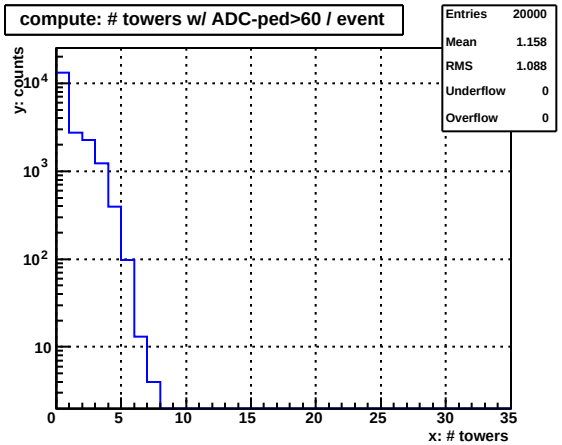

L2 COMPUTE time per input event
Entries 20000
Mean 0.8829
RMS 1.053
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

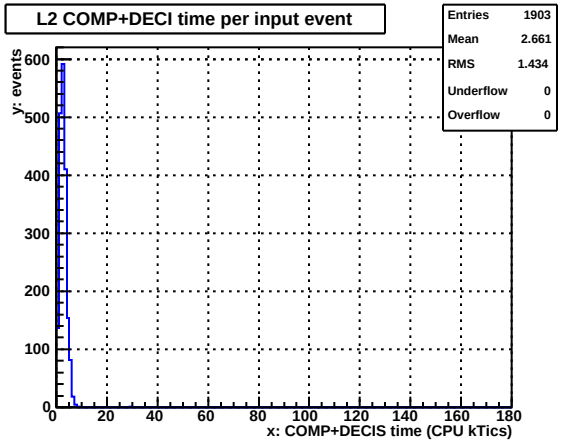

rate of COMPUTE
Entries 20000
Mean 99.31
RMS 27.42
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

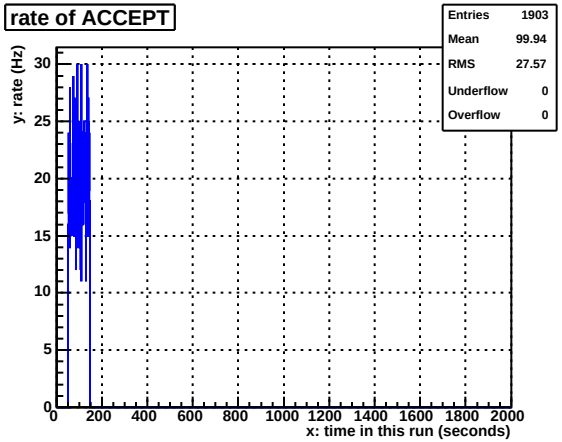

total events 0=anyInput, 10=anyAccept
Entries 43806
Mean 1.478
RMS 2.005
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

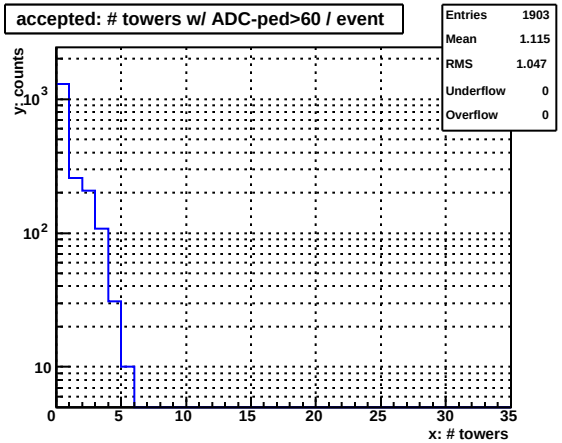

L2 DECISION time per input event
Entries 1903
Mean 1.804
RMS 1.246
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

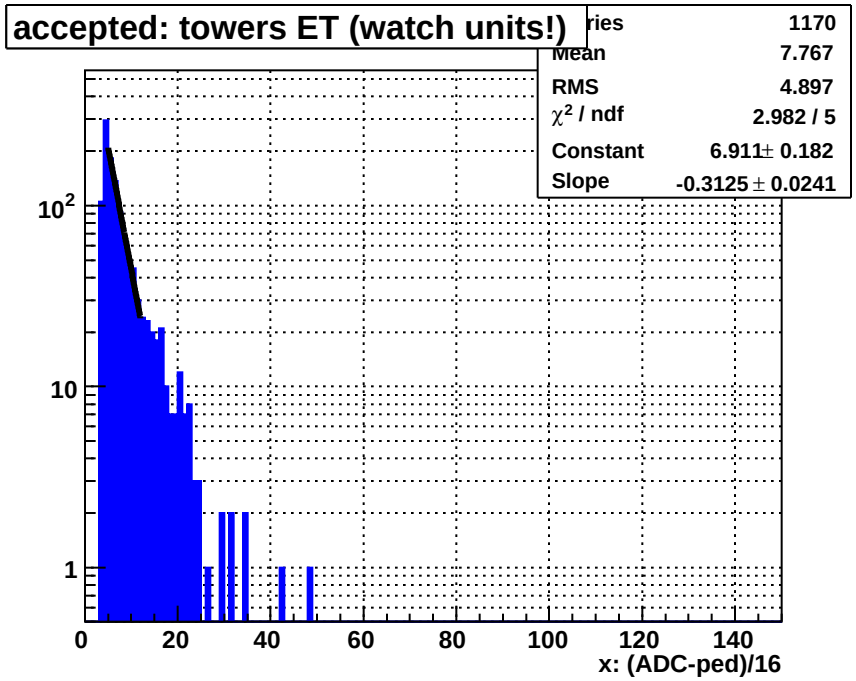

rate of DECISION
Entries 1903
Mean 99.94
RMS 27.57
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

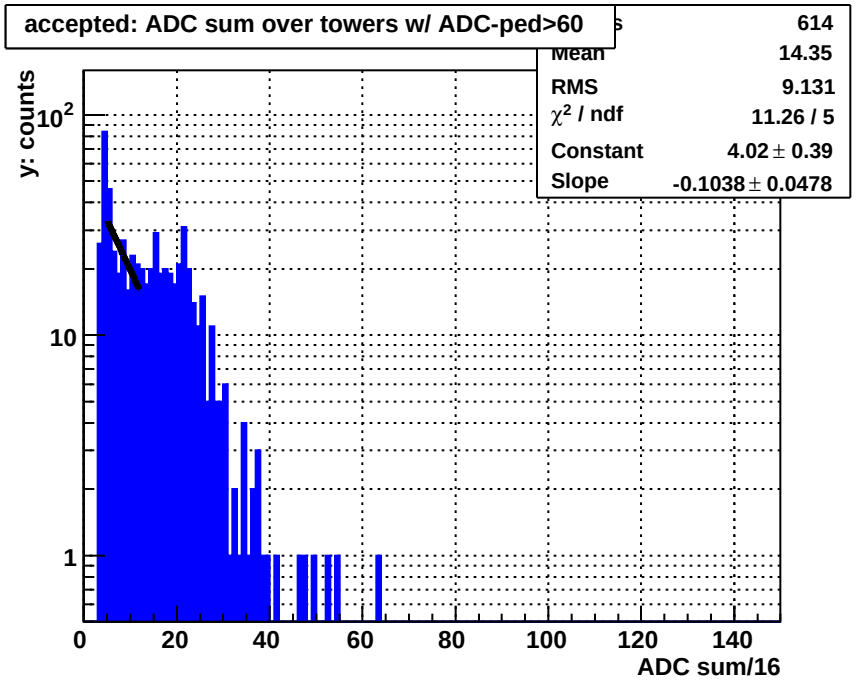

compute: # towers w/ ADC-ped>60 / event
Entries 20000
Mean 1.158
RMS 1.088
Underflow 0
Overflow 0
y: counts
x: # towers

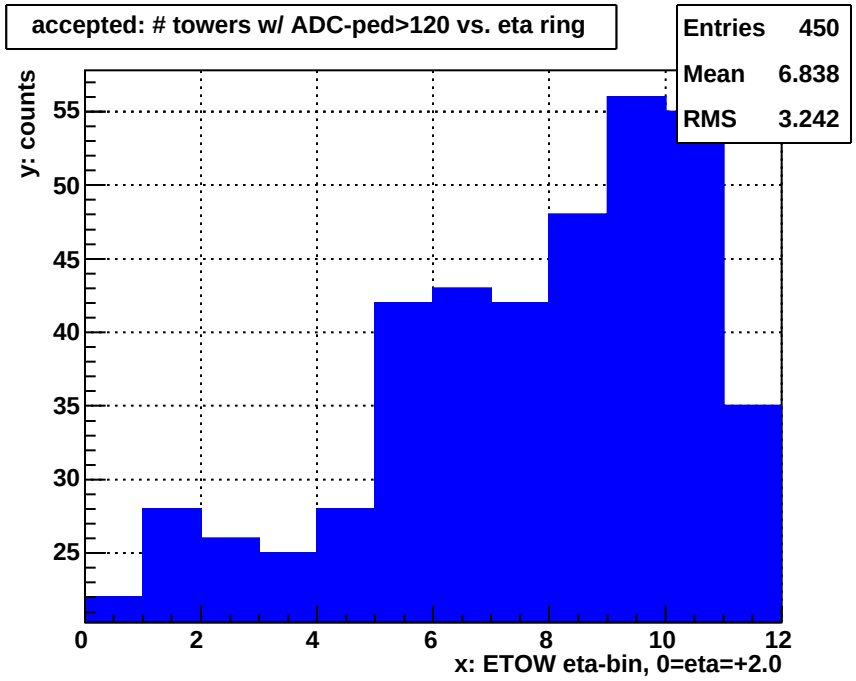

L2 COMP+DECI time per input event
Entries 1903
Mean 2.661
RMS 1.434
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

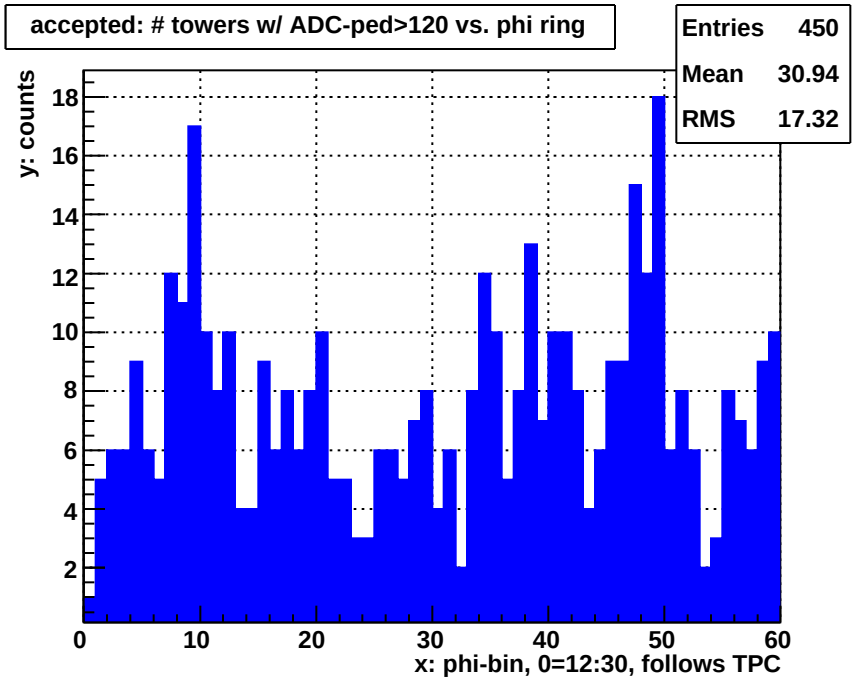

rate of ACCEPT
Entries 1903
Mean 99.94
RMS 27.57
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 1903
Mean 1.115
RMS 1.047
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 1170
Mean 7.767
RMS 4.897
χ² / ndf 2.982 / 5
Constant 6.911 ± 0.182
Slope -0.3125 ± 0.0241
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 614
Mean 14.35
RMS 9.131
χ² / ndf 11.26 / 5
Constant 4.02 ± 0.39
Slope -0.1038 ± 0.0478
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 450
Mean 6.838
RMS 3.242
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 450
Mean 30.94
RMS 17.32
y: counts
x: phi-bin, 0=12:30, follows TPC