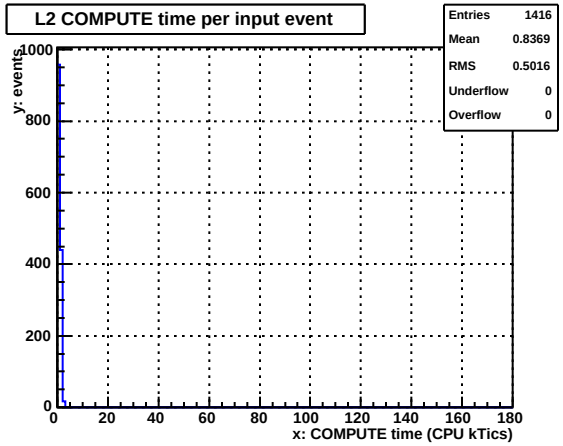
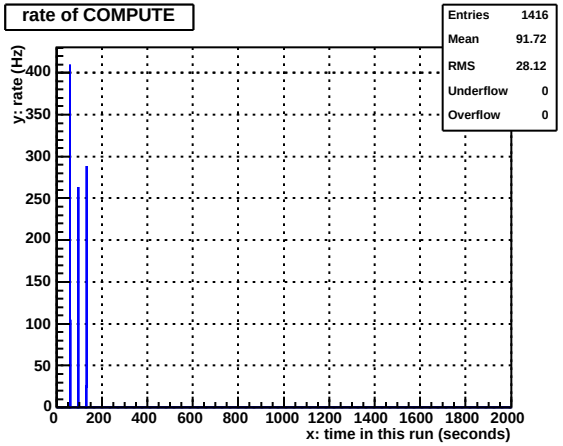
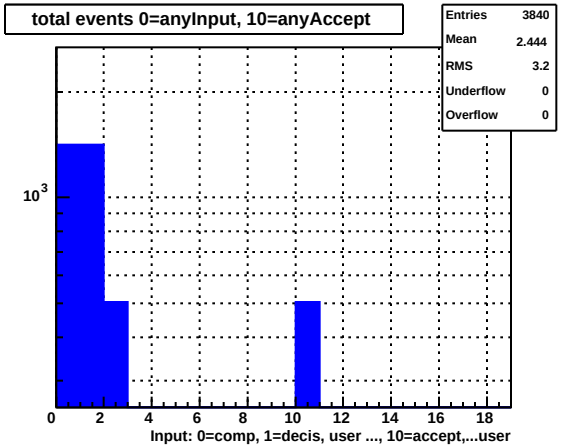
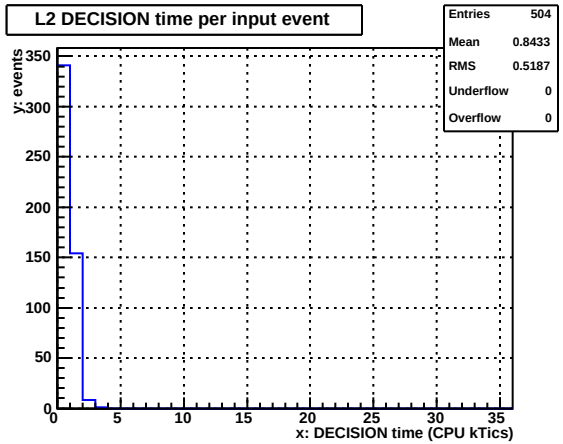
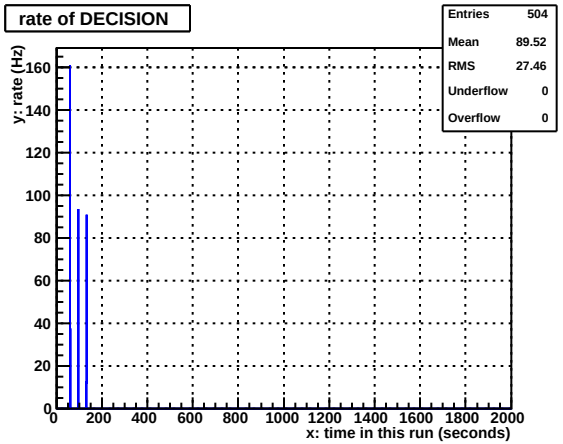
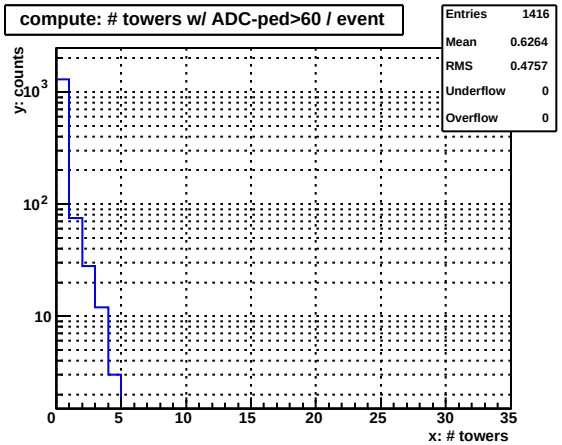

L2 COMPUTE time per input event
Entries 1416
Mean 0.8369
RMS 0.5016
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

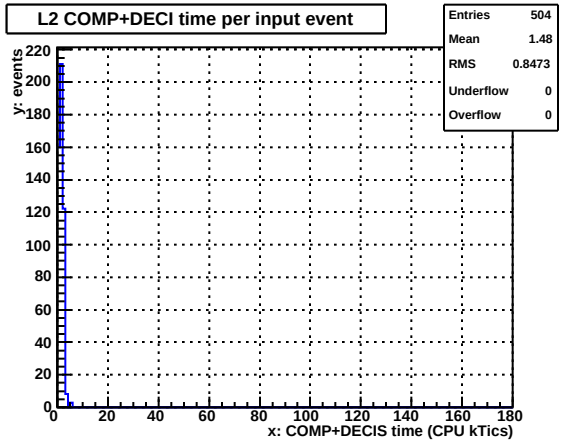

rate of COMPUTE
Entries 1416
Mean 91.72
RMS 28.12
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

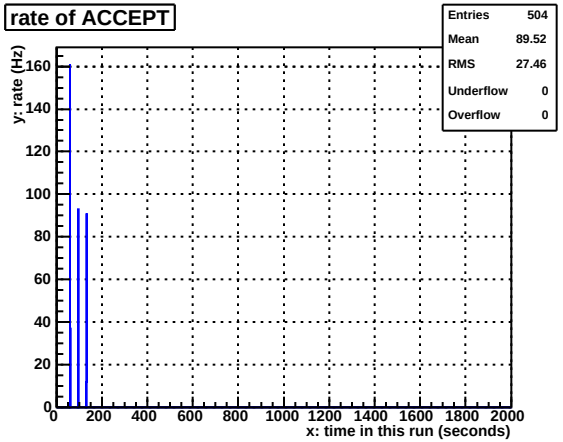

total events 0=anyInput, 10=anyAccept
Entries 3840
Mean 2.444
RMS 3.2
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

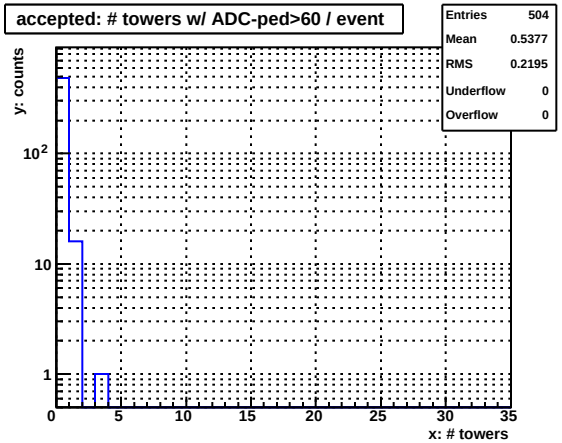

L2 DECISION time per input event
Entries 504
Mean 0.8433
RMS 0.5187
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

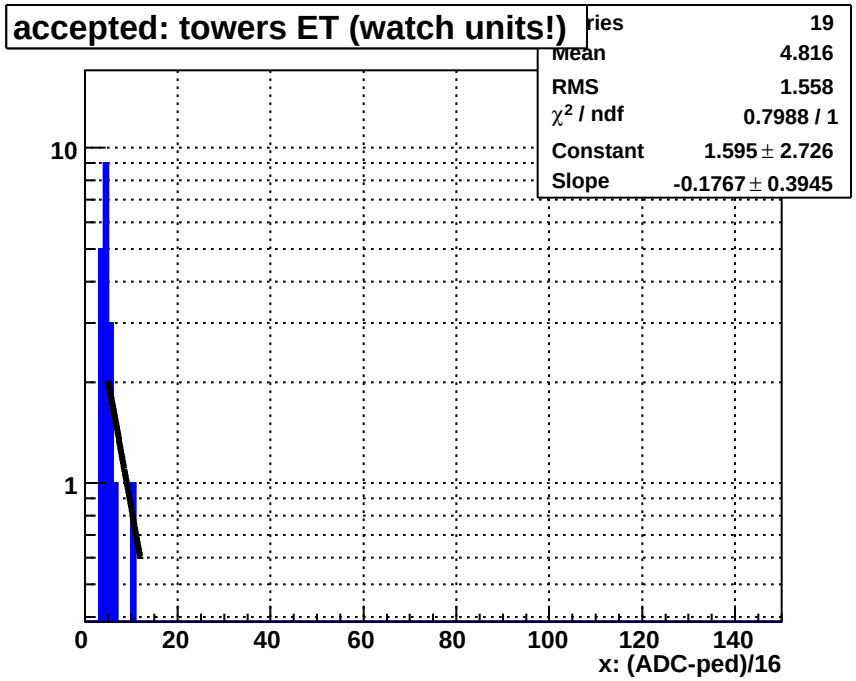

rate of DECISION
Entries 504
Mean 89.52
RMS 27.46
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

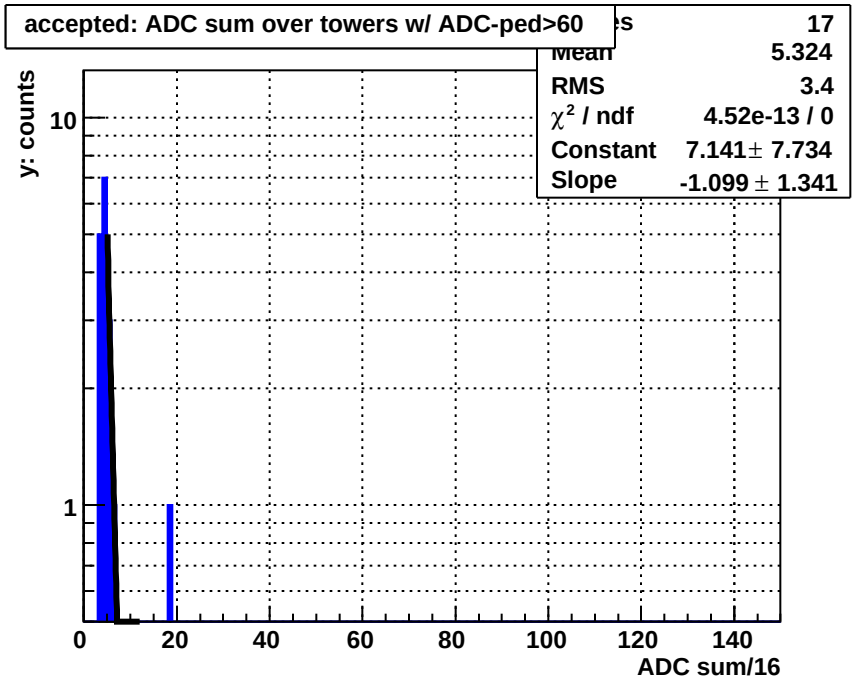

compute: # towers w/ ADC-ped>60 / event
Entries 1416
Mean 0.6264
RMS 0.4757
Underflow 0
Overflow 0
y: counts
x: # towers

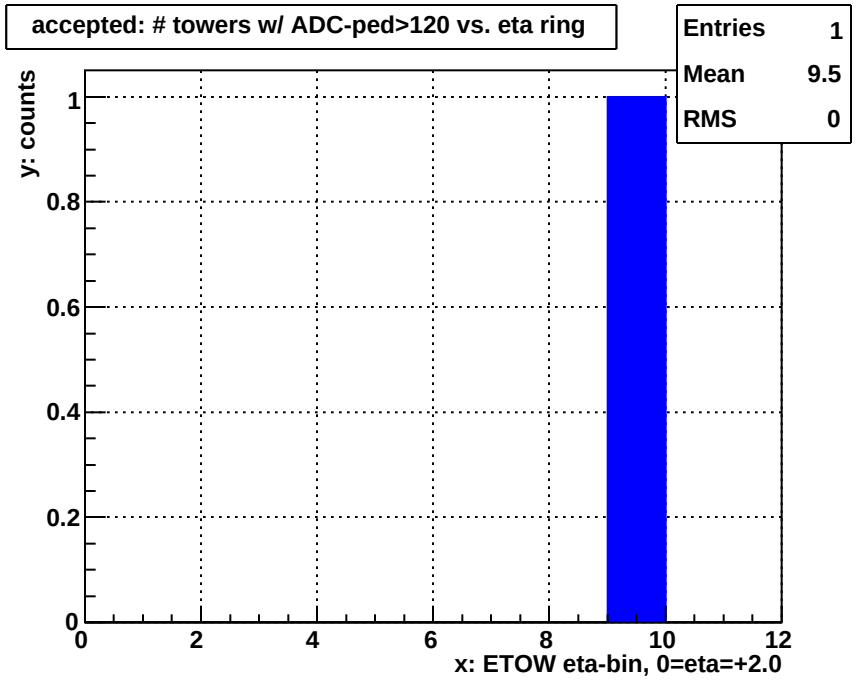

L2 COMP+DECI time per input event
Entries 504
Mean 1.48
RMS 0.8473
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

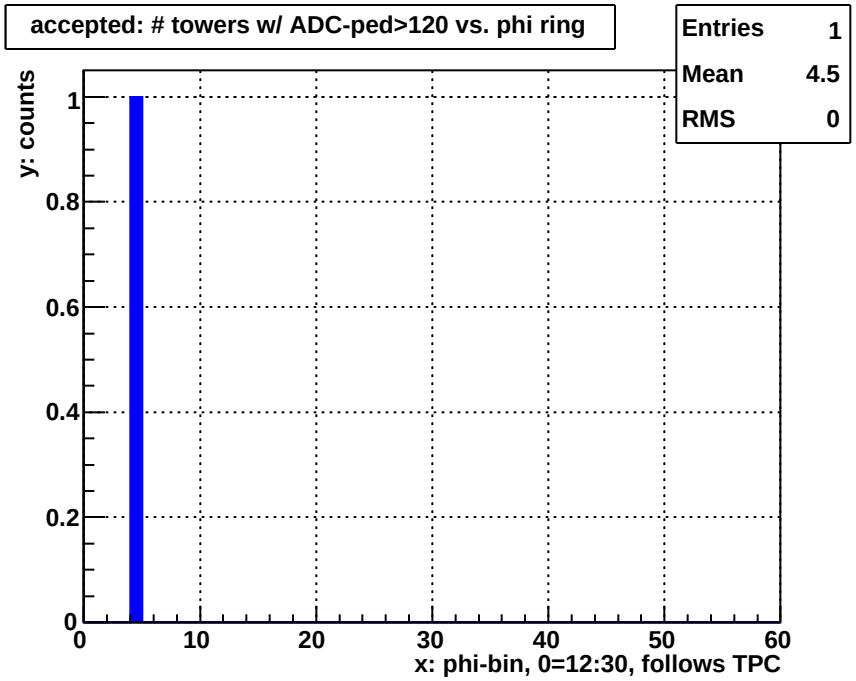

rate of ACCEPT
Entries 504
Mean 89.52
RMS 27.46
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 504
Mean 0.5377
RMS 0.2195
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 19
Mean 4.816
RMS 1.558
χ² / ndf 0.7988 / 1
Constant 1.595 ± 2.726
Slope -0.1767 ± 0.3945
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
es 17
Mean 5.324
RMS 3.4
χ² / ndf 4.52e-13 / 0
Constant 7.141 ± 7.734
Slope -1.099 ± 1.341
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 1
Mean 9.5
RMS 0
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 1
Mean 4.5
RMS 0
y: counts
x: phi-bin, 0=12:30, follows TPC