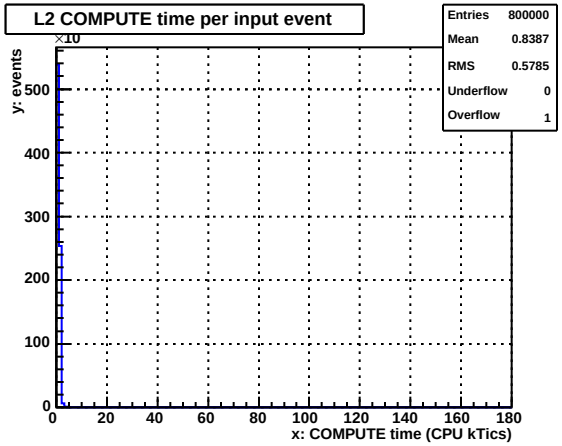
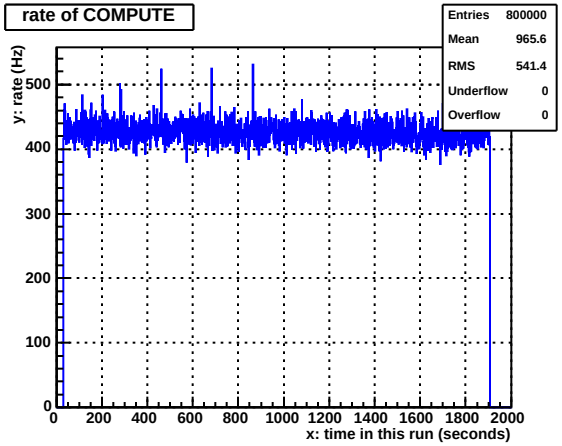
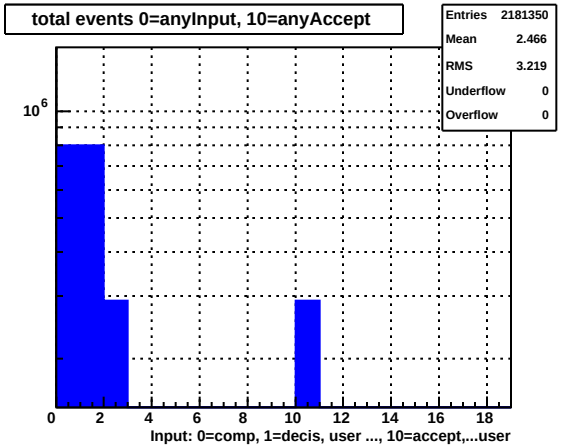
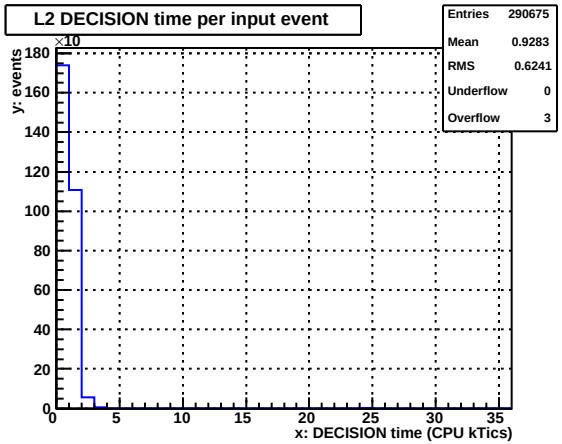
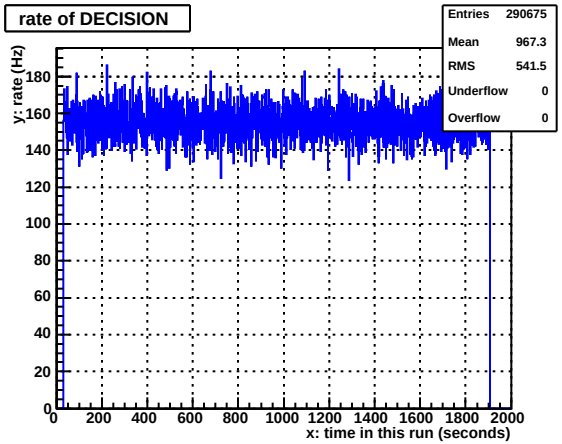
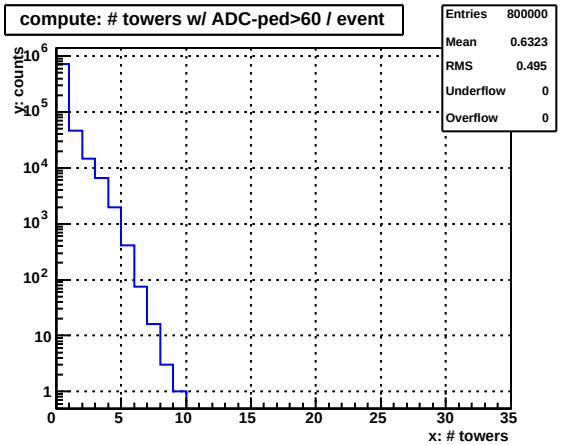

L2 COMPUTE time per input event
Entries 800000
Mean 0.8387
RMS 0.5785
Underflow 0
Overflow 1
y: events
x: COMPUTE time (CPU kTics)

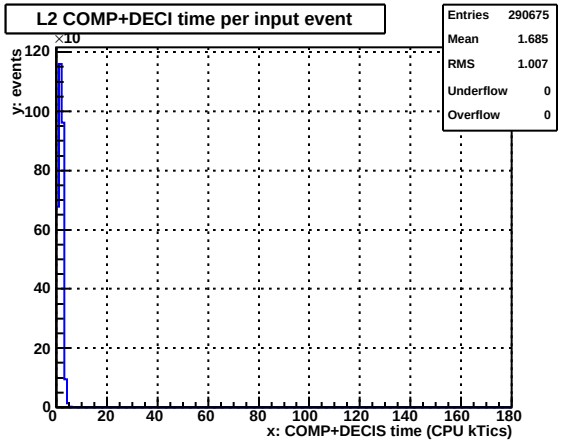

rate of COMPUTE
Entries 800000
Mean 965.6
RMS 541.4
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

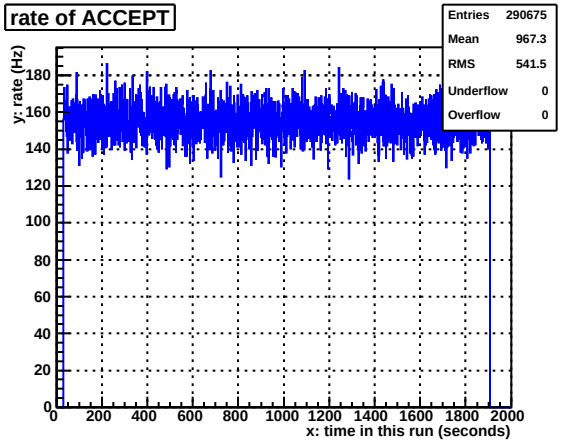

total events 0=anyInput, 10=anyAccept
Entries 2181350
Mean 2.466
RMS 3.219
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

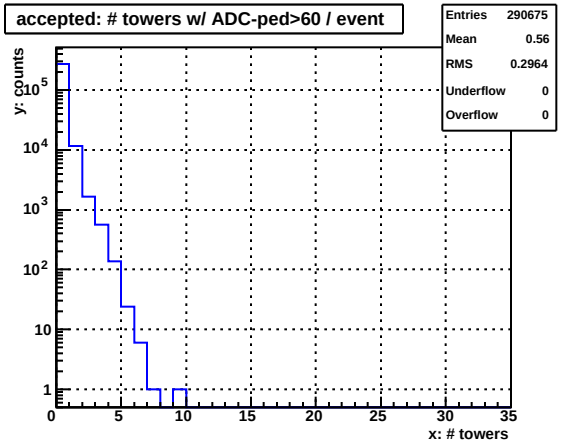

L2 DECISION time per input event
Entries 290675
Mean 0.9283
RMS 0.6241
Underflow 0
Overflow 3
y: events
x: DECISION time (CPU kTics)

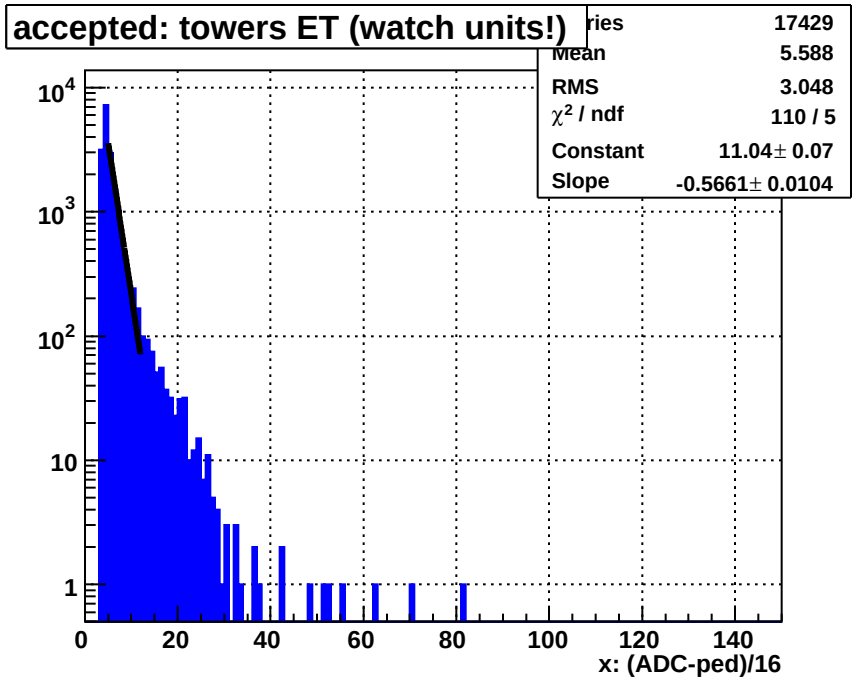

rate of DECISION
Entries 290675
Mean 967.3
RMS 541.5
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

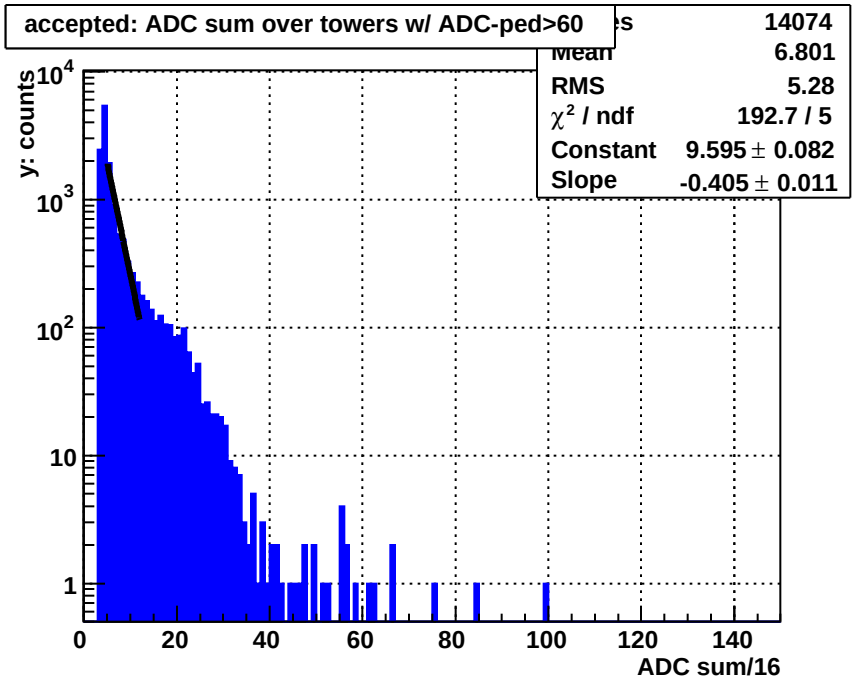

compute: # towers w/ ADC-ped>60 / event
Entries 800000
Mean 0.6323
RMS 0.495
Underflow 0
Overflow 0
y: counts
x: # towers

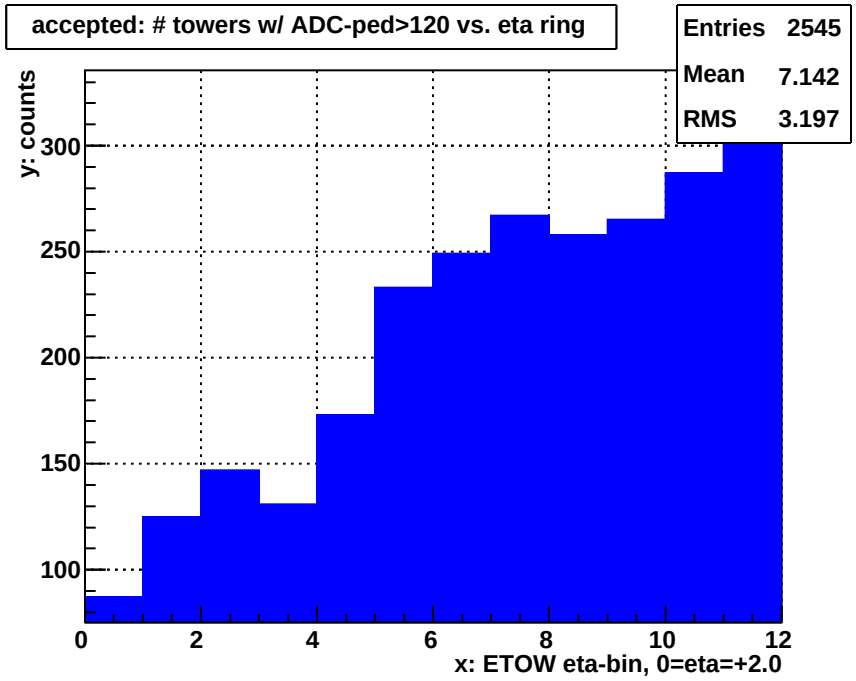

L2 COMP+DECI time per input event
Entries 290675
Mean 1.685
RMS 1.007
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

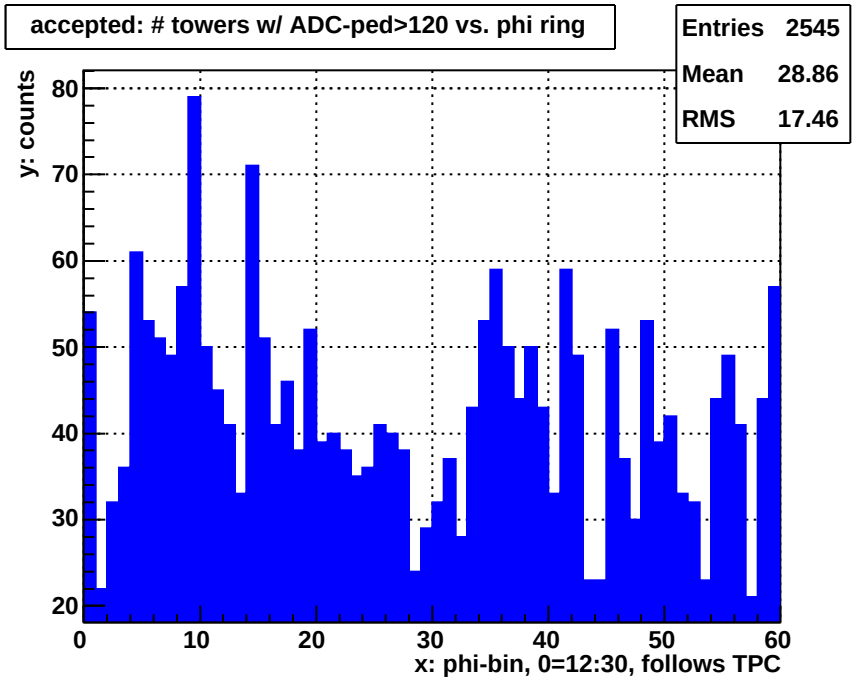

rate of ACCEPT
Entries 290675
Mean 967.3
RMS 541.5
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 290675
Mean 0.56
RMS 0.2964
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 17429
Mean 5.588
RMS 3.048
χ² / ndf 110 / 5
Constant 11.04 ± 0.07
Slope -0.5661 ± 0.0104
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 14074
Mean 6.801
RMS 5.28
χ² / ndf 192.7 / 5
Constant 9.595 ± 0.082
Slope -0.405 ± 0.011
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 2545
Mean 7.142
RMS 3.197
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 2545
Mean 28.86
RMS 17.46
y: counts
x: phi-bin, 0=12:30, follows TPC