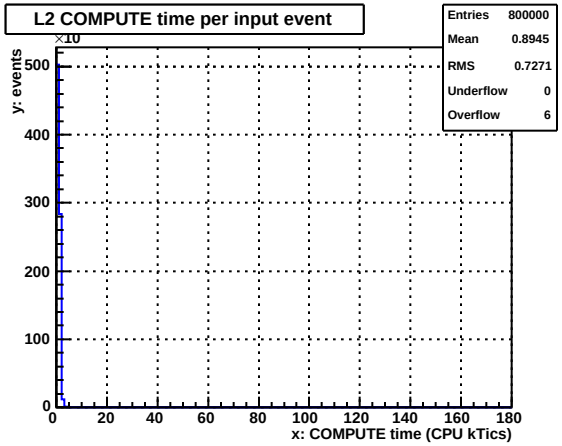
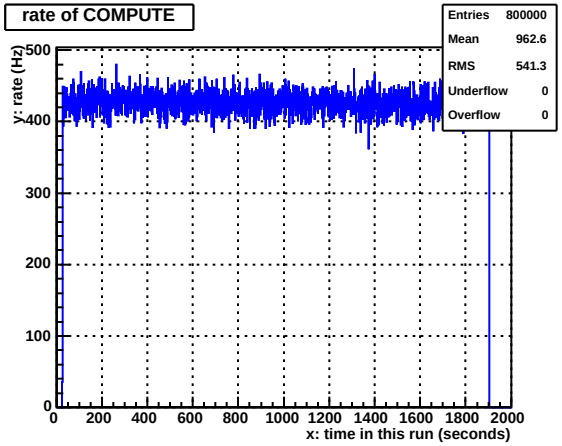
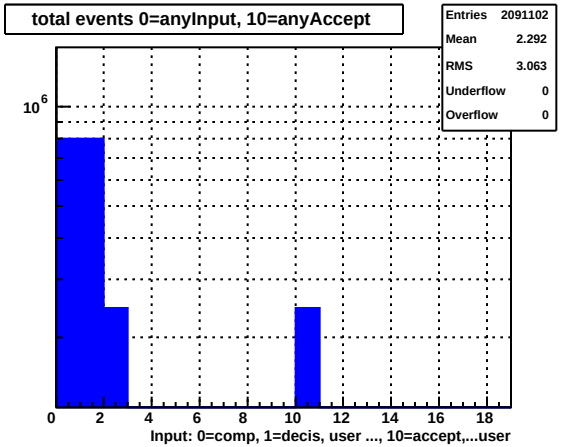
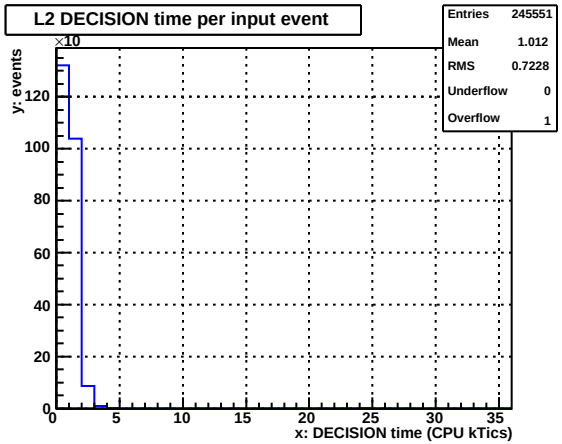
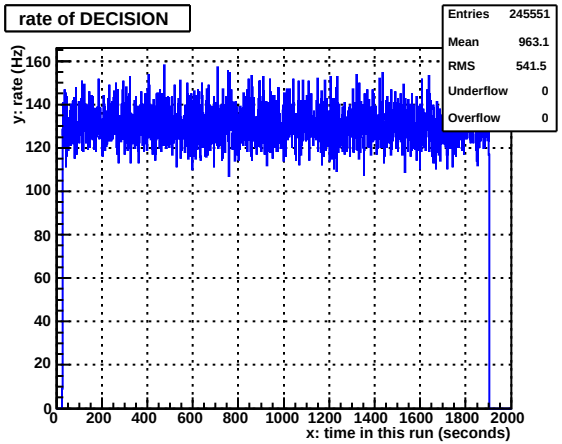
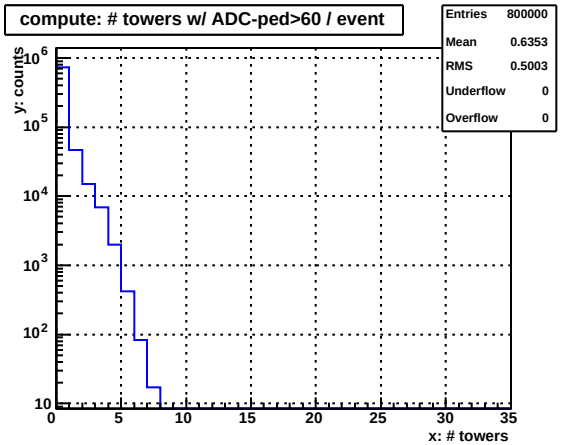

L2 COMPUTE time per input event
Entries 800000
Mean 0.8945
RMS 0.7271
Underflow 0
Overflow 6
y: events
x: COMPUTE time (CPU kTics)

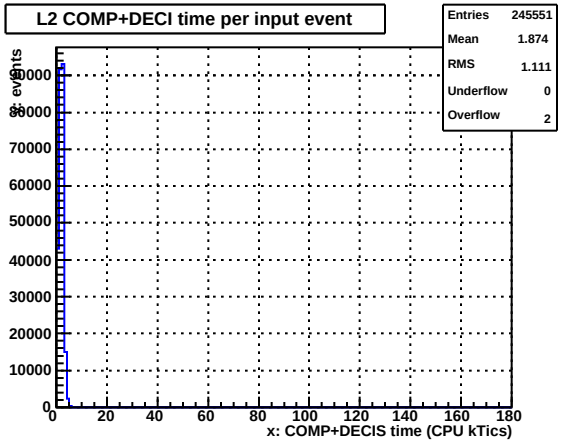

rate of COMPUTE
Entries 800000
Mean 962.6
RMS 541.3
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

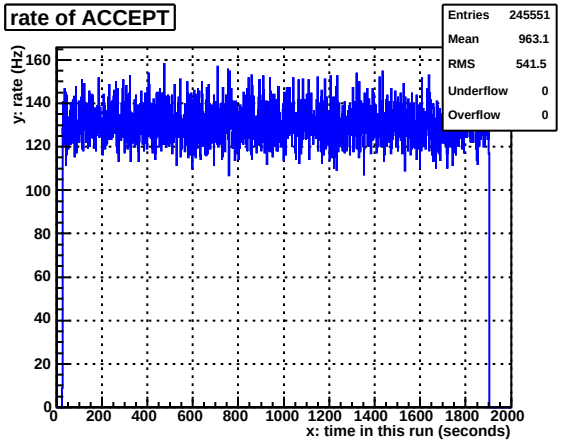

total events 0=anyInput, 10=anyAccept
Entries 2091102
Mean 2.292
RMS 3.063
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

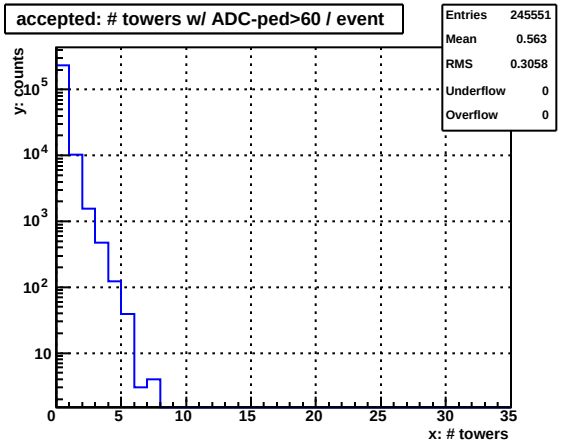

L2 DECISION time per input event
Entries 245551
Mean 1.012
RMS 0.7228
Underflow 0
Overflow 1
y: events
x: DECISION time (CPU kTics)

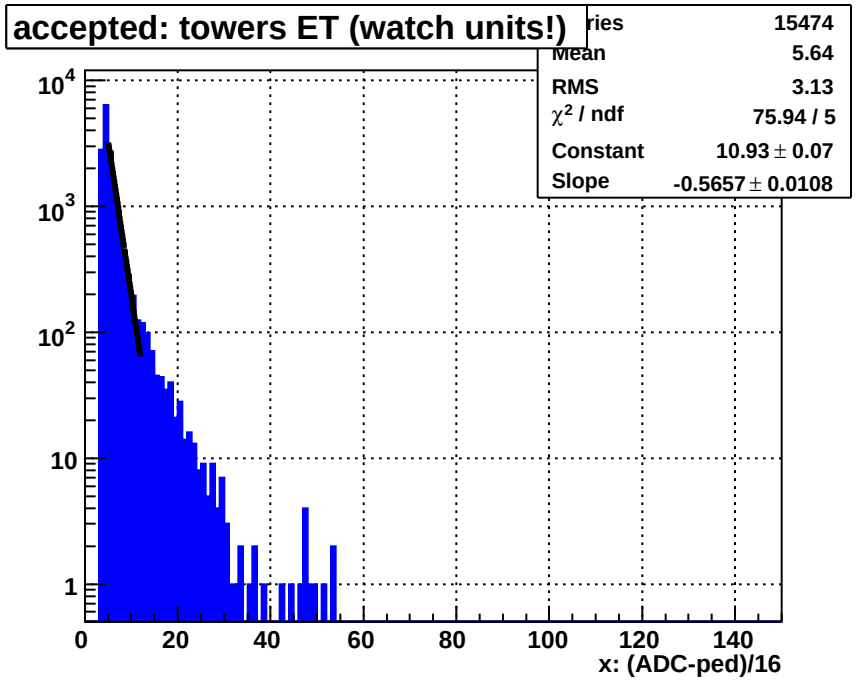

rate of DECISION
Entries 245551
Mean 963.1
RMS 541.5
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

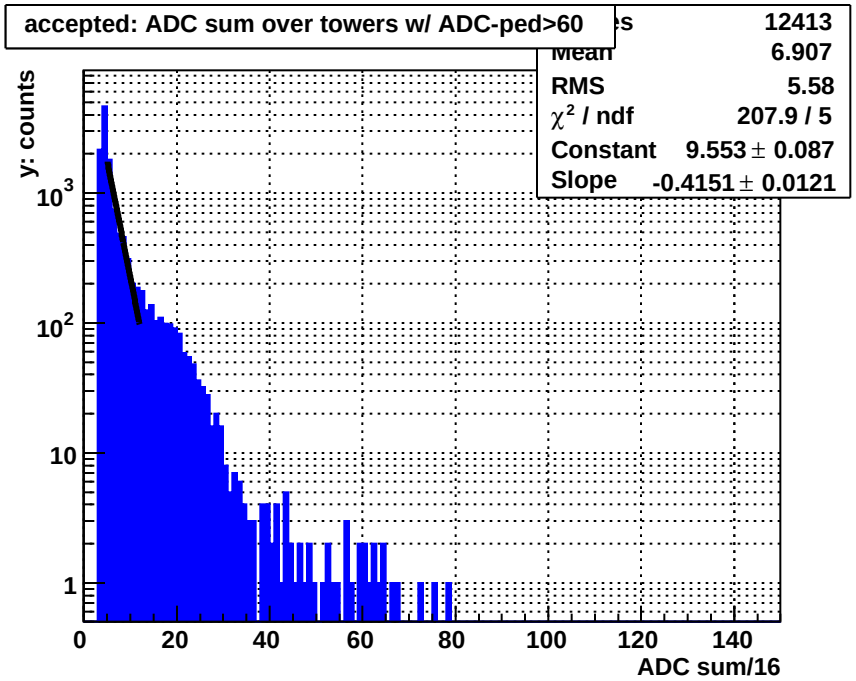

compute: # towers w/ ADC-ped>60 / event
Entries 800000
Mean 0.6353
RMS 0.5003
Underflow 0
Overflow 0
y: counts
x: # towers

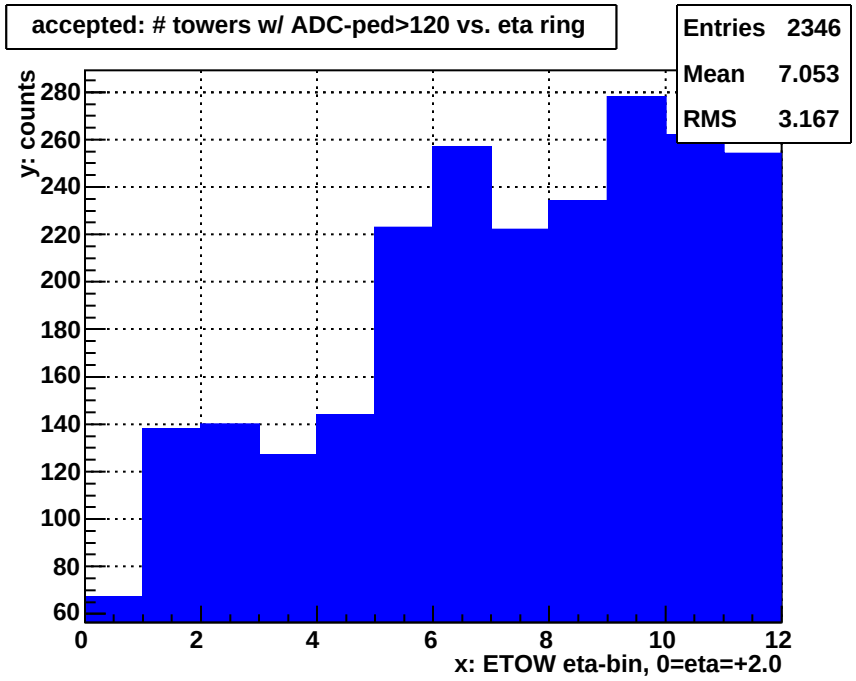

L2 COMP+DECI time per input event
Entries 245551
Mean 1.874
RMS 1.111
Underflow 0
Overflow 2
y: events
x: COMP+DECIS time (CPU kTics)

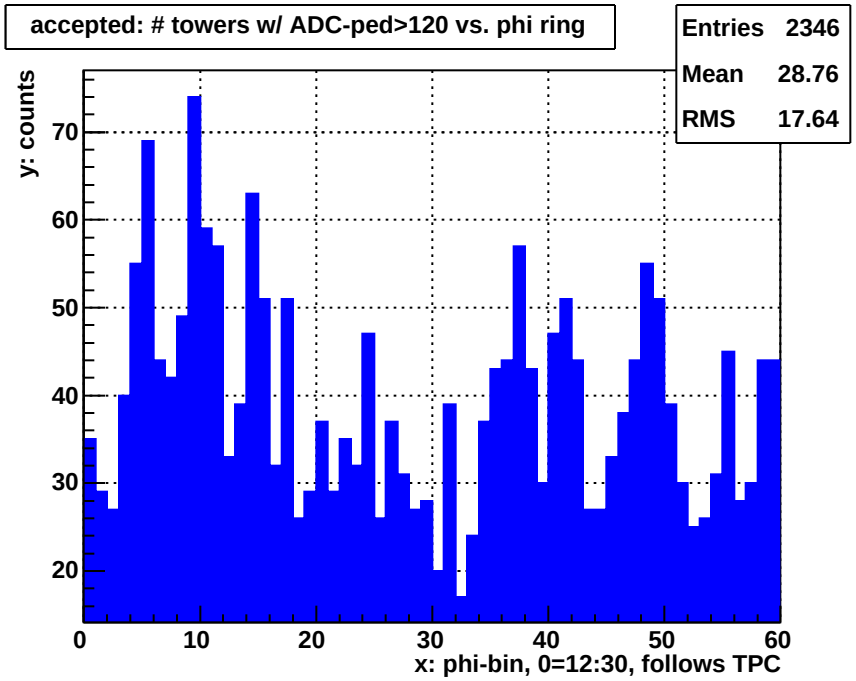

rate of ACCEPT
Entries 245551
Mean 963.1
RMS 541.5
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 245551
Mean 0.563
RMS 0.3058
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 15474
Mean 5.64
RMS 3.13
χ² / ndf 75.94 / 5
Constant 10.93 ± 0.07
Slope -0.5657 ± 0.0108
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
es 12413
Mean 6.907
RMS 5.58
χ² / ndf 207.9 / 5
Constant 9.553 ± 0.087
Slope -0.4151 ± 0.0121
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 2346
Mean 7.053
RMS 3.167
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 2346
Mean 28.76
RMS 17.64
y: counts
x: phi-bin, 0=12:30, follows TPC