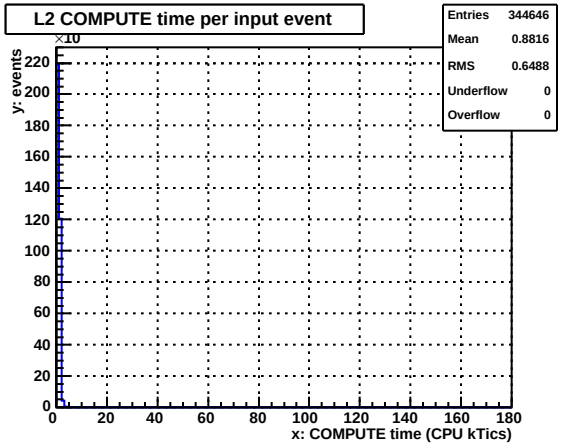
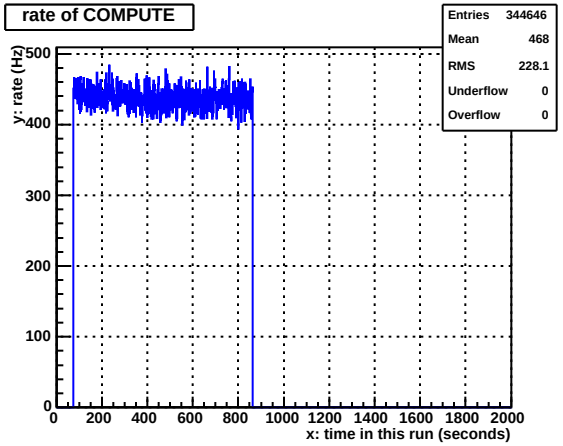
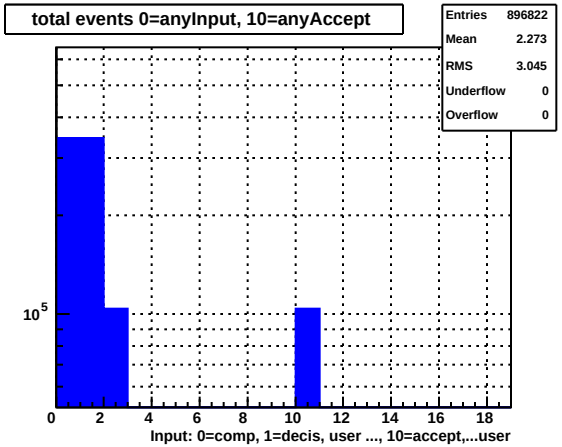
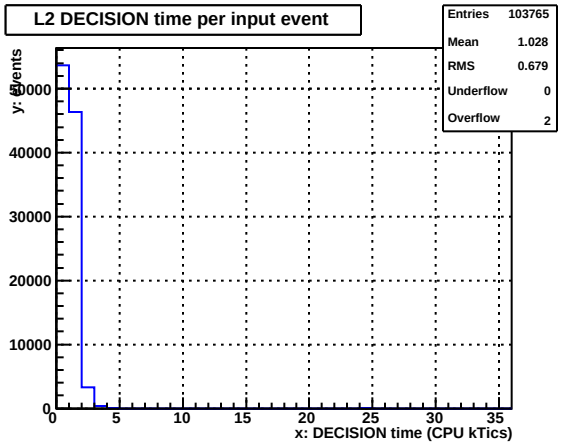
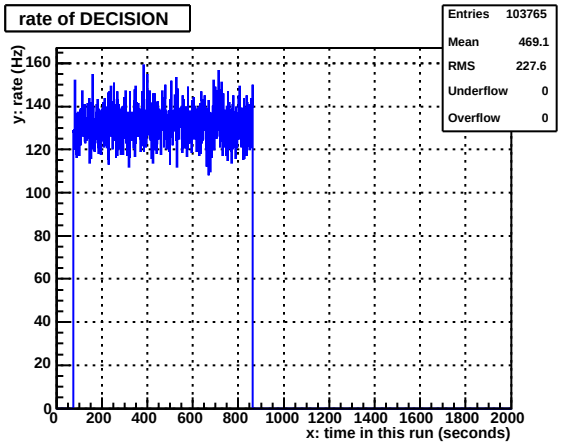
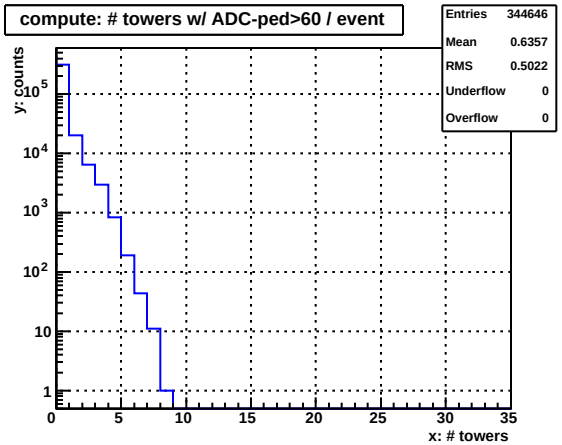

L2 COMPUTE time per input event
Entries 344646
Mean 0.8816
RMS 0.6488
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

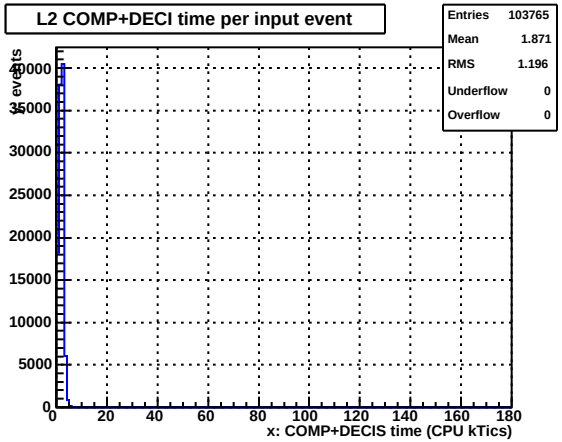

rate of COMPUTE
Entries 344646
Mean 468
RMS 228.1
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

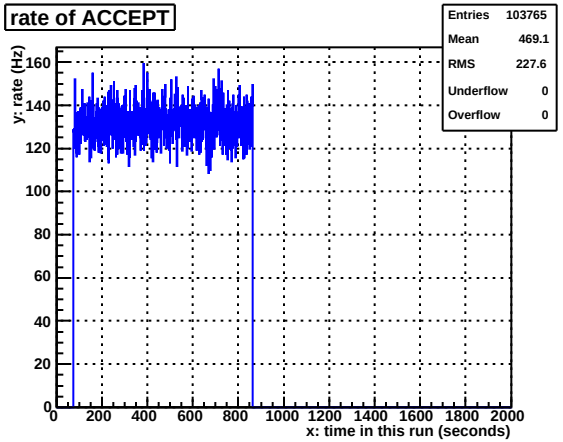

total events 0=anyInput, 10=anyAccept
Entries 896822
Mean 2.273
RMS 3.045
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

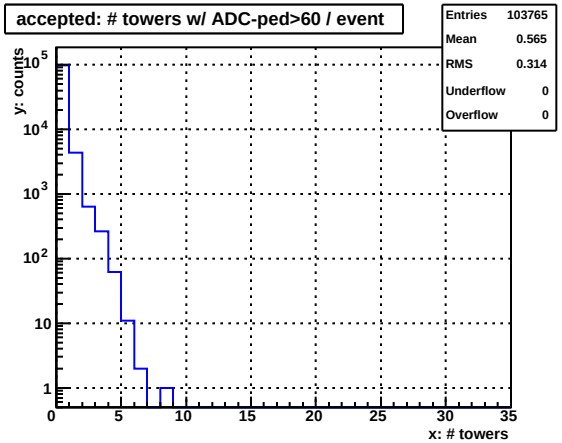

L2 DECISION time per input event
Entries 103765
Mean 1.028
RMS 0.679
Underflow 0
Overflow 2
y: events
x: DECISION time (CPU kTics)

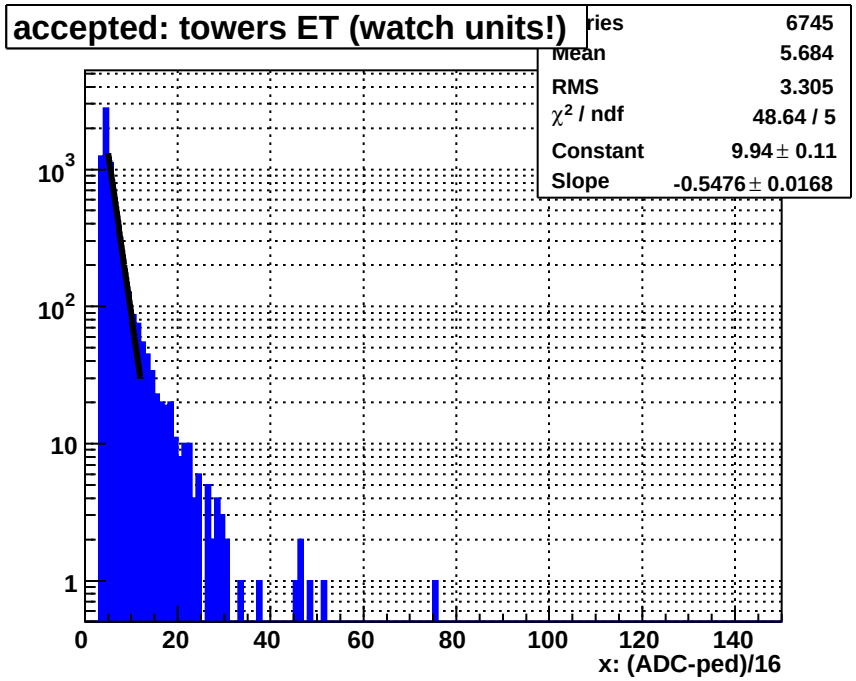

rate of DECISION
Entries 103765
Mean 469.1
RMS 227.6
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

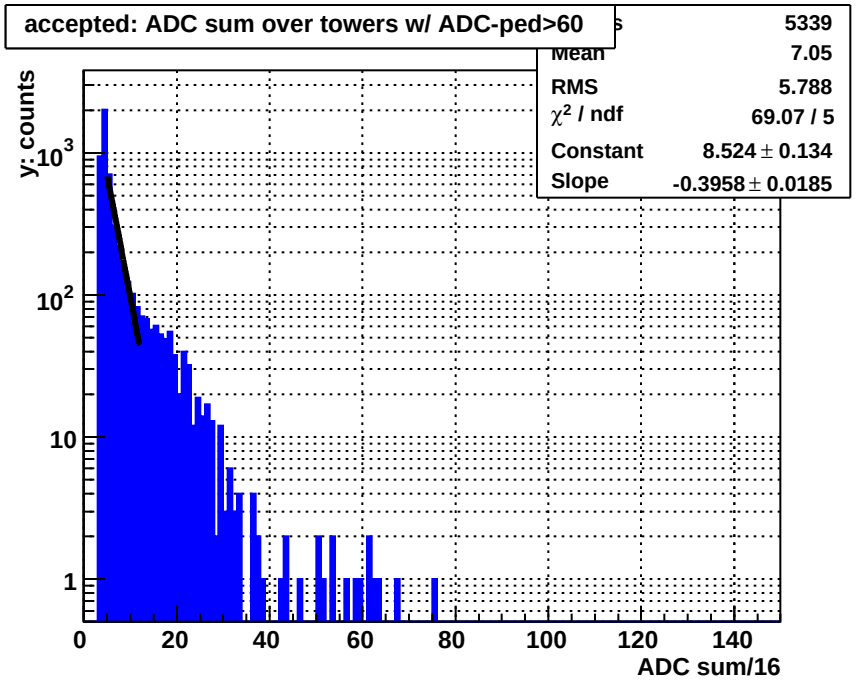

compute: # towers w/ ADC-ped>60 / event
Entries 344646
Mean 0.6357
RMS 0.5022
Underflow 0
Overflow 0
y: counts
x: # towers

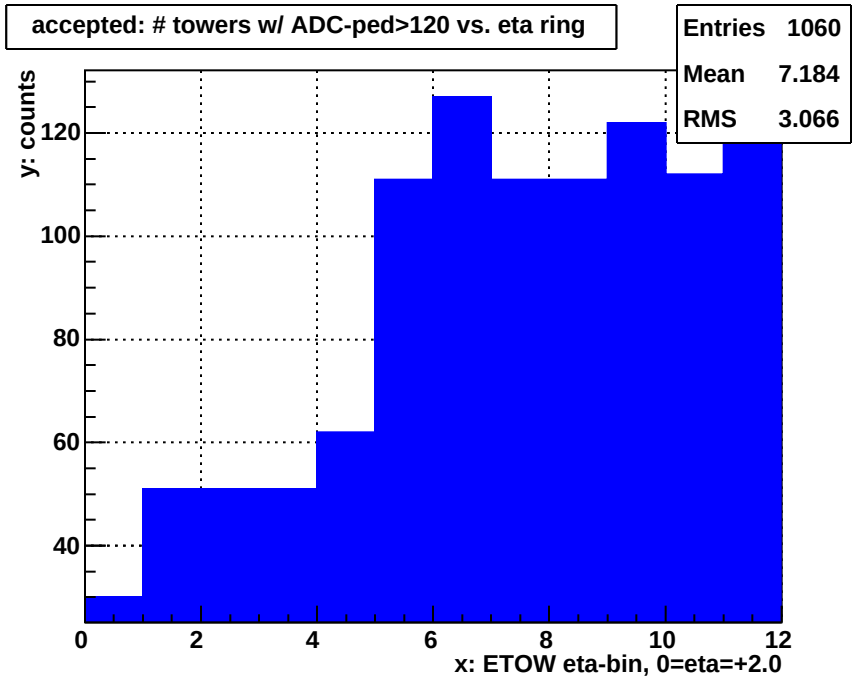

L2 COMP+DECI time per input event
Entries 103765
Mean 1.871
RMS 1.196
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

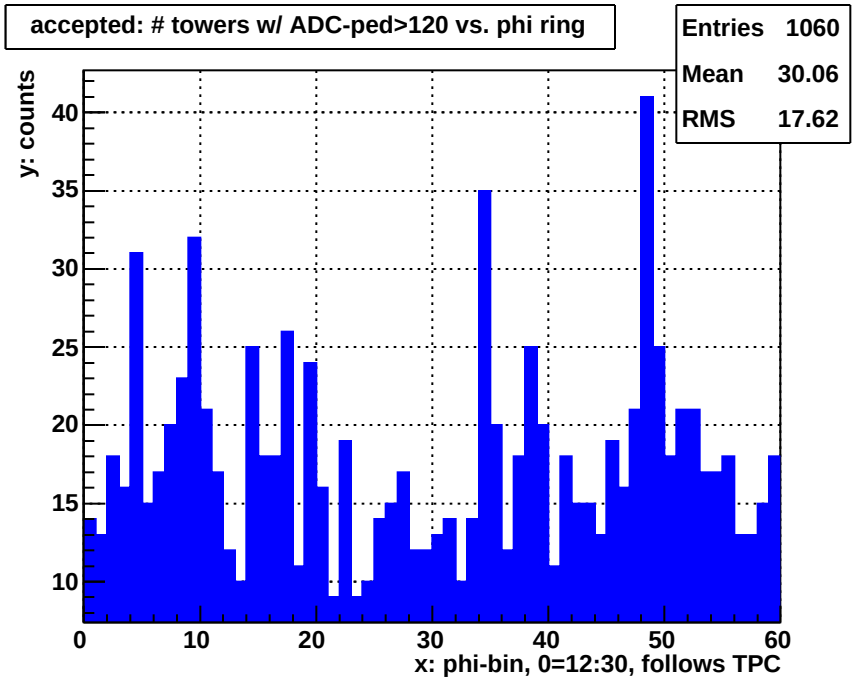

rate of ACCEPT
Entries 103765
Mean 469.1
RMS 227.6
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 103765
Mean 0.565
RMS 0.314
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 6745
Mean 5.684
RMS 3.305
χ² / ndf 48.64 / 5
Constant 9.94 ± 0.11
Slope -0.5476 ± 0.0168
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 5339
Mean 7.05
RMS 5.788
χ² / ndf 69.07 / 5
Constant 8.524 ± 0.134
Slope -0.3958 ± 0.0185
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 1060
Mean 7.184
RMS 3.066
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 1060
Mean 30.06
RMS 17.62
y: counts
x: phi-bin, 0=12:30, follows TPC