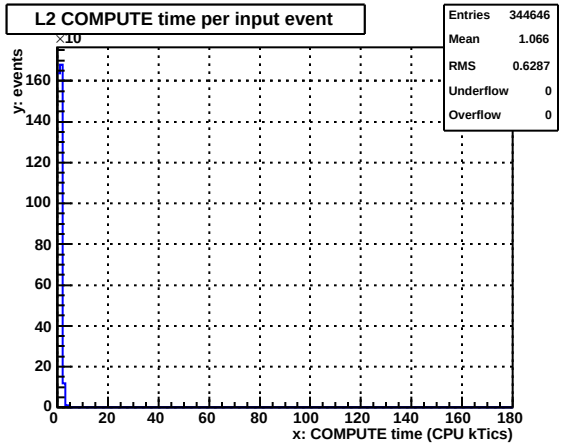
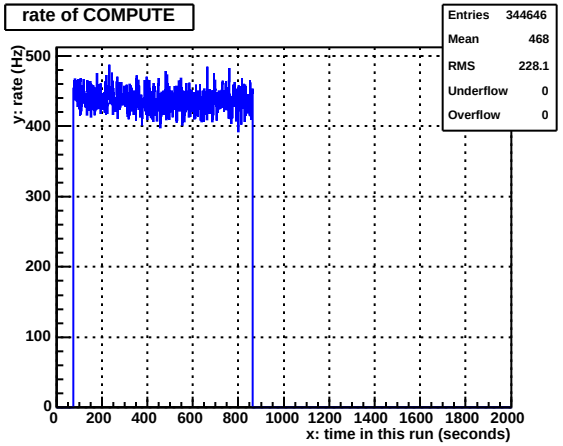
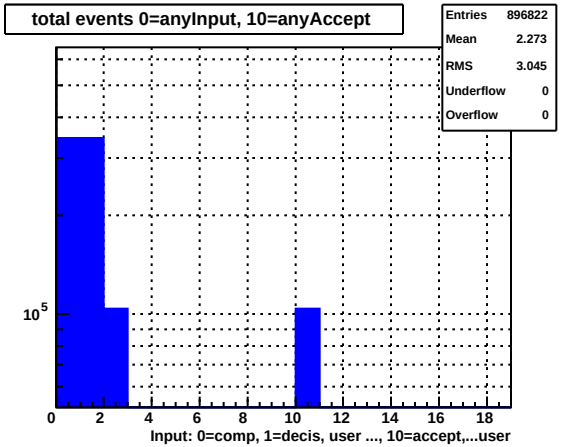
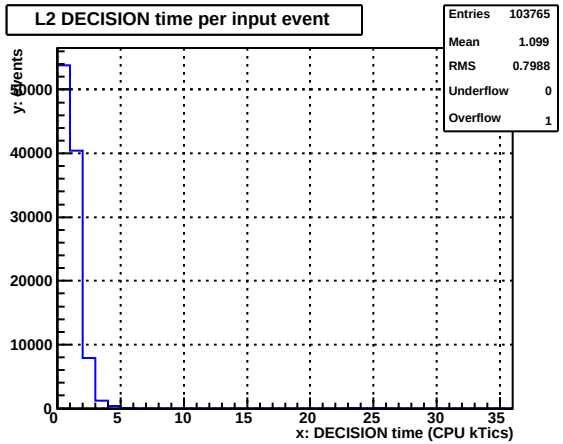
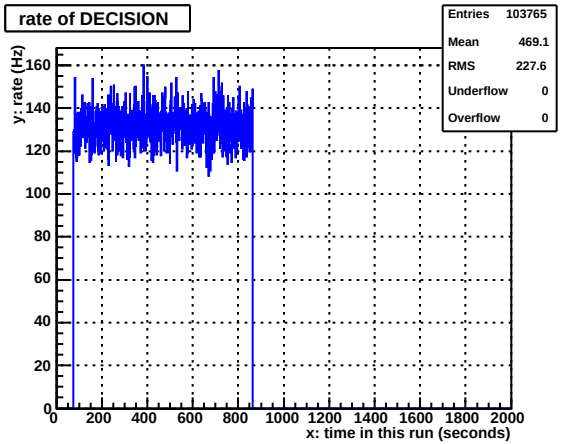
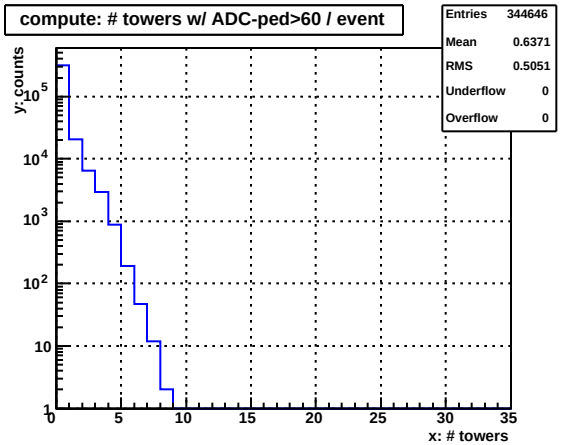

L2 COMPUTE time per input event
Entries 344646
Mean 1.066
RMS 0.6287
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

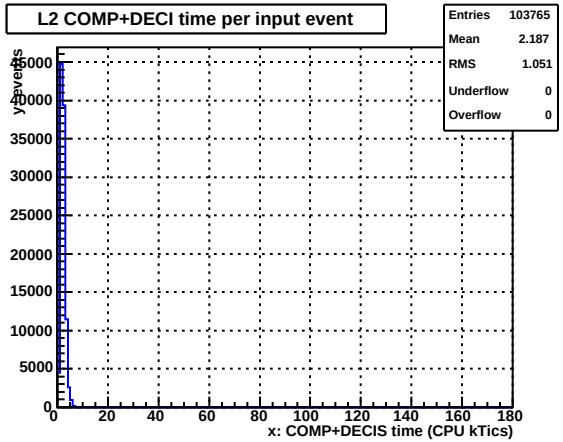

rate of COMPUTE
Entries 344646
Mean 468
RMS 228.1
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

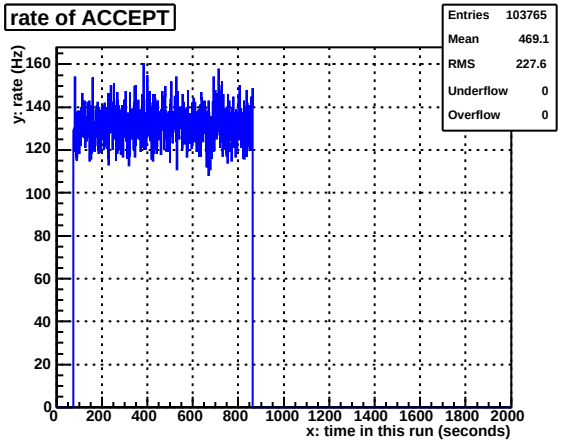

total events 0=anyInput, 10=anyAccept
Entries 896822
Mean 2.273
RMS 3.045
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

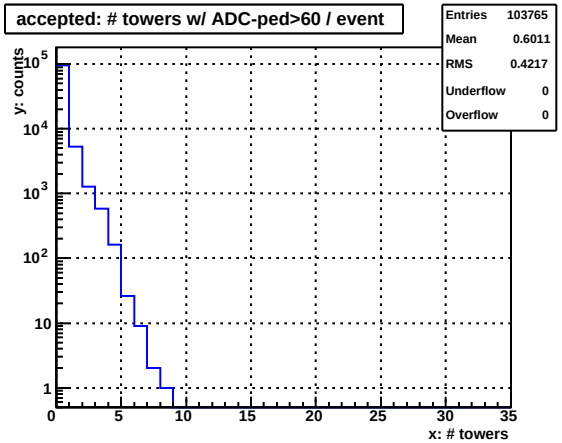

L2 DECISION time per input event
Entries 103765
Mean 1.099
RMS 0.7988
Underflow 0
Overflow 1
y: events
x: DECISION time (CPU kTics)

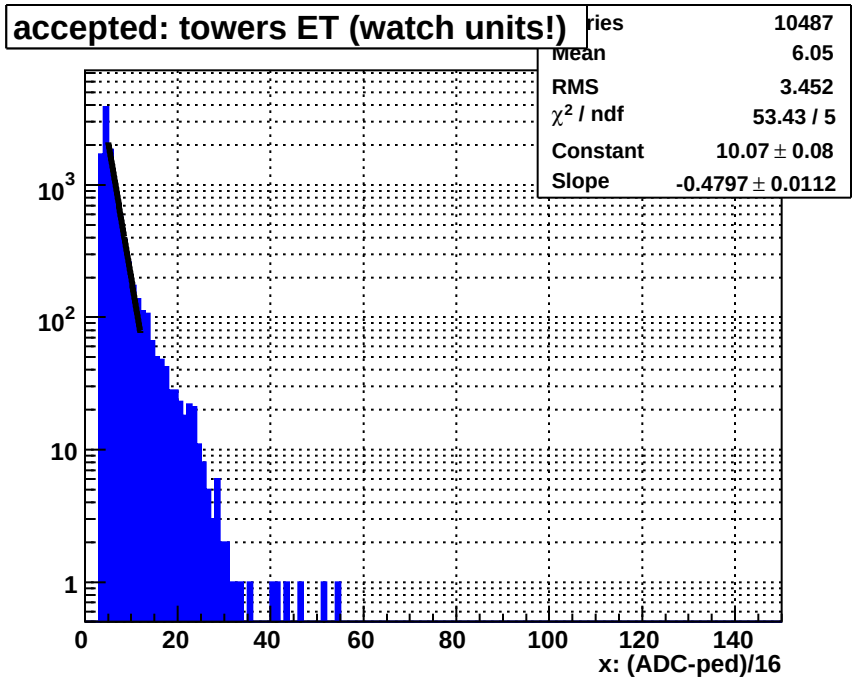

rate of DECISION
Entries 103765
Mean 469.1
RMS 227.6
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

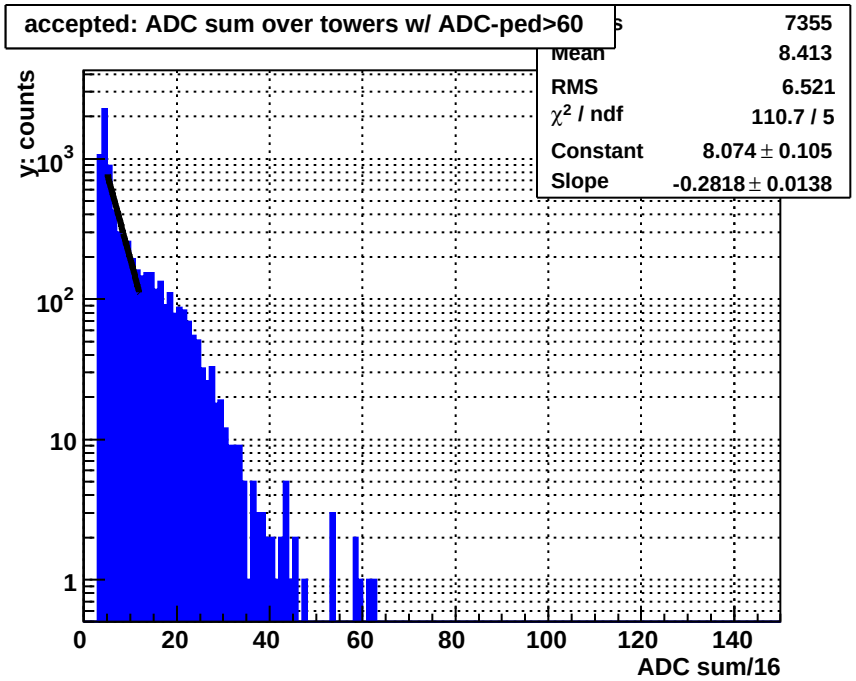

compute: # towers w/ ADC-ped>60 / event
Entries 344646
Mean 0.6371
RMS 0.5051
Underflow 0
Overflow 0
y: counts
x: # towers

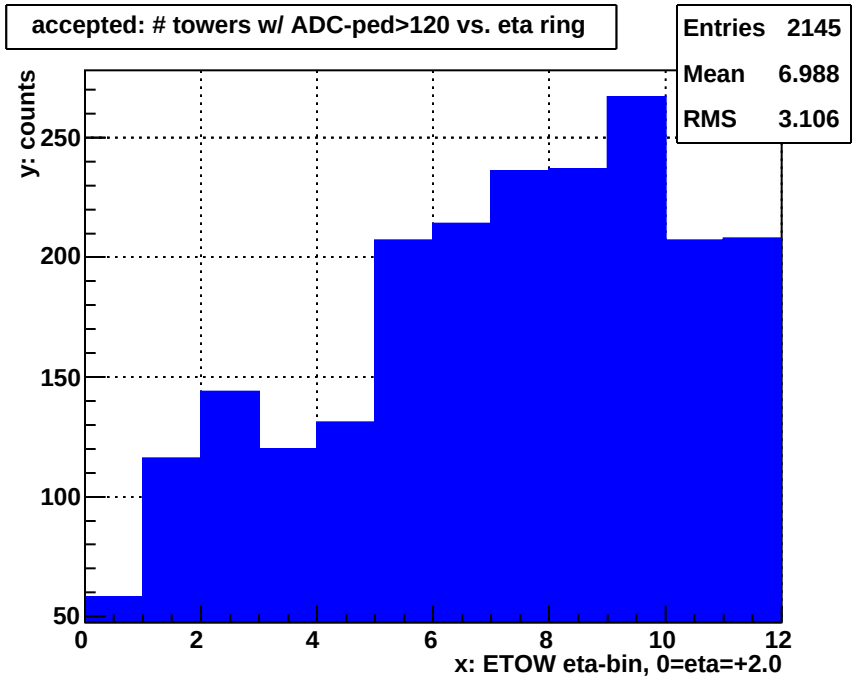

L2 COMP+DECI time per input event
Entries 103765
Mean 2.187
RMS 1.051
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

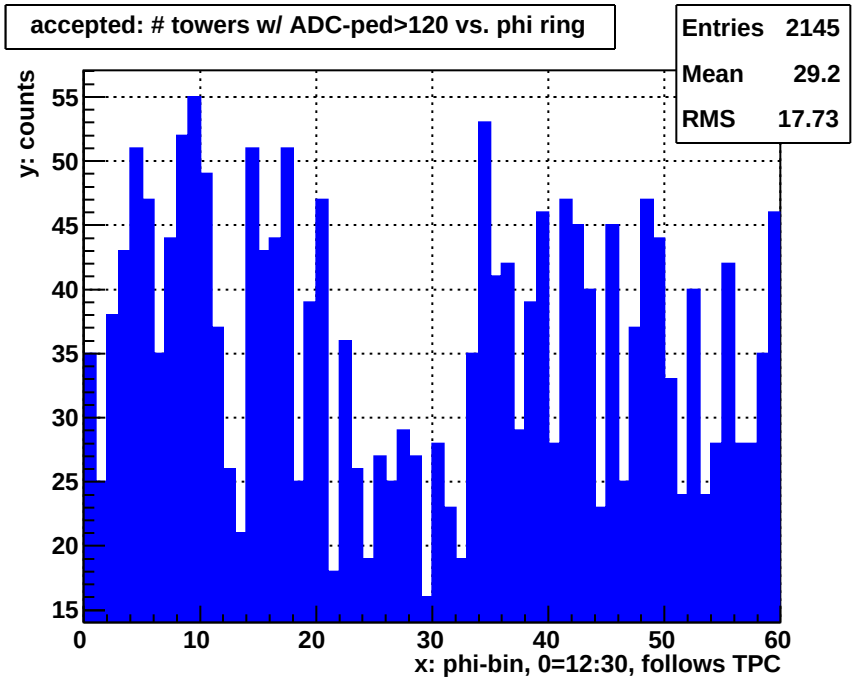

rate of ACCEPT
Entries 103765
Mean 469.1
RMS 227.6
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 103765
Mean 0.6011
RMS 0.4217
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 10487
Mean 6.05
RMS 3.452
χ² / ndf 53.43 / 5
Constant 10.07 ± 0.08
Slope -0.4797 ± 0.0112
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 7355
Mean 8.413
RMS 6.521
χ² / ndf 110.7 / 5
Constant 8.074 ± 0.105
Slope -0.2818 ± 0.0138
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 2145
Mean 6.988
RMS 3.106
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 2145
Mean 29.2
RMS 17.73
y: counts
x: phi-bin, 0=12:30, follows TPC