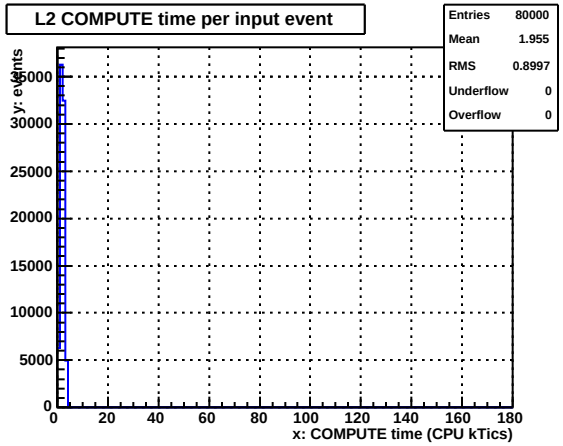
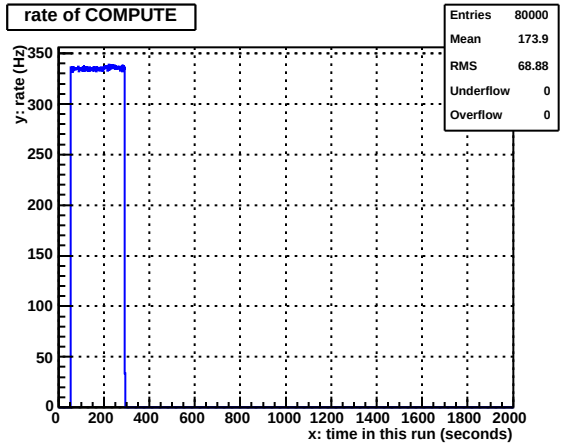
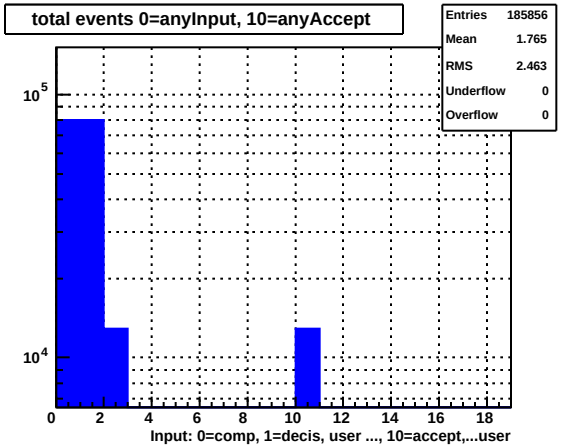
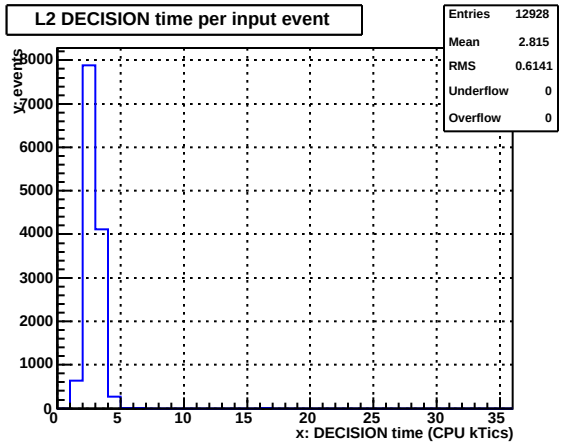
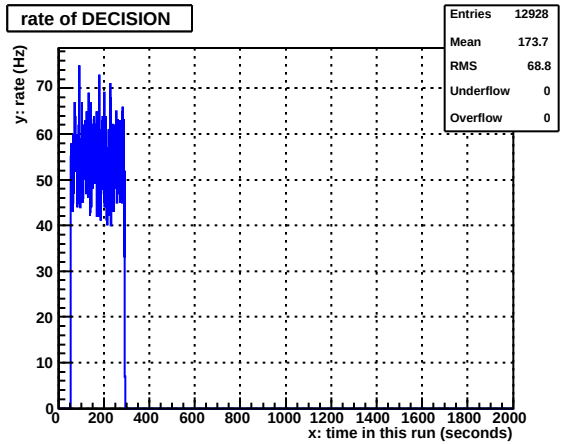
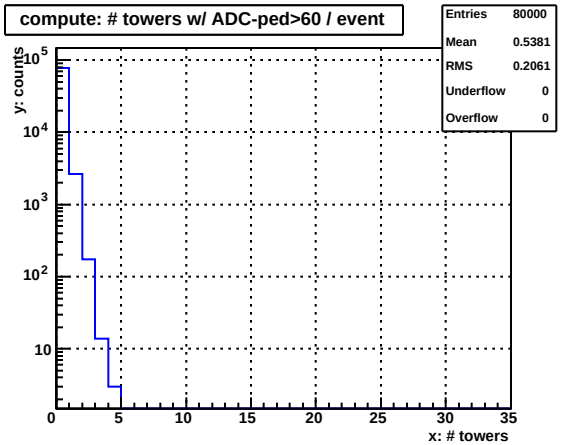

L2 COMPUTE time per input event
Entries 80000
Mean 1.955
RMS 0.8997
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

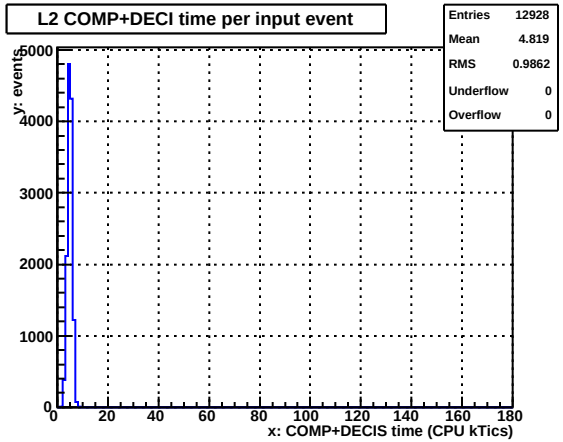

rate of COMPUTE
Entries 80000
Mean 173.9
RMS 68.88
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

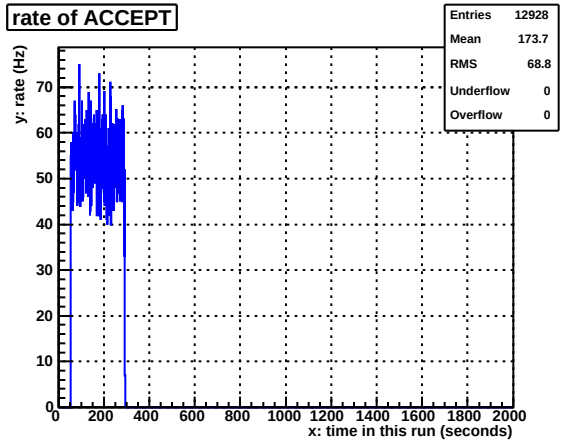

total events 0=anyInput, 10=anyAccept
Entries 185856
Mean 1.765
RMS 2.463
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

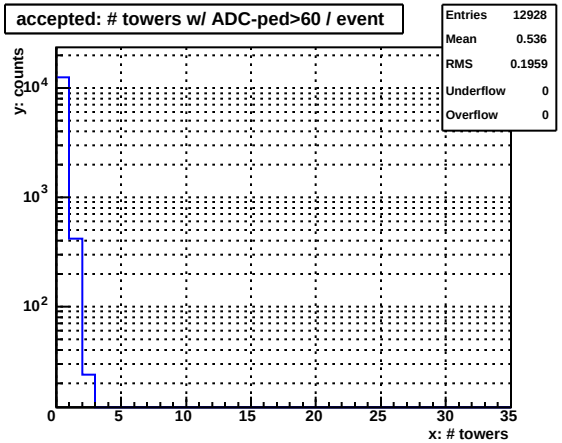

L2 DECISION time per input event
Entries 12928
Mean 2.815
RMS 0.6141
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

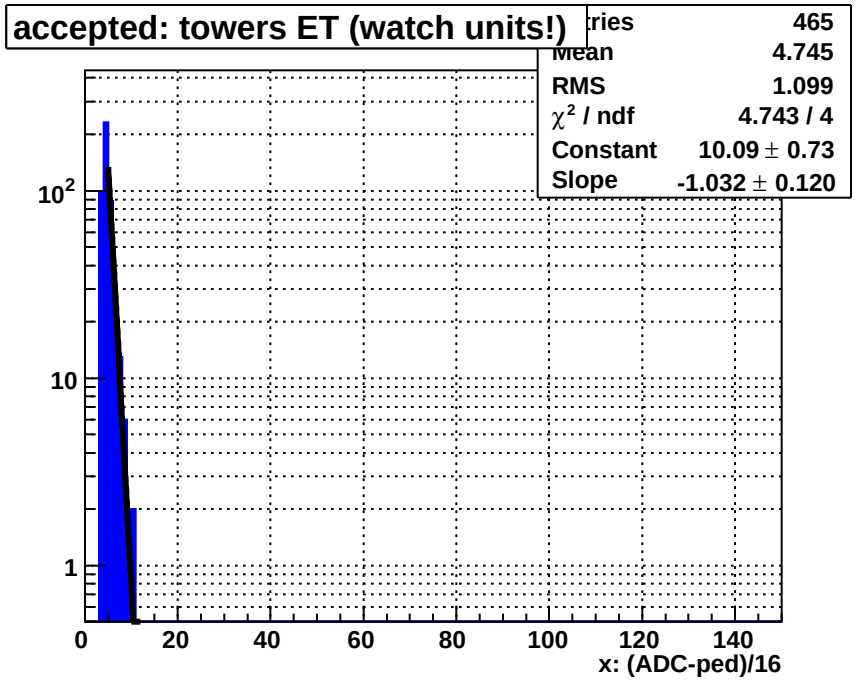

rate of DECISION
Entries 12928
Mean 173.7
RMS 68.8
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

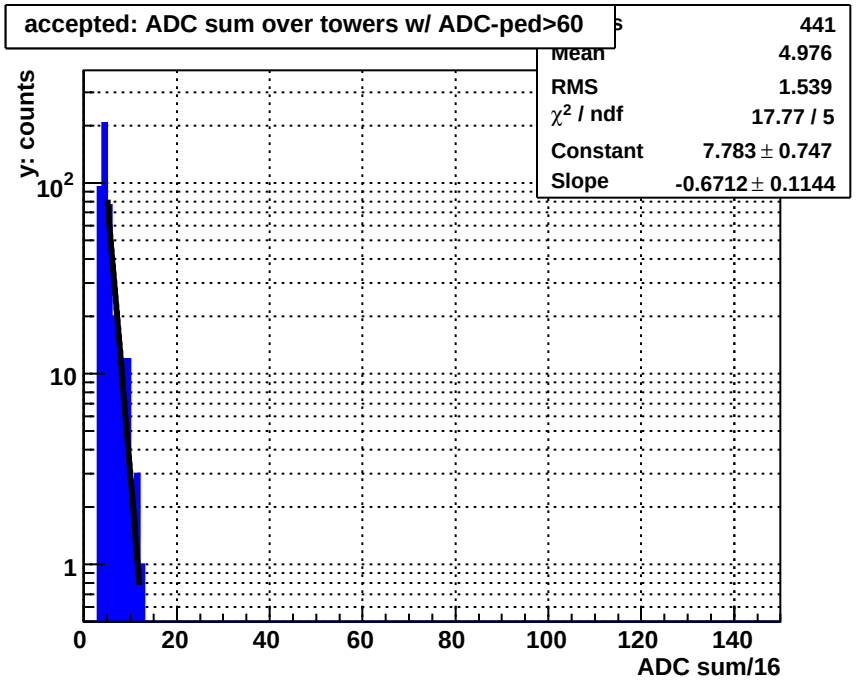

compute: # towers w/ ADC-ped>60 / event
Entries 80000
Mean 0.5381
RMS 0.2061
Underflow 0
Overflow 0
y: counts
x: # towers

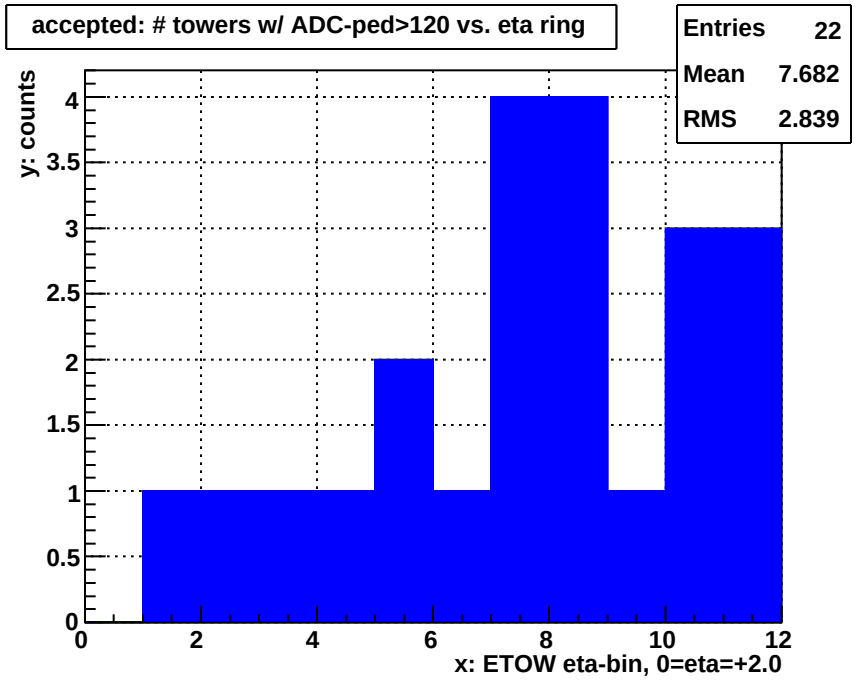

L2 COMP+DECI time per input event
Entries 12928
Mean 4.819
RMS 0.9862
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

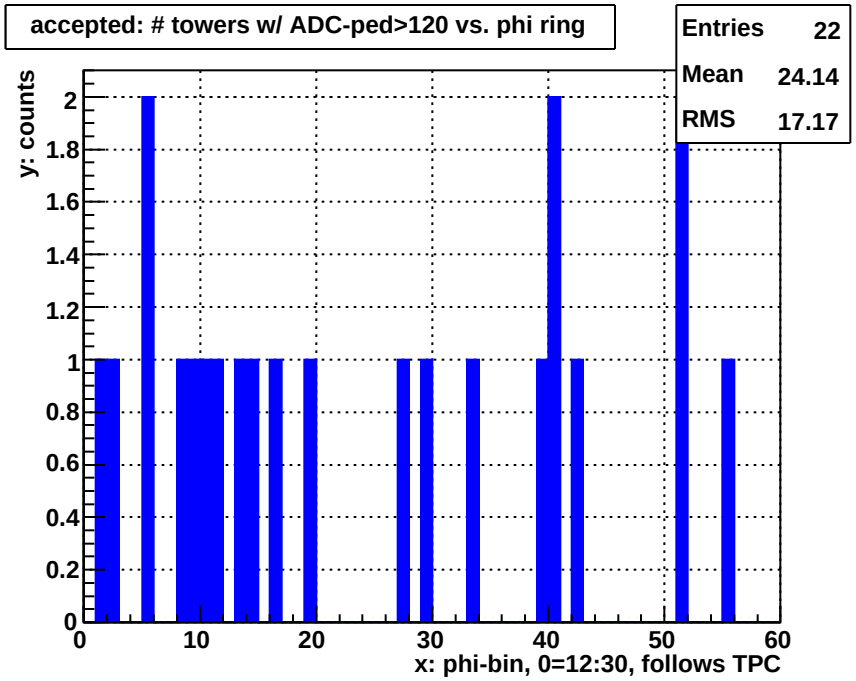

rate of ACCEPT
Entries 12928
Mean 173.7
RMS 68.8
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 12928
Mean 0.536
RMS 0.1959
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
Entries 465
Mean 4.745
RMS 1.099
χ² / ndf 4.743 / 4
Constant 10.09 ± 0.73
Slope -1.032 ± 0.120
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
441
Mean 4.976
RMS 1.539
χ² / ndf 17.77 / 5
Constant 7.783 ± 0.747
Slope -0.6712 ± 0.1144
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 22
Mean 7.682
RMS 2.839
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 22
Mean 24.14
RMS 17.17
y: counts
x: phi-bin, 0=12:30, follows TPC