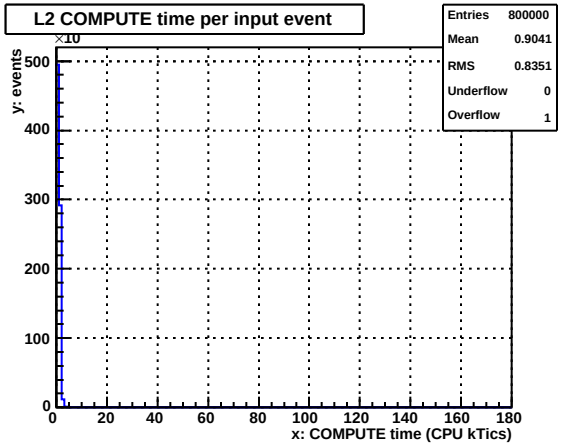
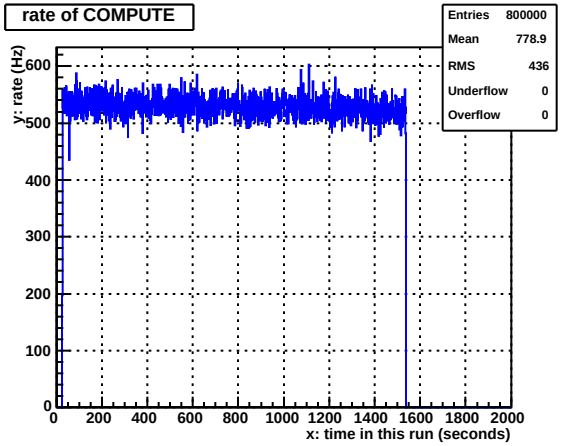
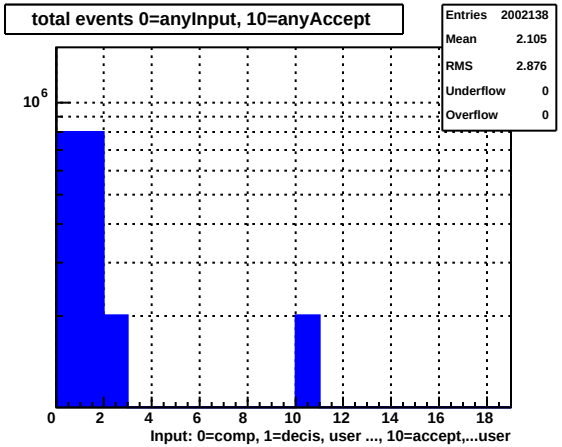
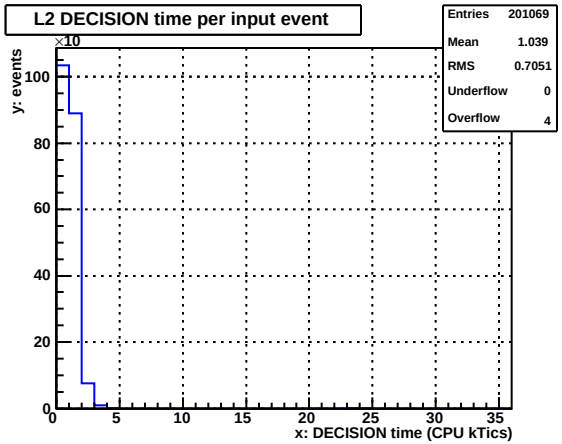
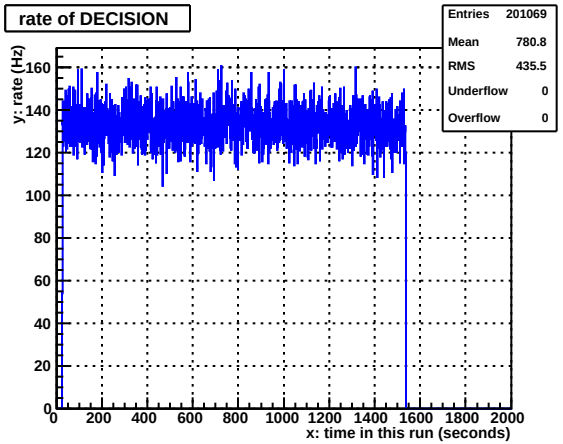
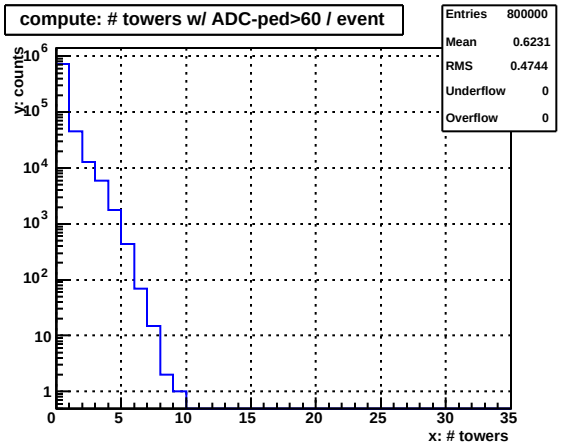

L2 COMPUTE time per input event
Entries 800000
Mean 0.9041
RMS 0.8351
Underflow 0
Overflow 1
y: events
x: COMPUTE time (CPU kTics)

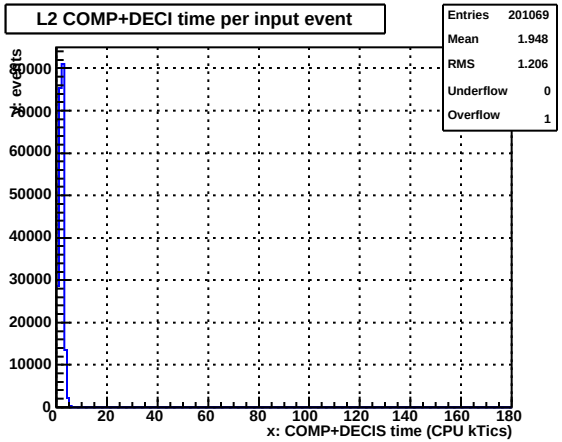

rate of COMPUTE
Entries 800000
Mean 778.9
RMS 436
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

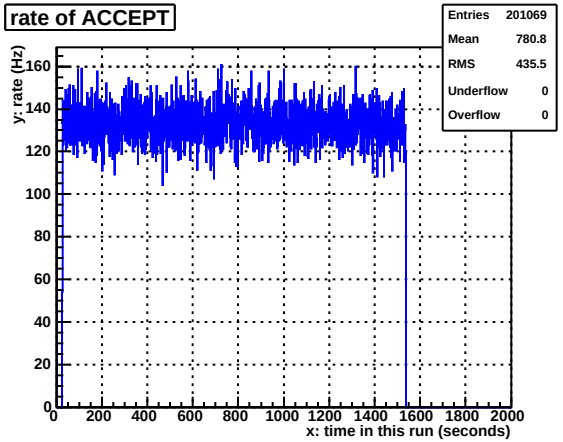

total events 0=anyInput, 10=anyAccept
Entries 2002138
Mean 2.105
RMS 2.876
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

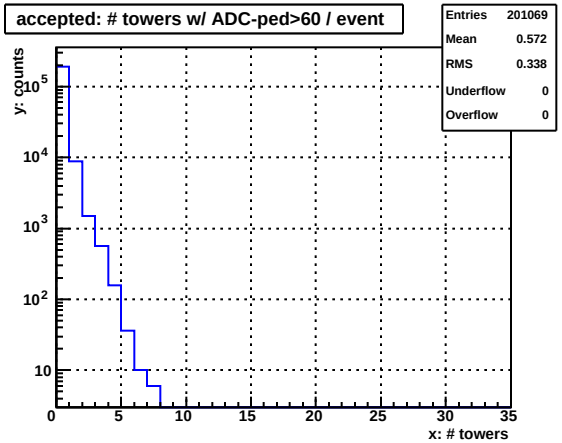

L2 DECISION time per input event
Entries 201069
Mean 1.039
RMS 0.7051
Underflow 0
Overflow 4
y: events
x: DECISION time (CPU kTics)

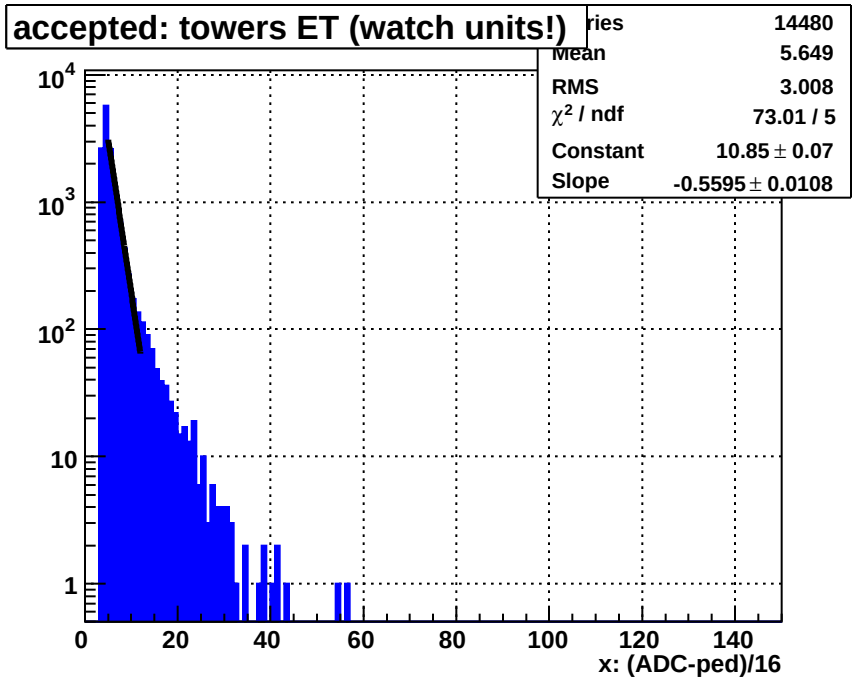

rate of DECISION
Entries 201069
Mean 780.8
RMS 435.5
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

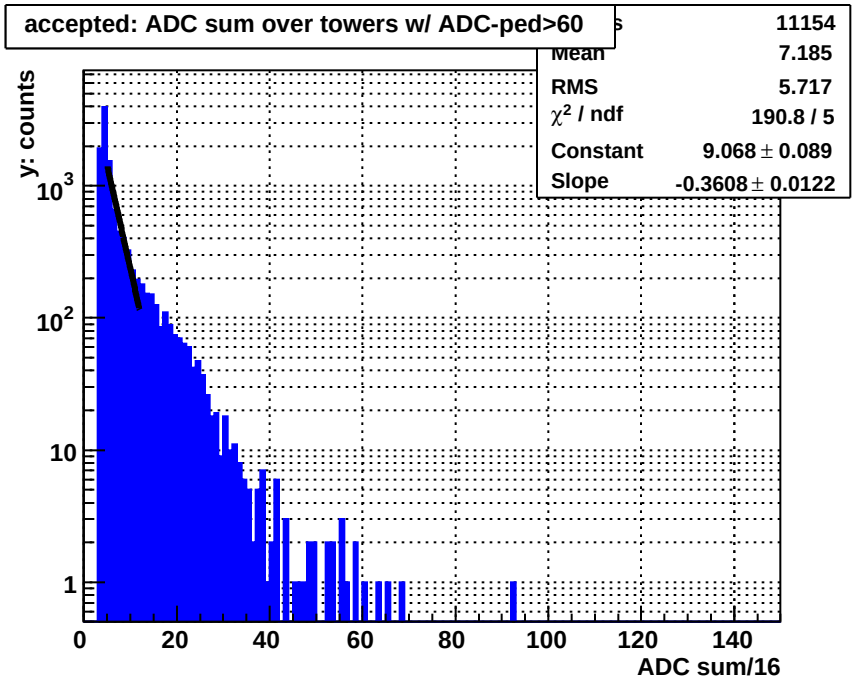

compute: # towers w/ ADC-ped>60 / event
Entries 800000
Mean 0.6231
RMS 0.4744
Underflow 0
Overflow 0
y: counts
x: # towers

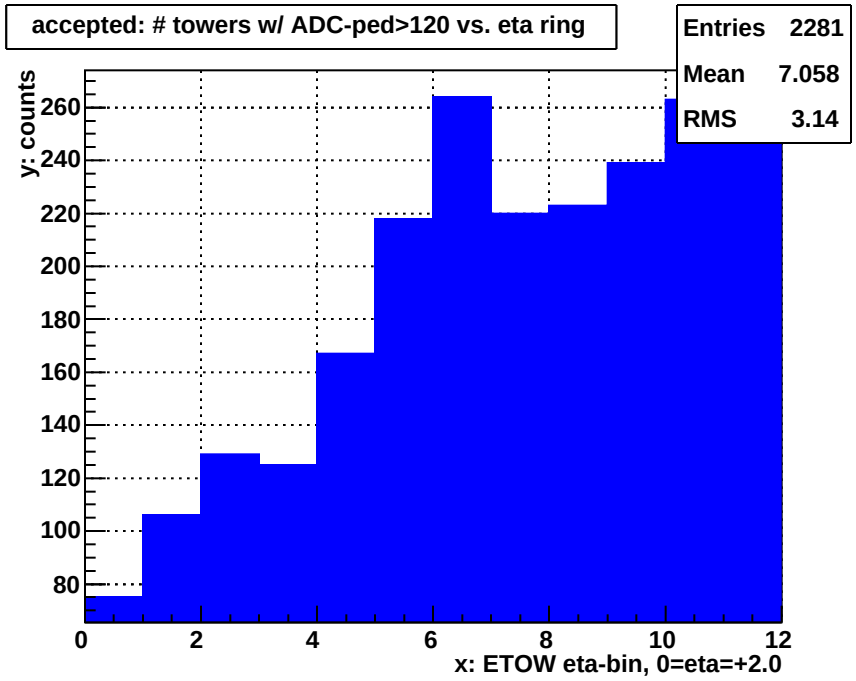

L2 COMP+DECI time per input event
Entries 201069
Mean 1.948
RMS 1.206
Underflow 0
Overflow 1
y: events
x: COMP+DECIS time (CPU kTics)

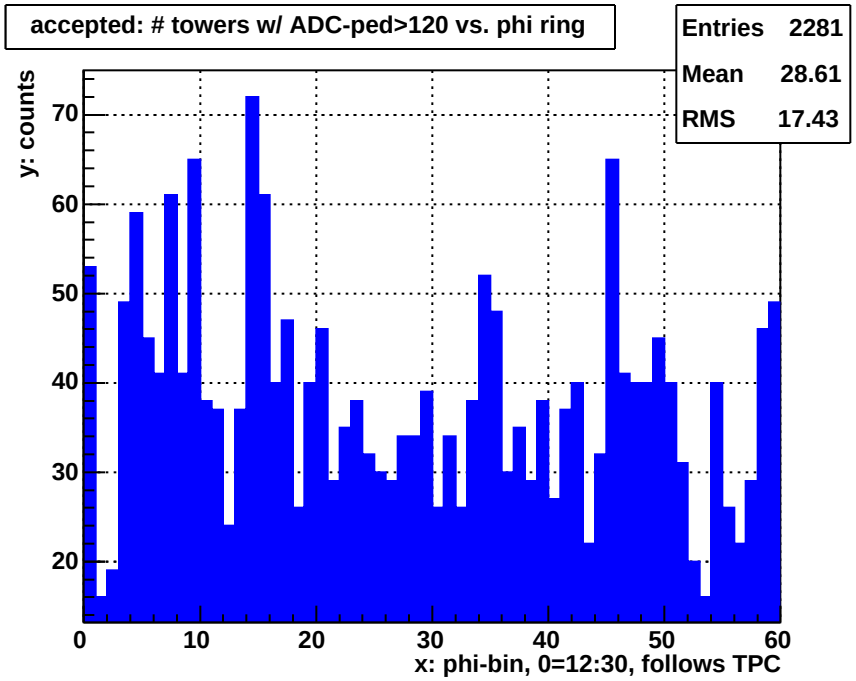

rate of ACCEPT
Entries 201069
Mean 780.8
RMS 435.5
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 201069
Mean 0.572
RMS 0.338
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 14480
Mean 5.649
RMS 3.008
χ² / ndf 73.01 / 5
Constant 10.85 ± 0.07
Slope -0.5595 ± 0.0108
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 11154
Mean 7.185
RMS 5.717
χ² / ndf 190.8 / 5
Constant 9.068 ± 0.089
Slope -0.3608 ± 0.0122
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 2281
Mean 7.058
RMS 3.14
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 2281
Mean 28.61
RMS 17.43
y: counts
x: phi-bin, 0=12:30, follows TPC